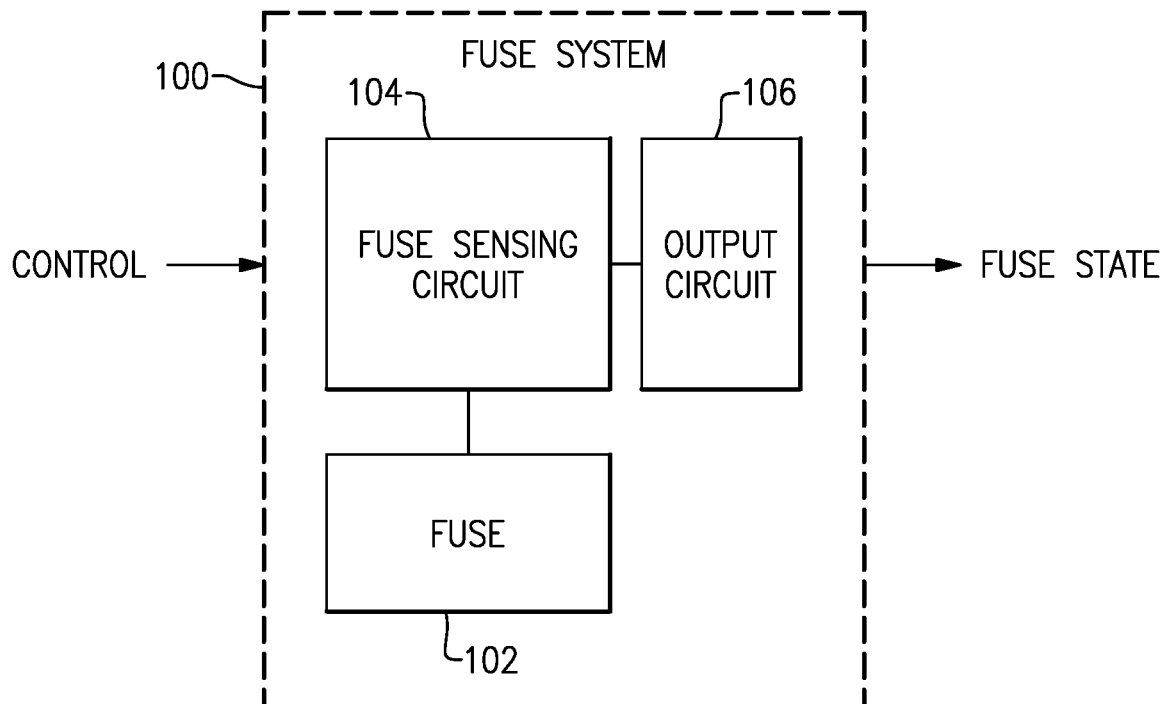
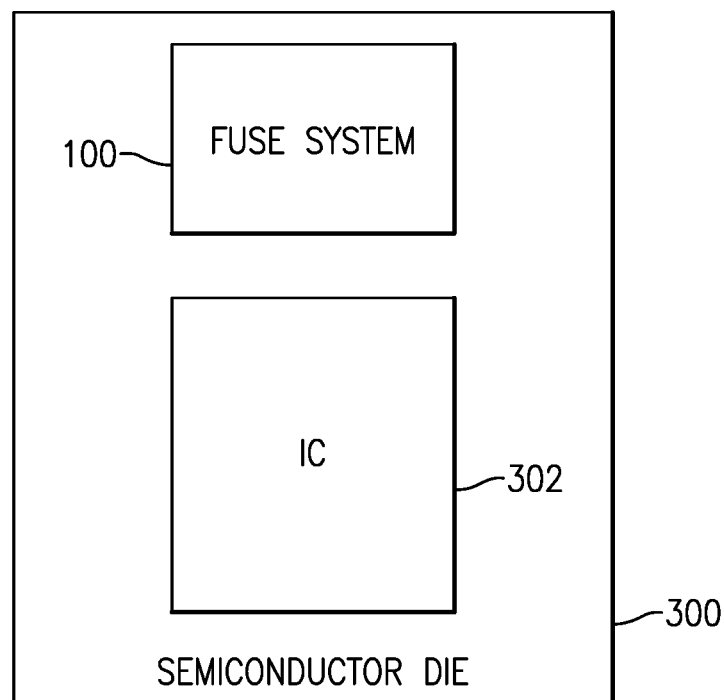
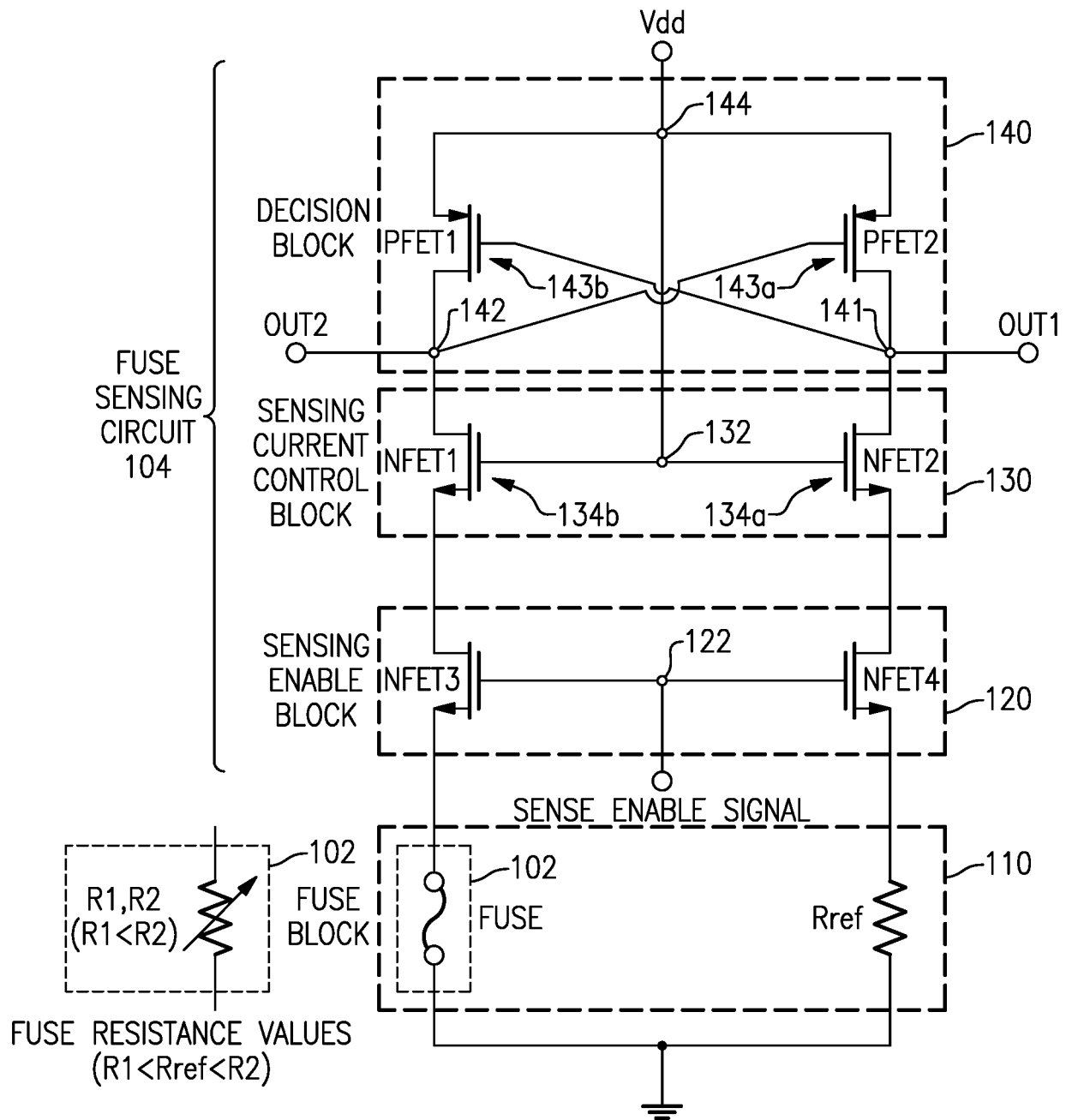


1/21

**FIG.1****FIG.2**

2/21

**FIG.3**

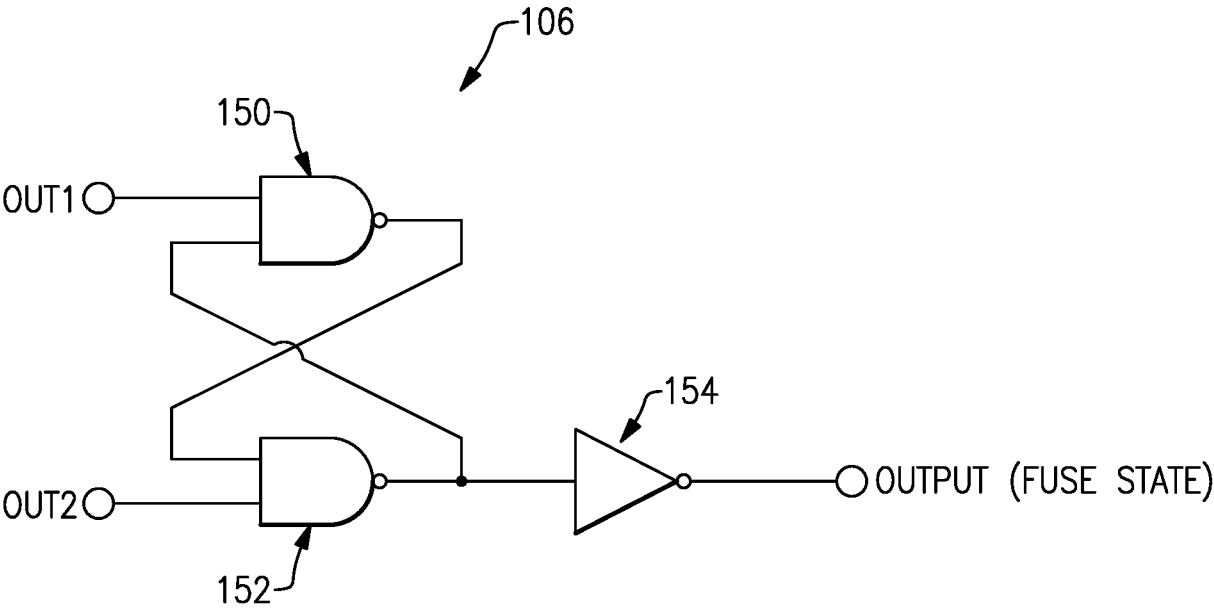


FIG.4

4/21

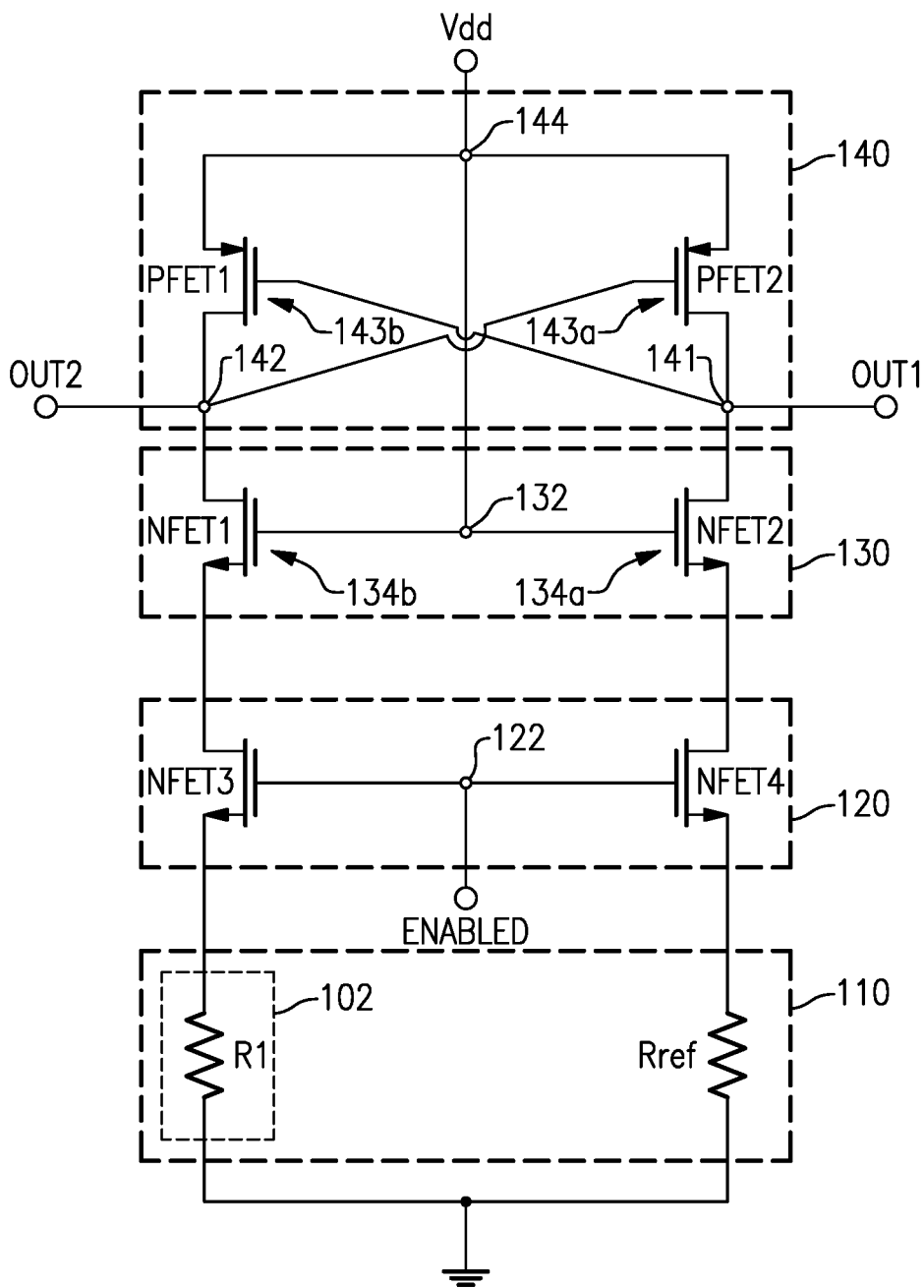


FIG.5A

OUT1-OUT2	OUTPUT (FUSE STATE)
POSITIVE	LOW

FIG.5B

5/21

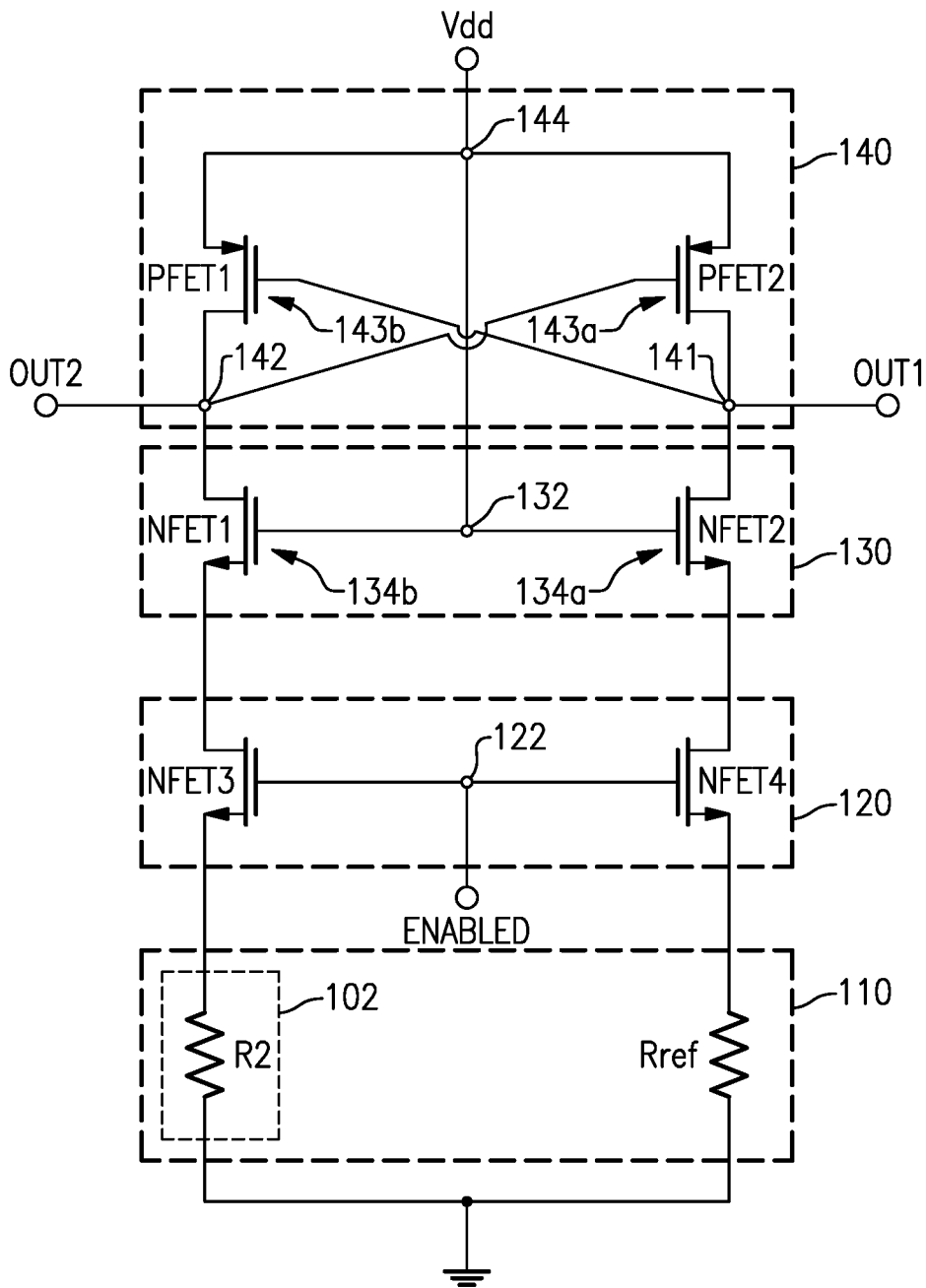


FIG.6A

OUT1-OUT2	OUTPUT (FUSE STATE)
NEGATIVE	HIGH

FIG.6B

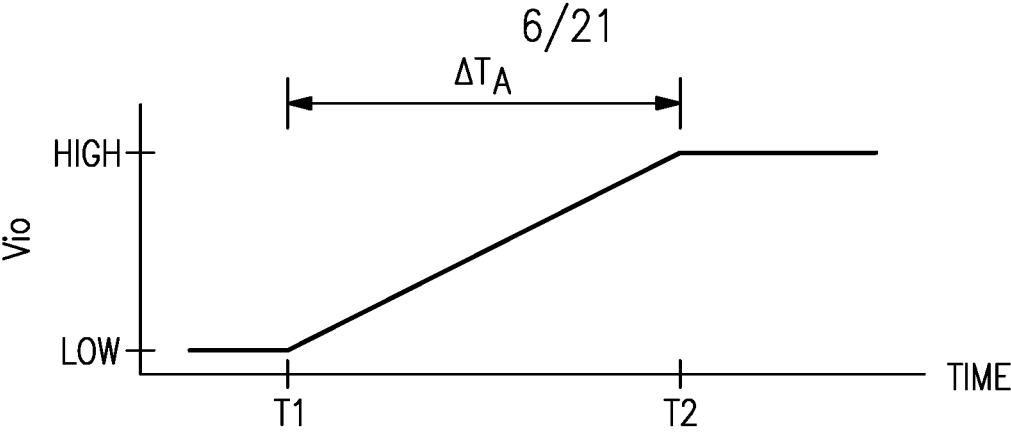


FIG.7A

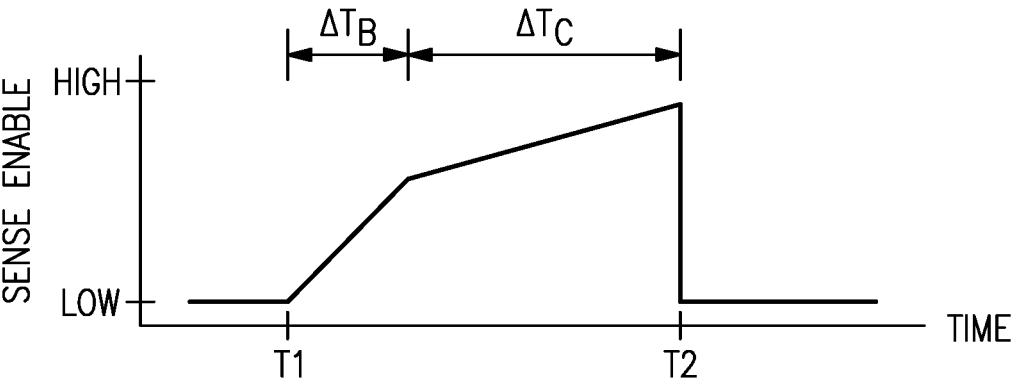


FIG.7B

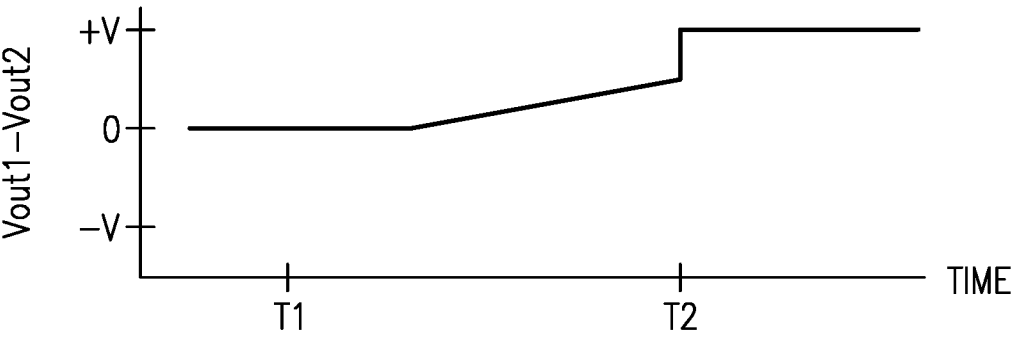


FIG.7C

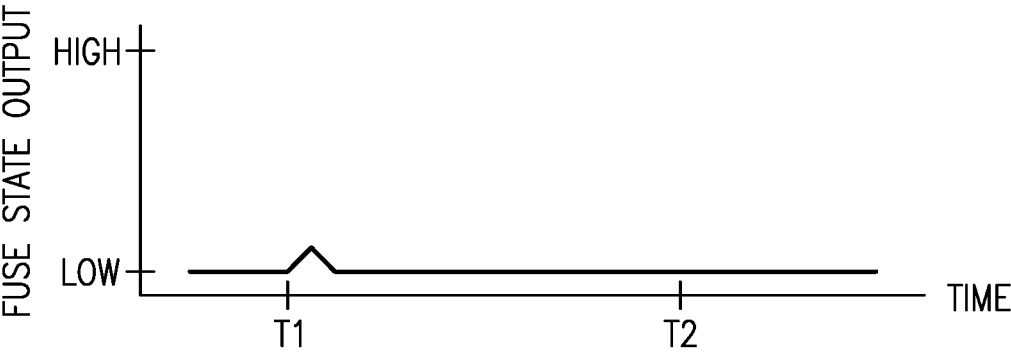
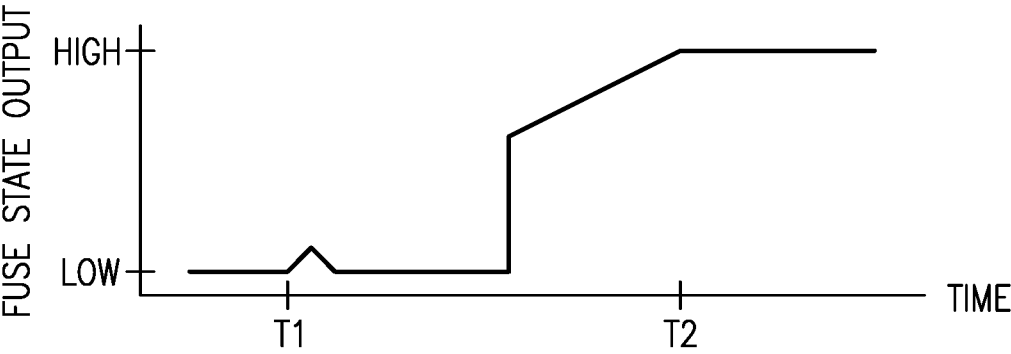
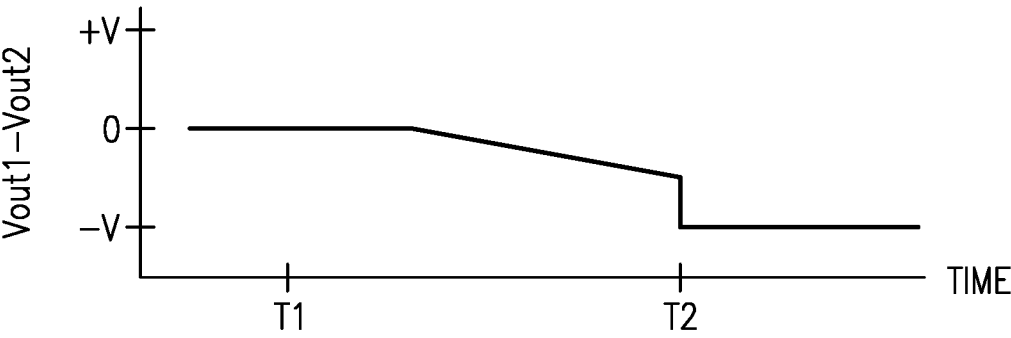
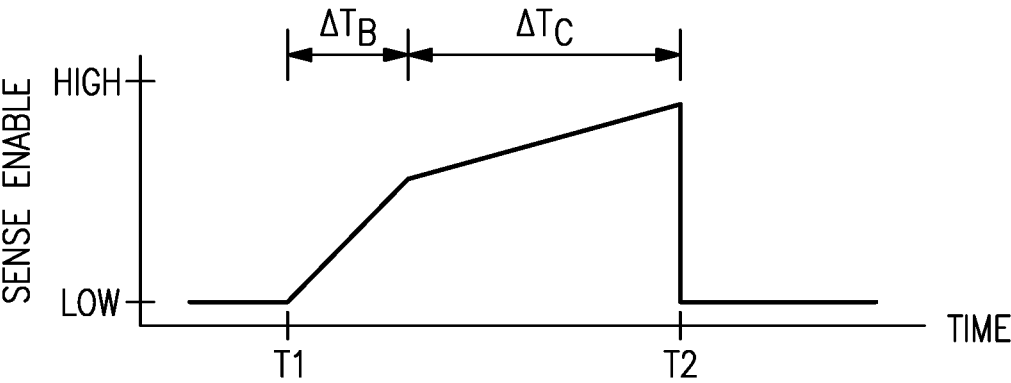
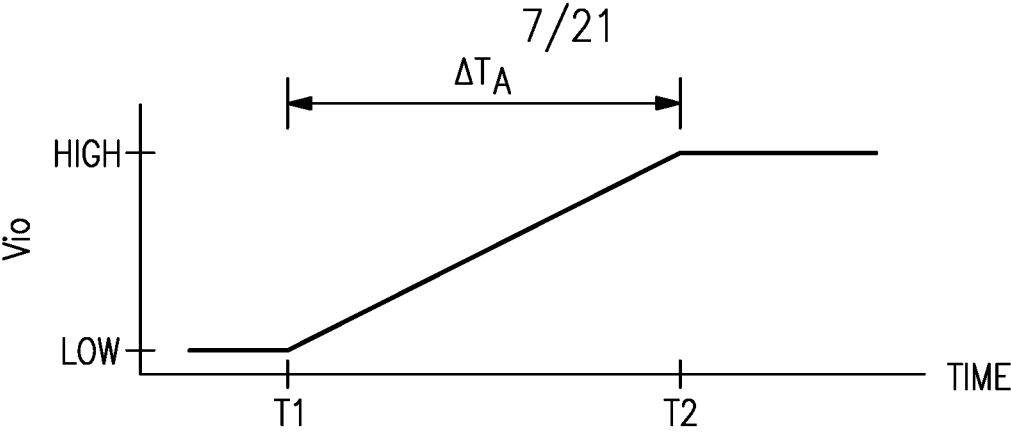


FIG.7D



8/21

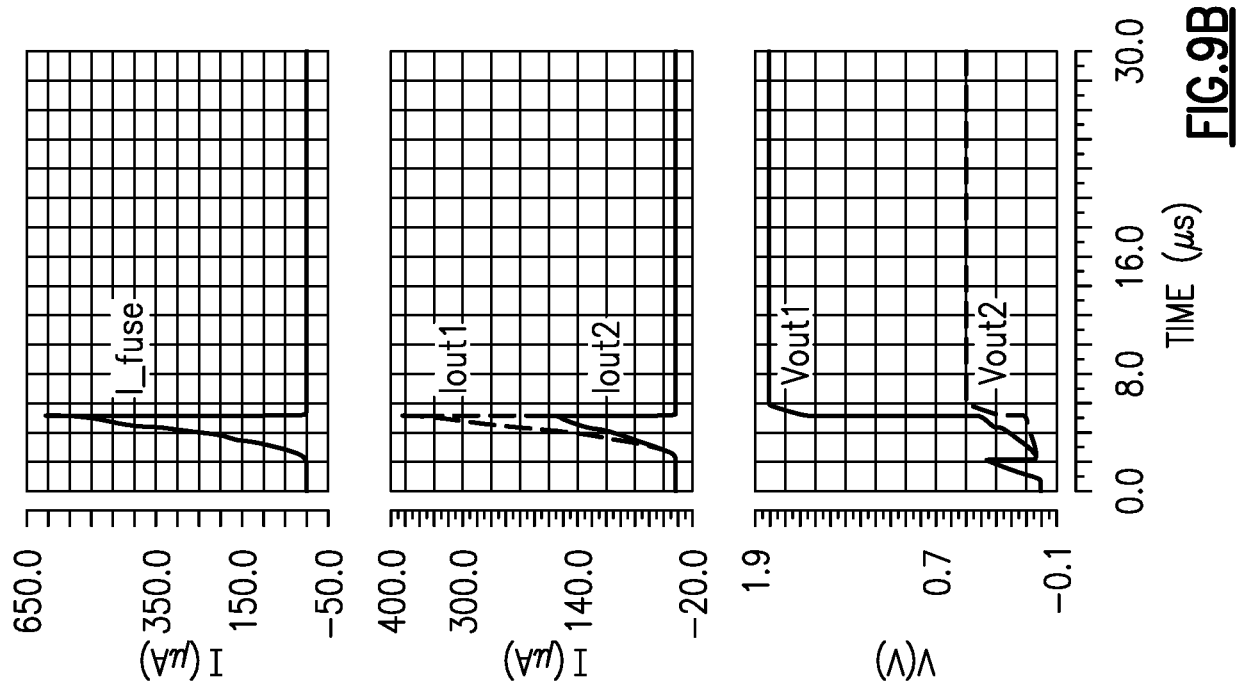


FIG.9A

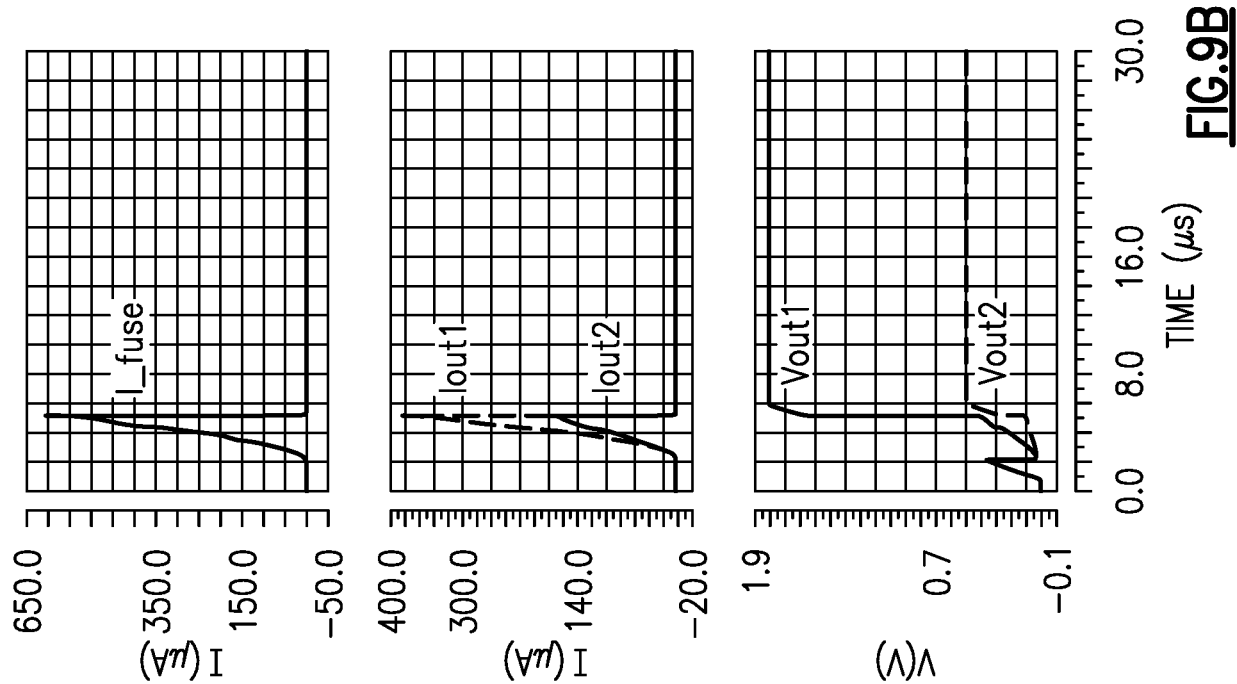


FIG.9B

9/21

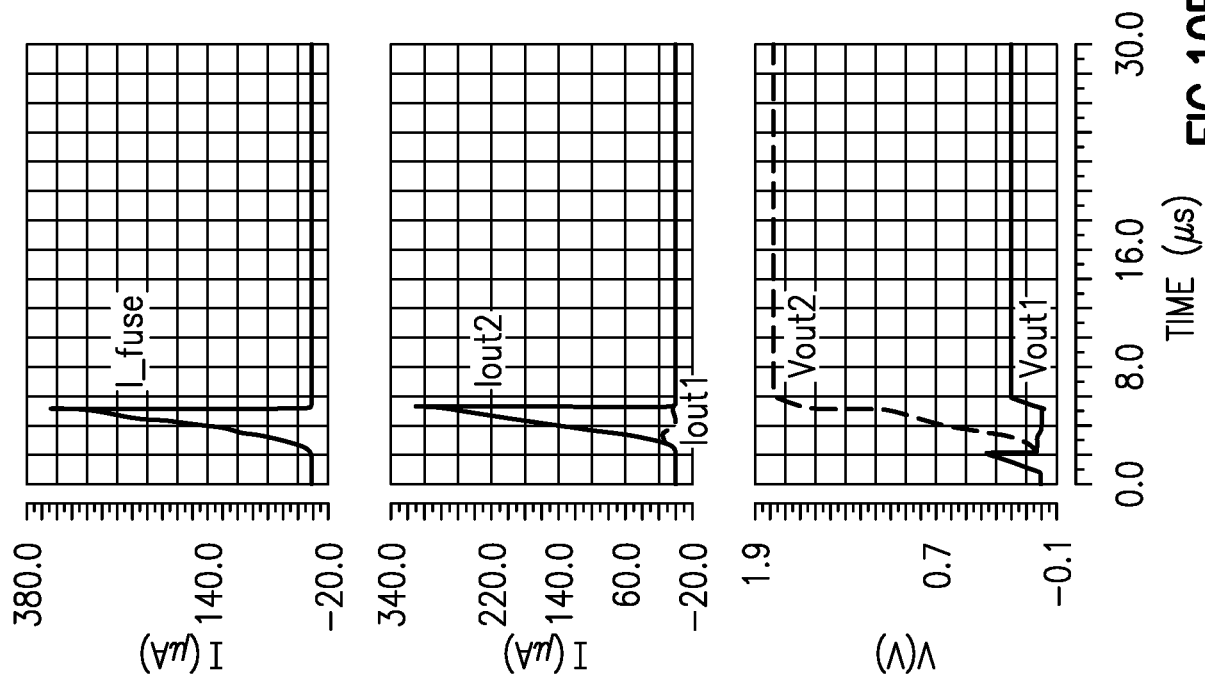


FIG. 10A

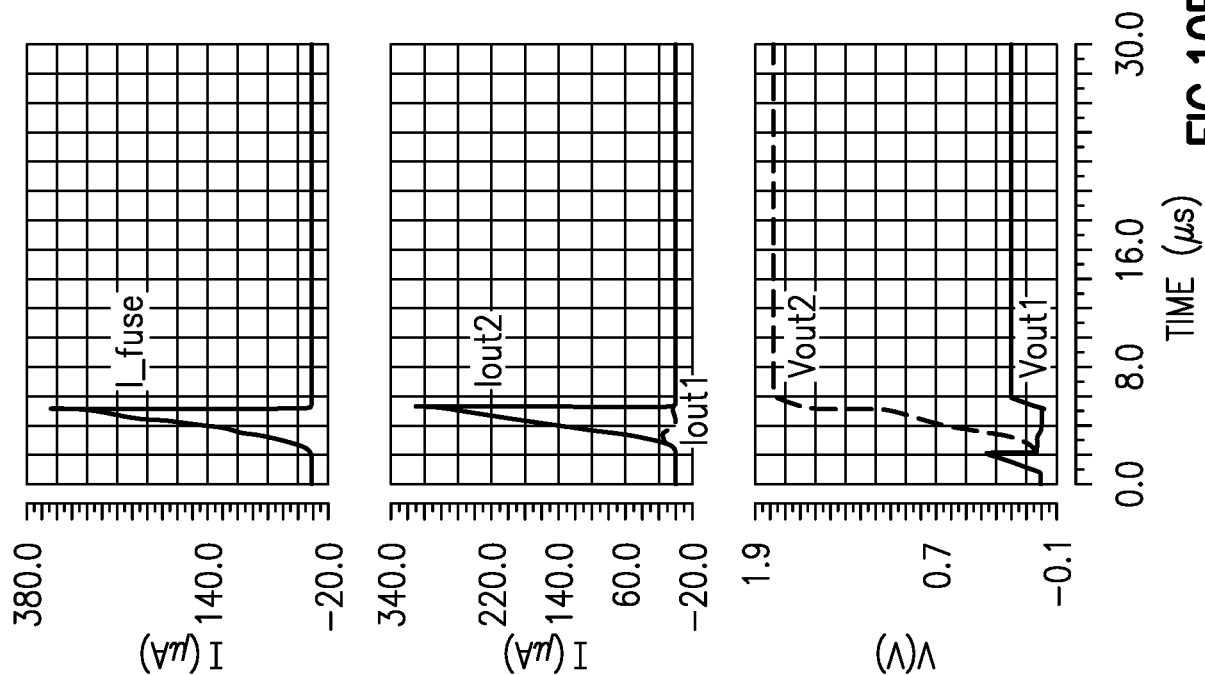


FIG. 10B

10/21

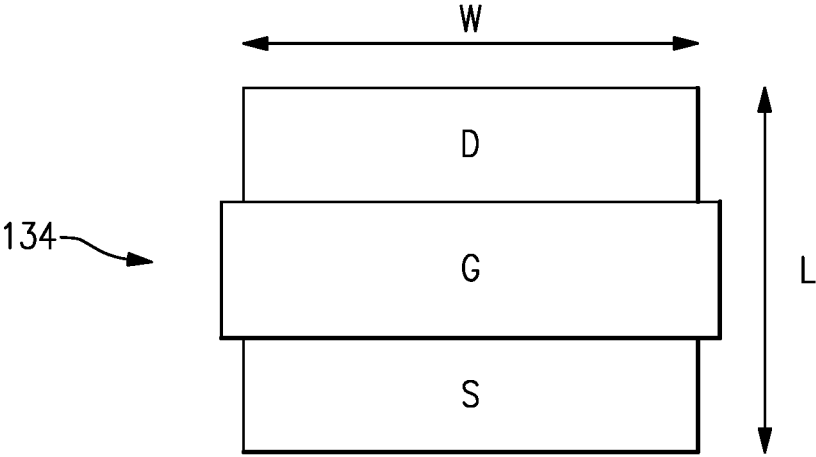


FIG.11

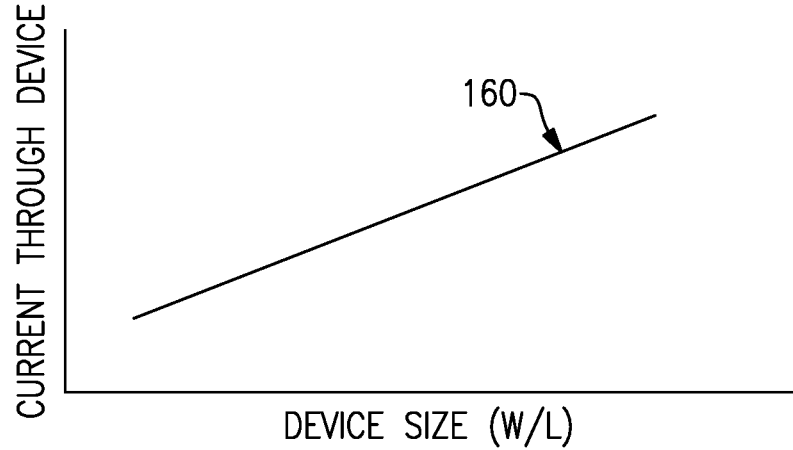


FIG.12

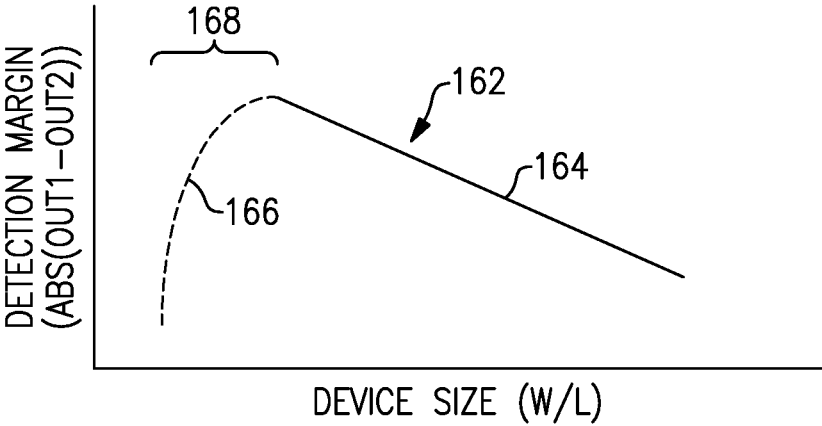
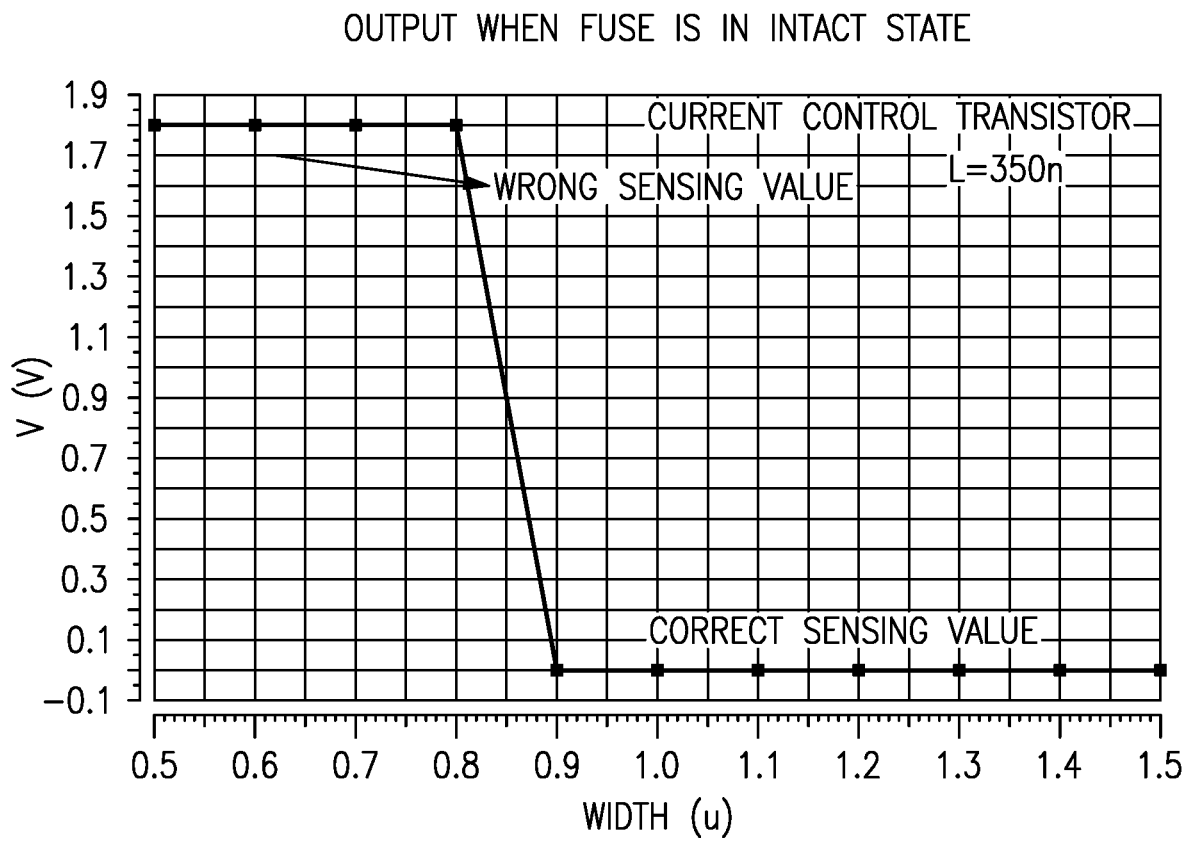
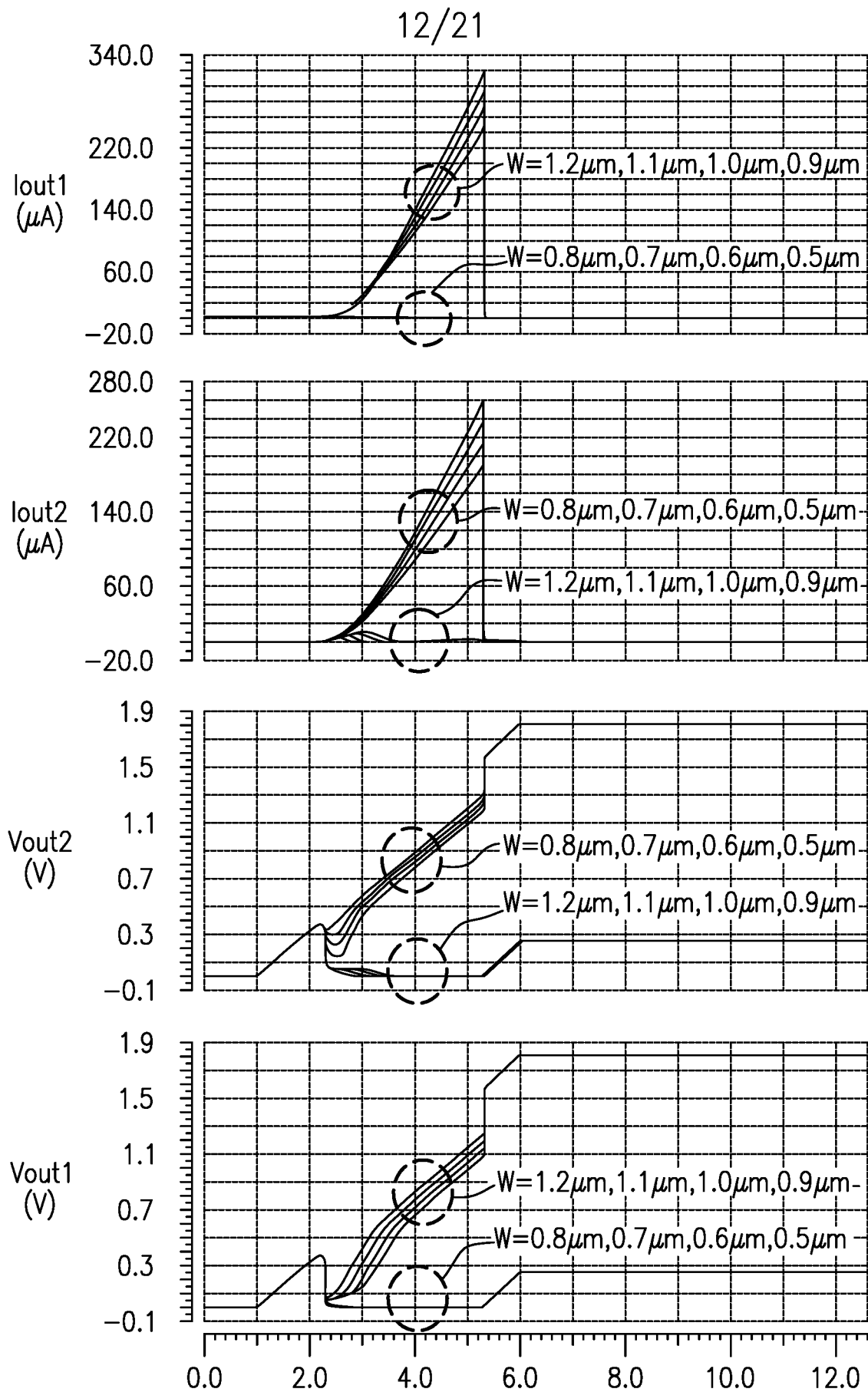


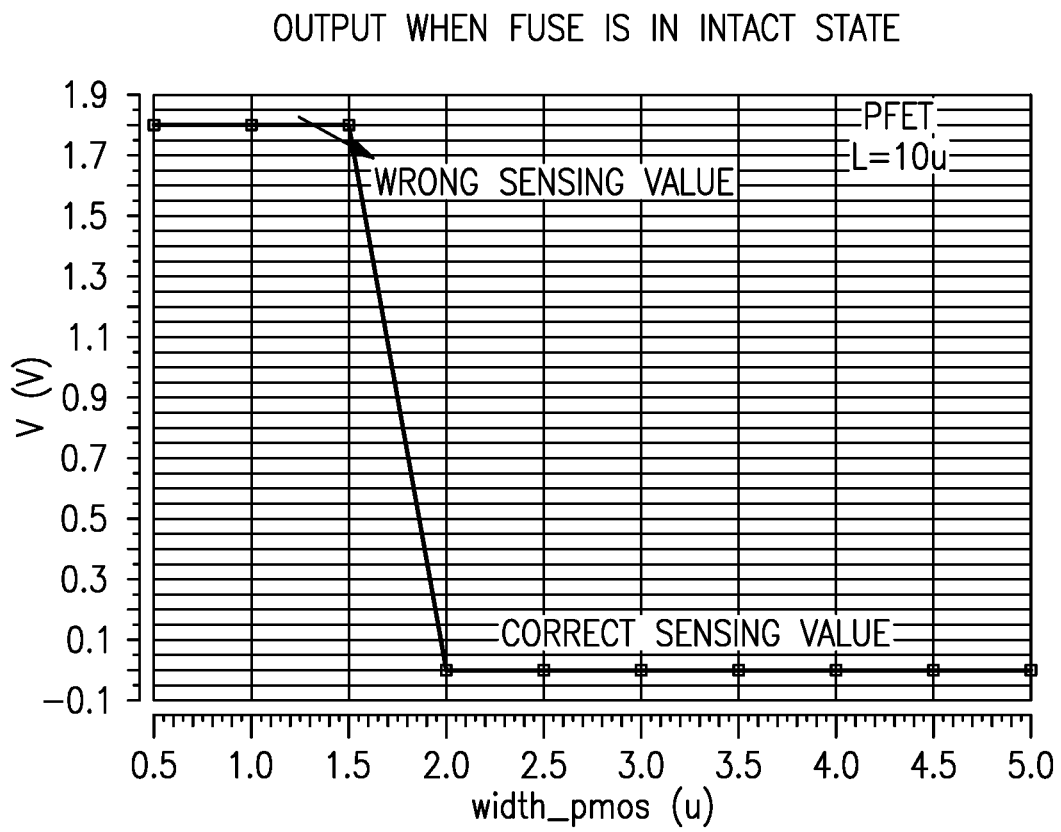
FIG.13

11/21

**FIG.14**



13/21

**FIG.16**

14/21

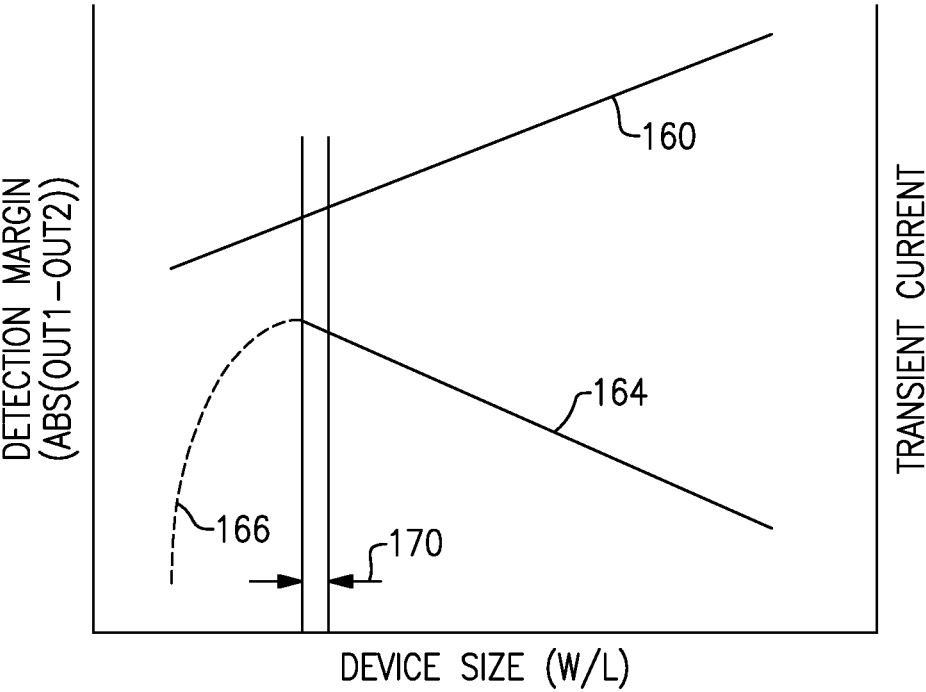


FIG.17

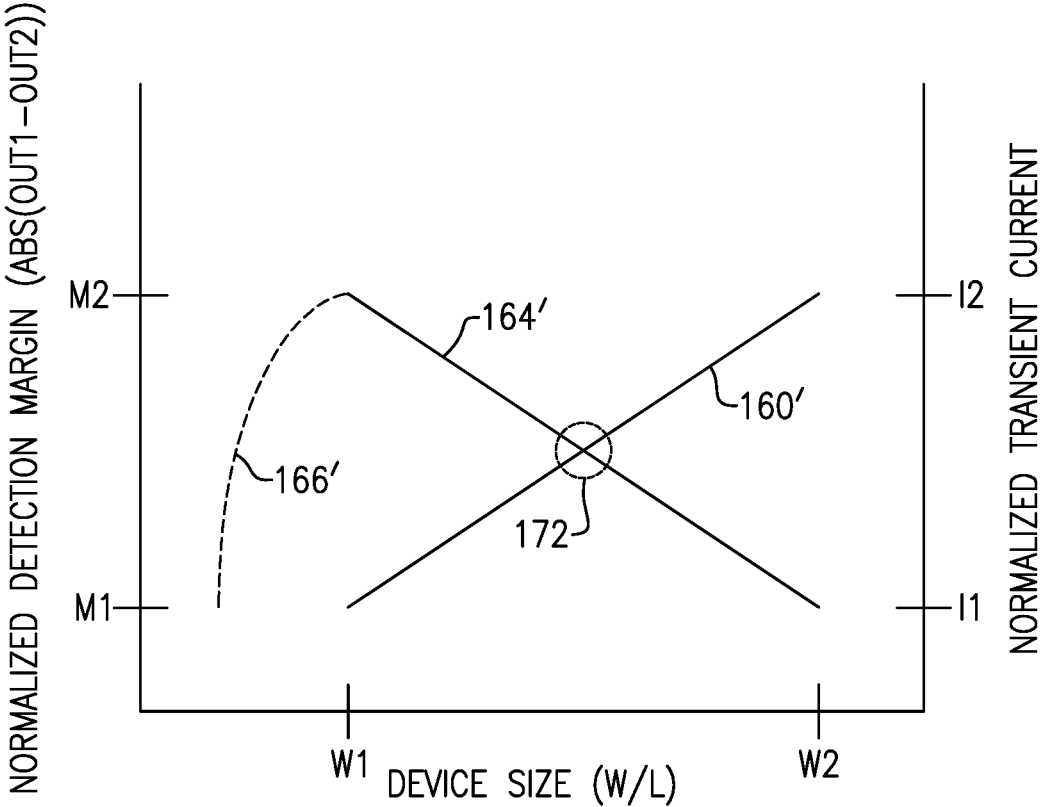
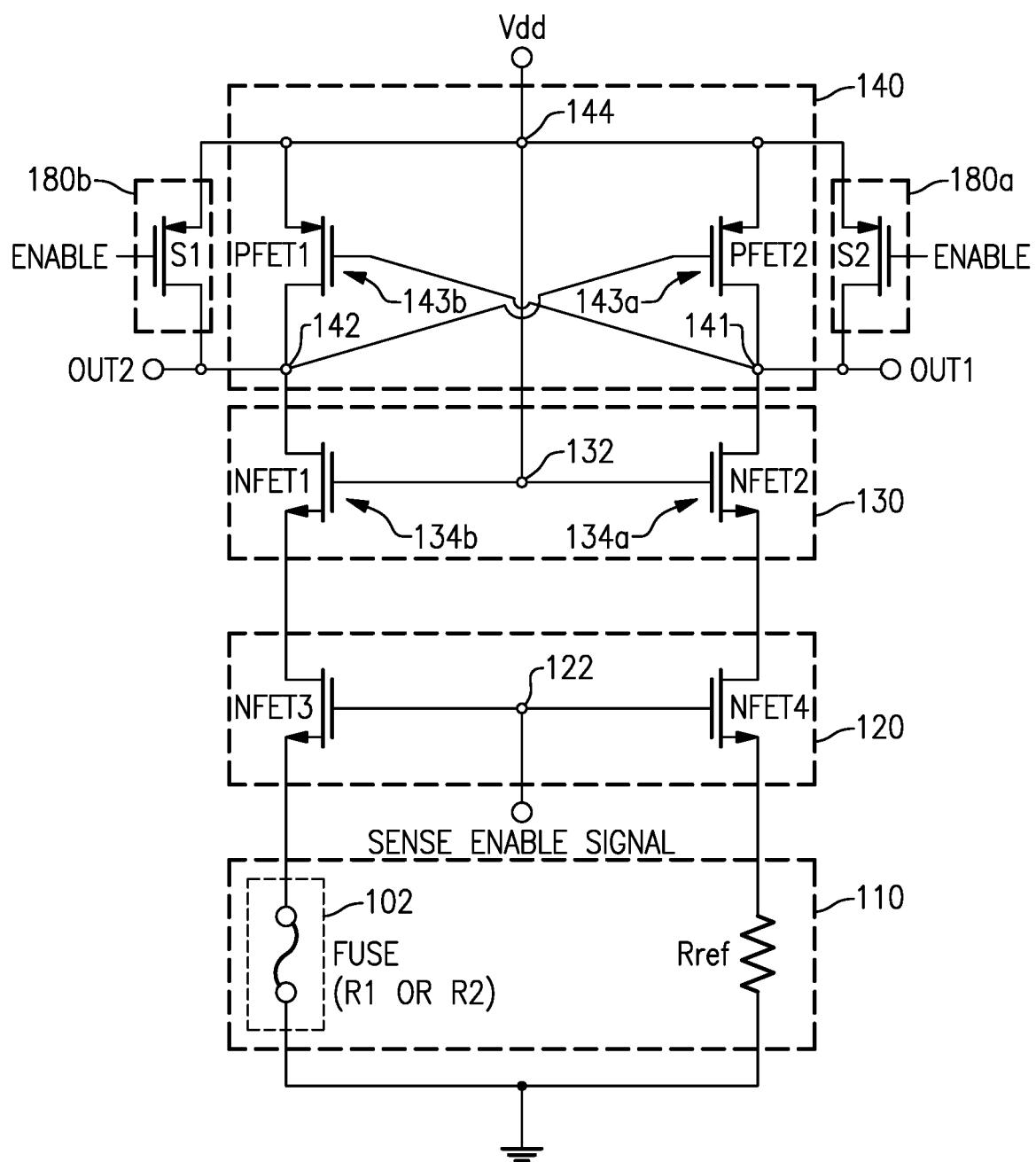
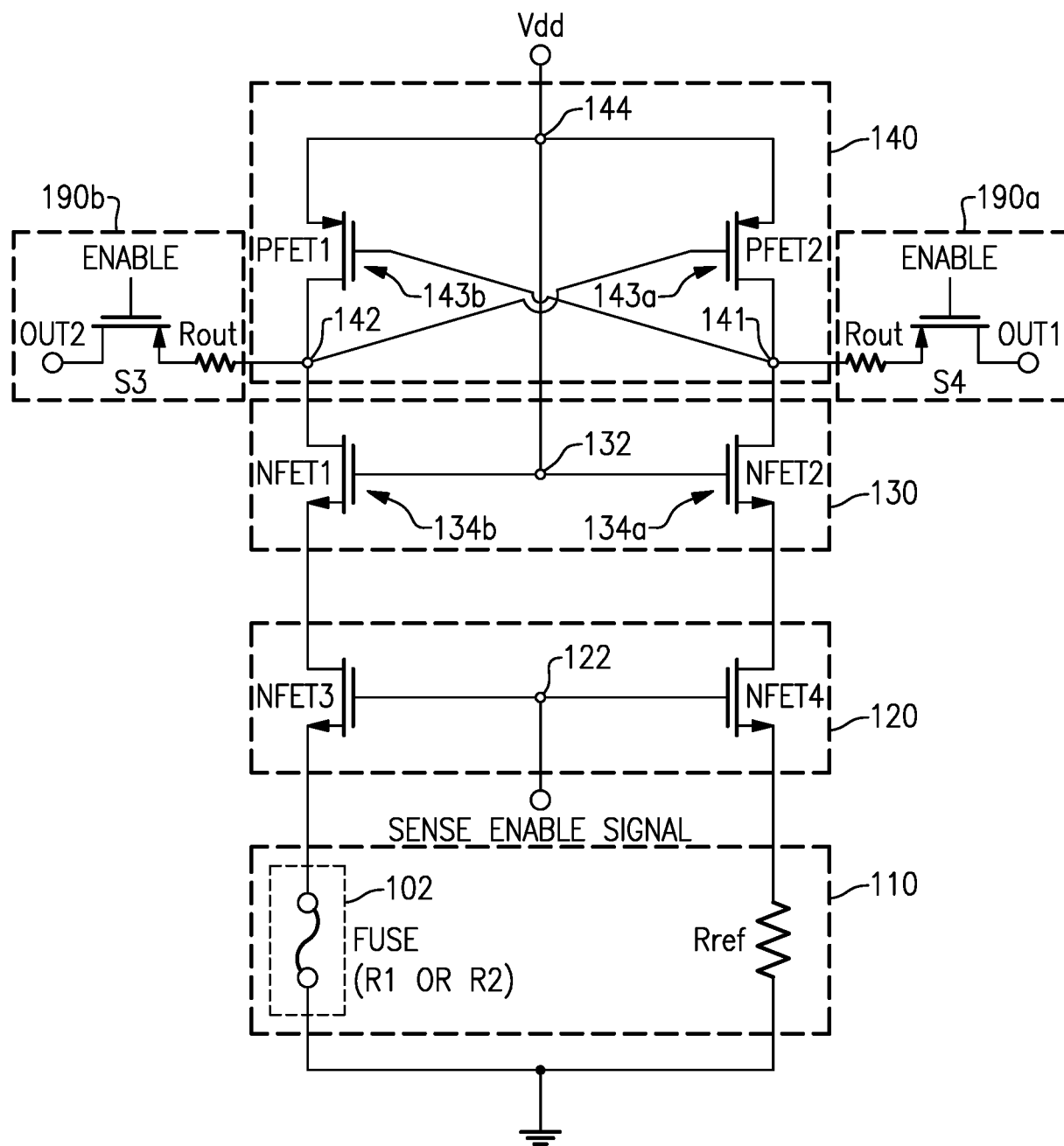


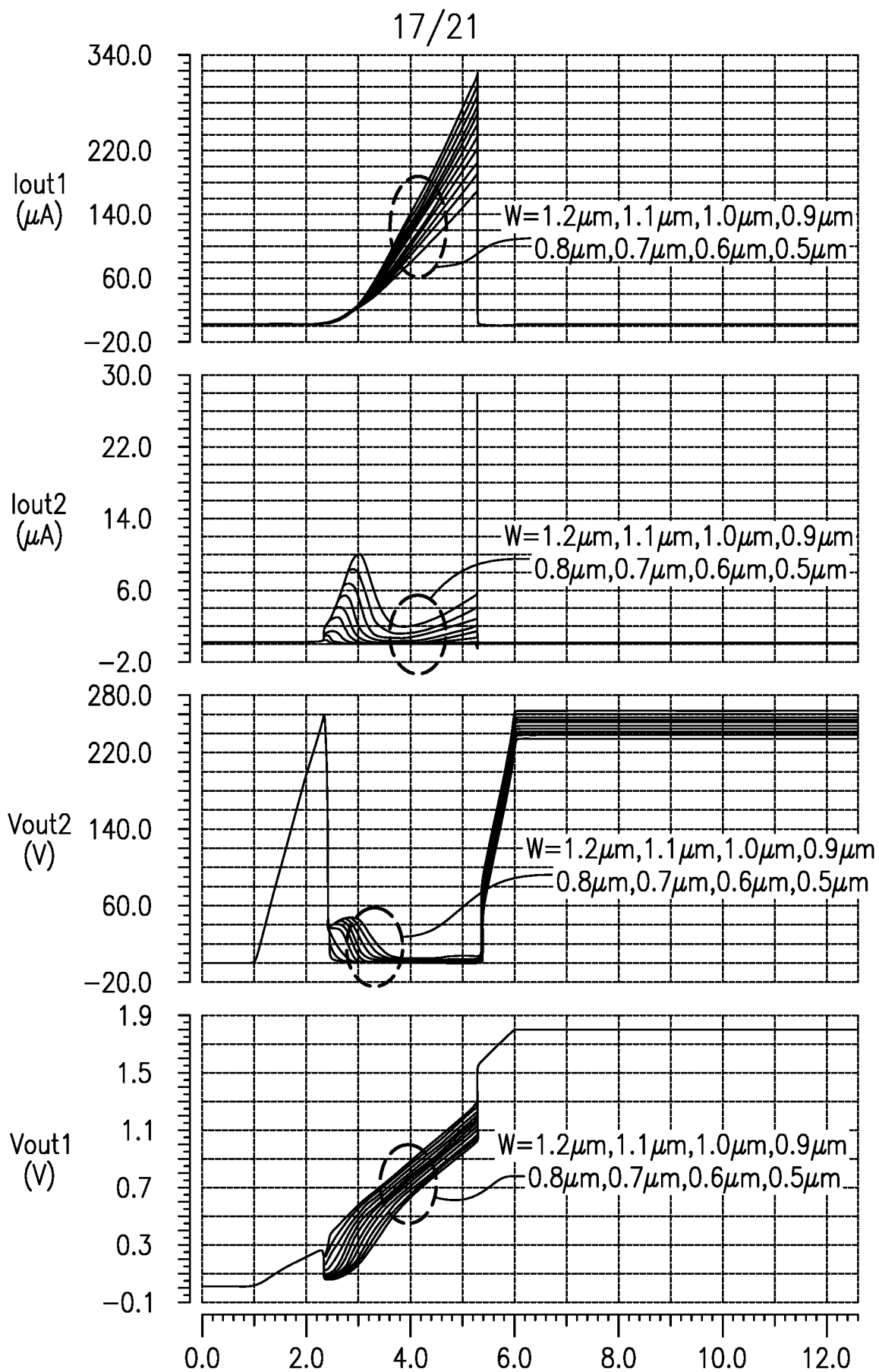
FIG.18

15/21

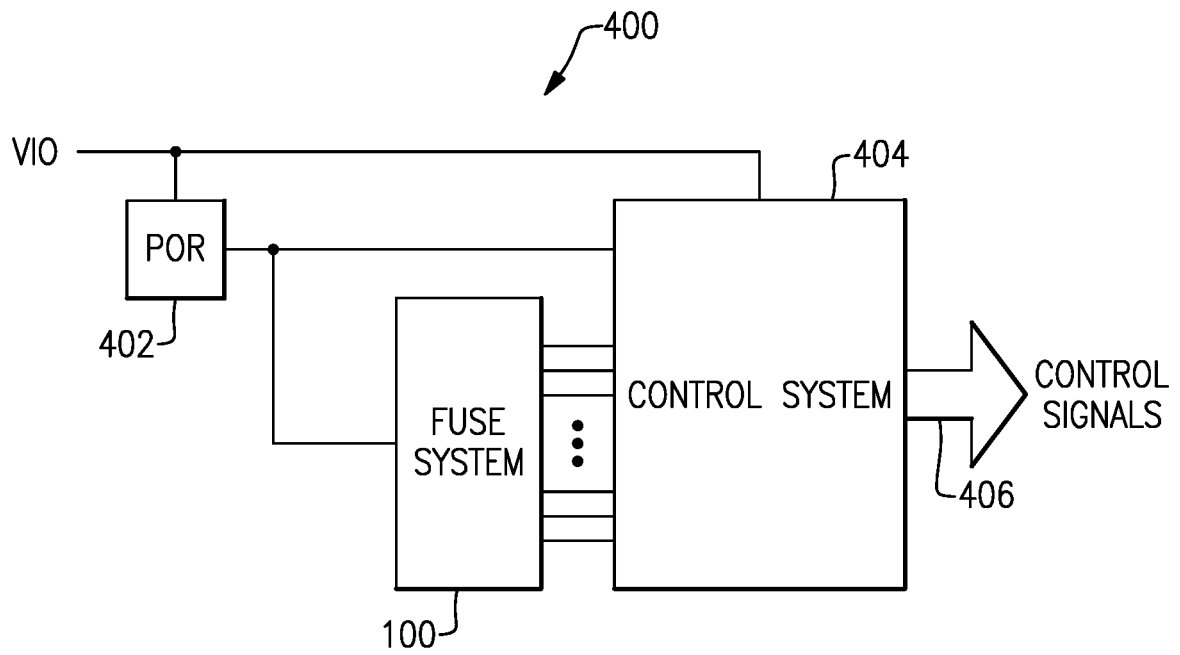
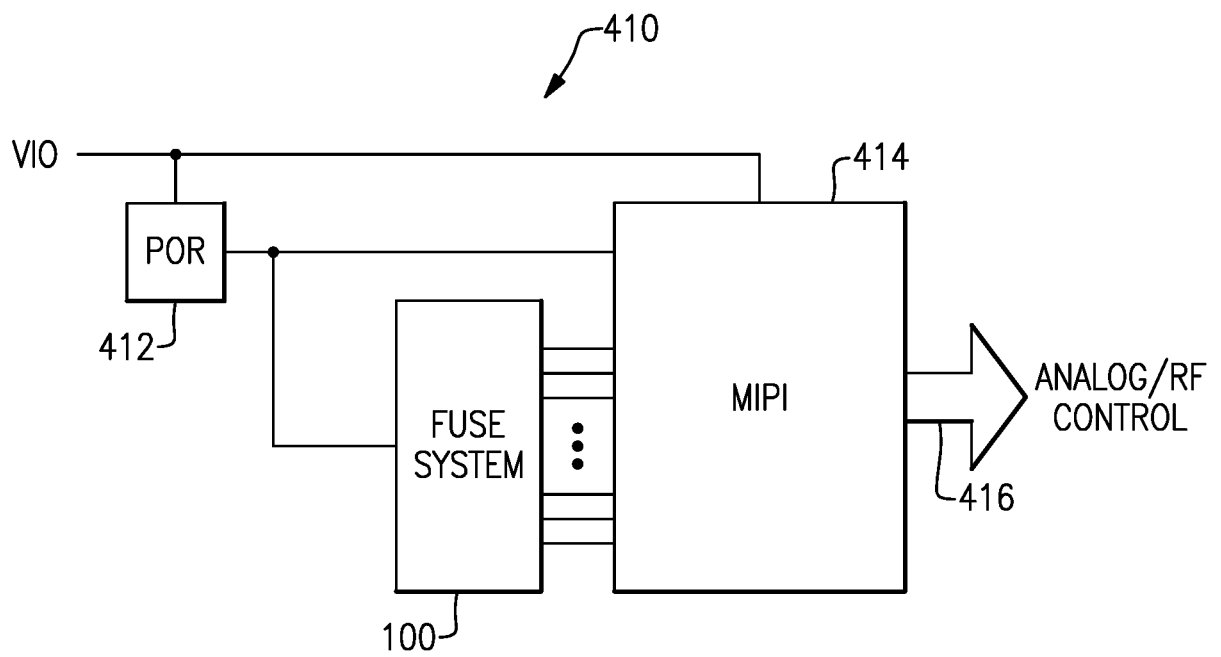
**FIG.19**

16/21

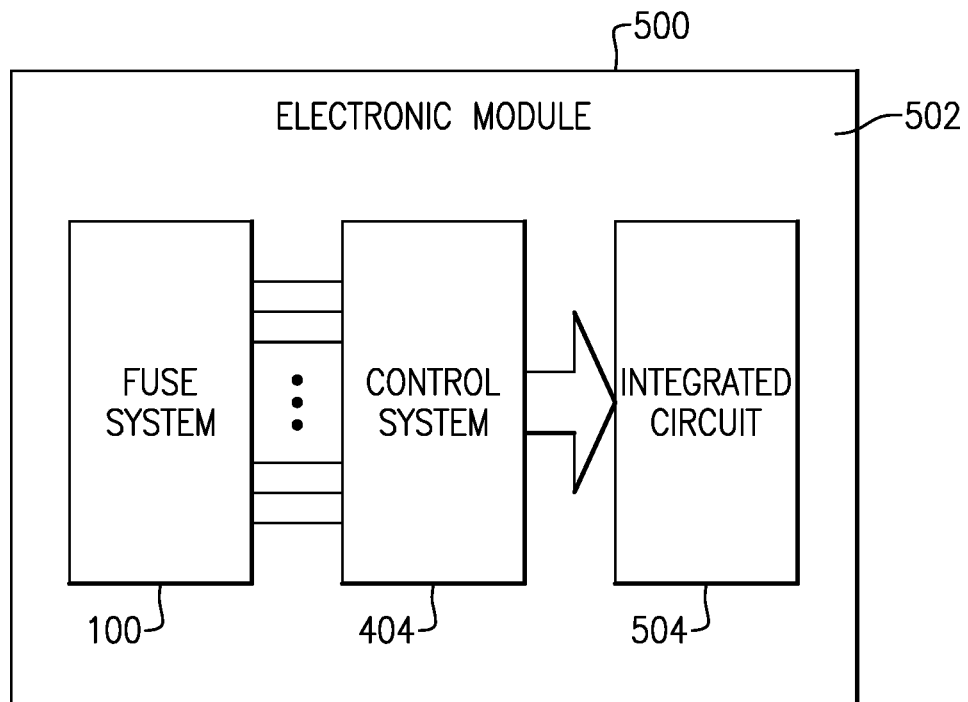
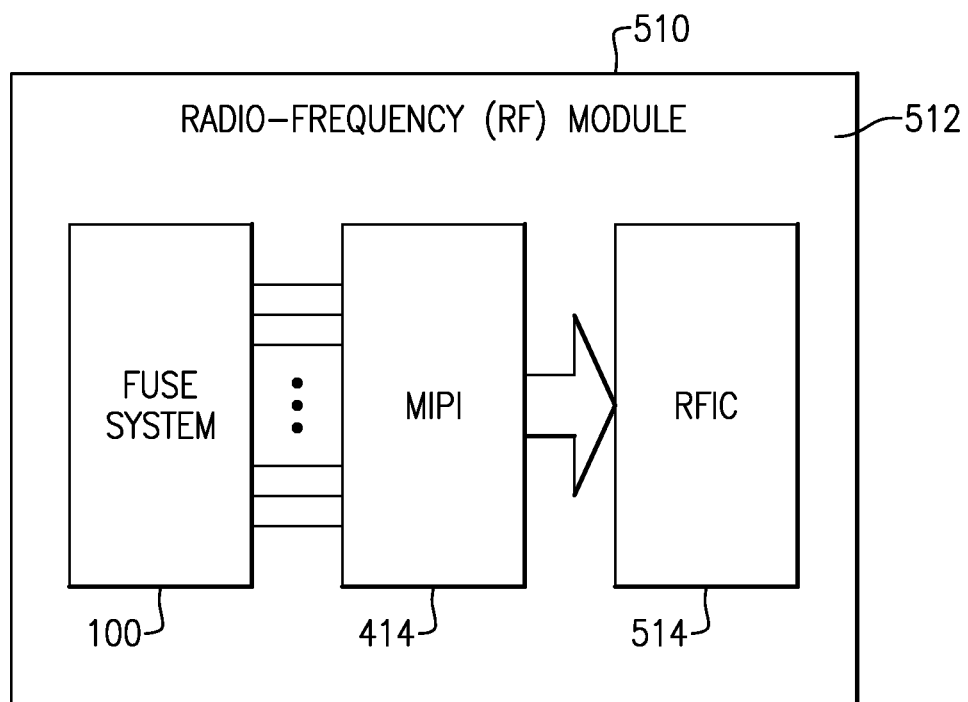
**FIG.20**

**FIG.21**

18/21

**FIG.22****FIG.23**

19/21

**FIG.24****FIG.25**

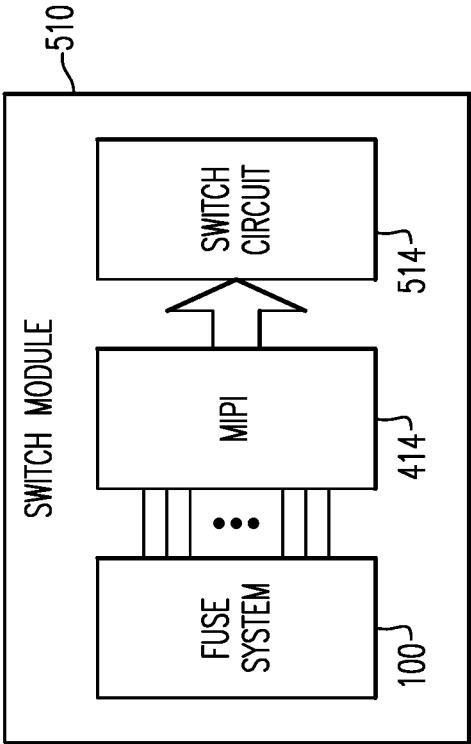


FIG.26C

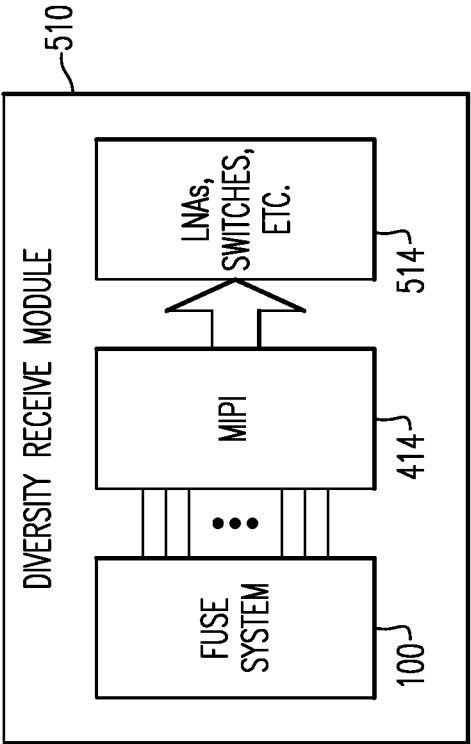


FIG.26D

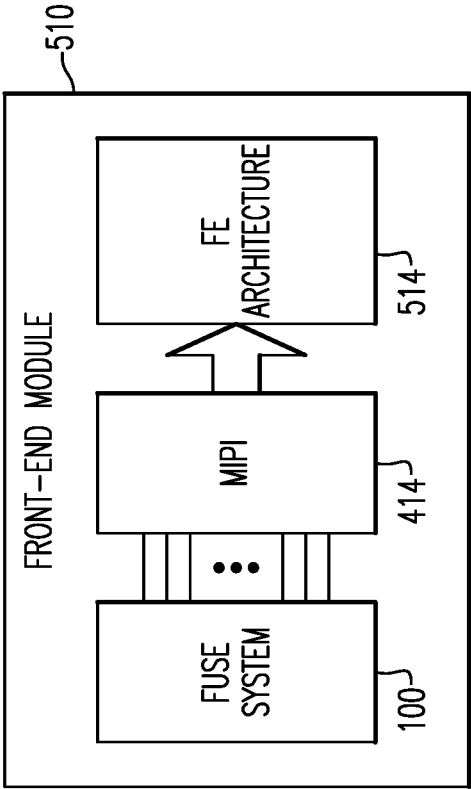


FIG.26A

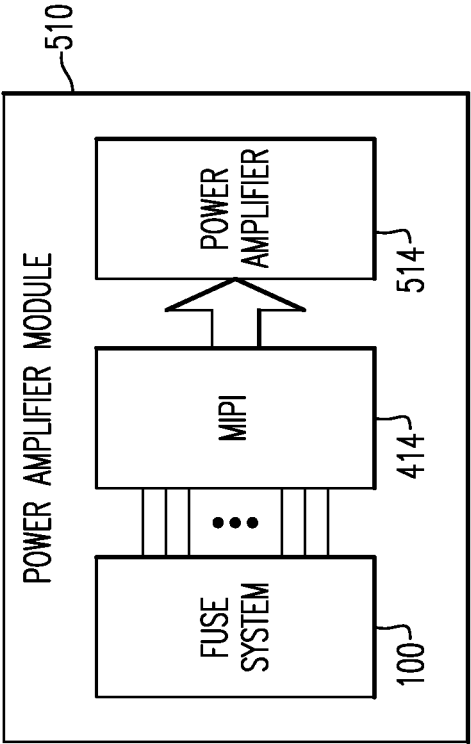
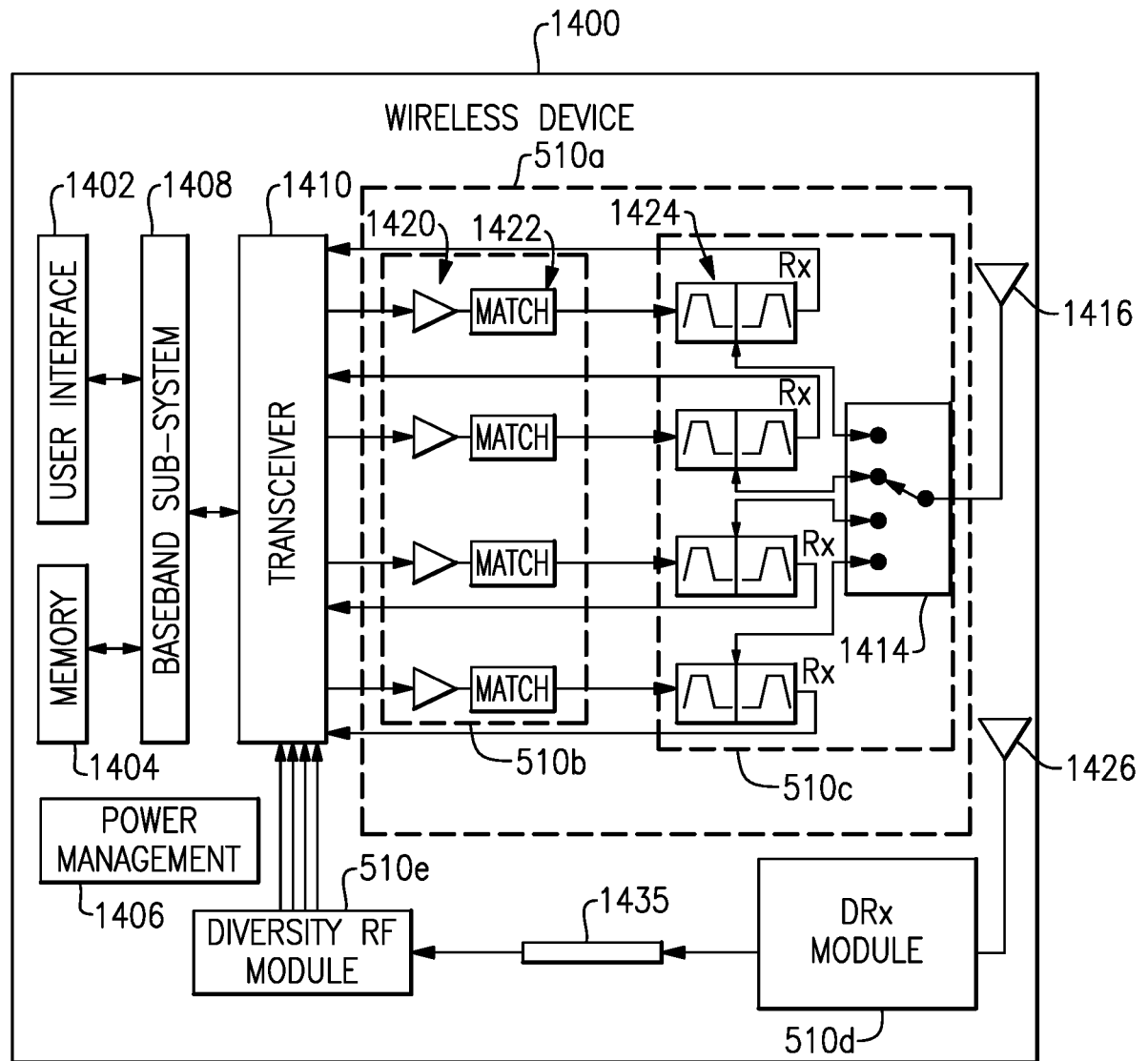


FIG.26B

21/21

**FIG.27**