

FIG. 1

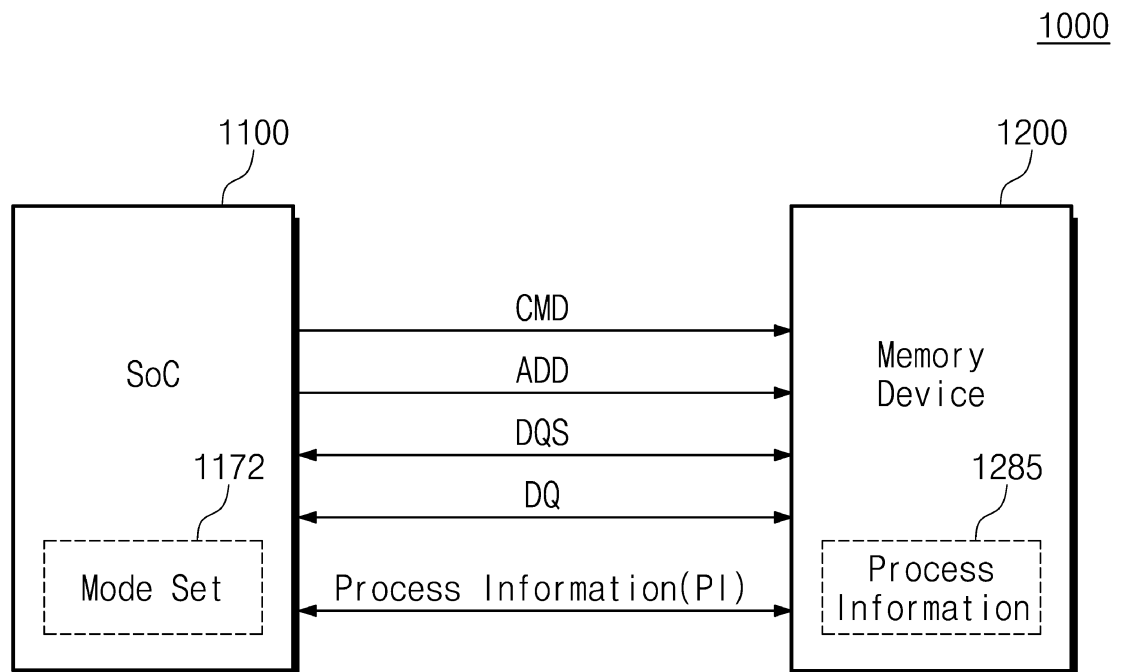


FIG. 2

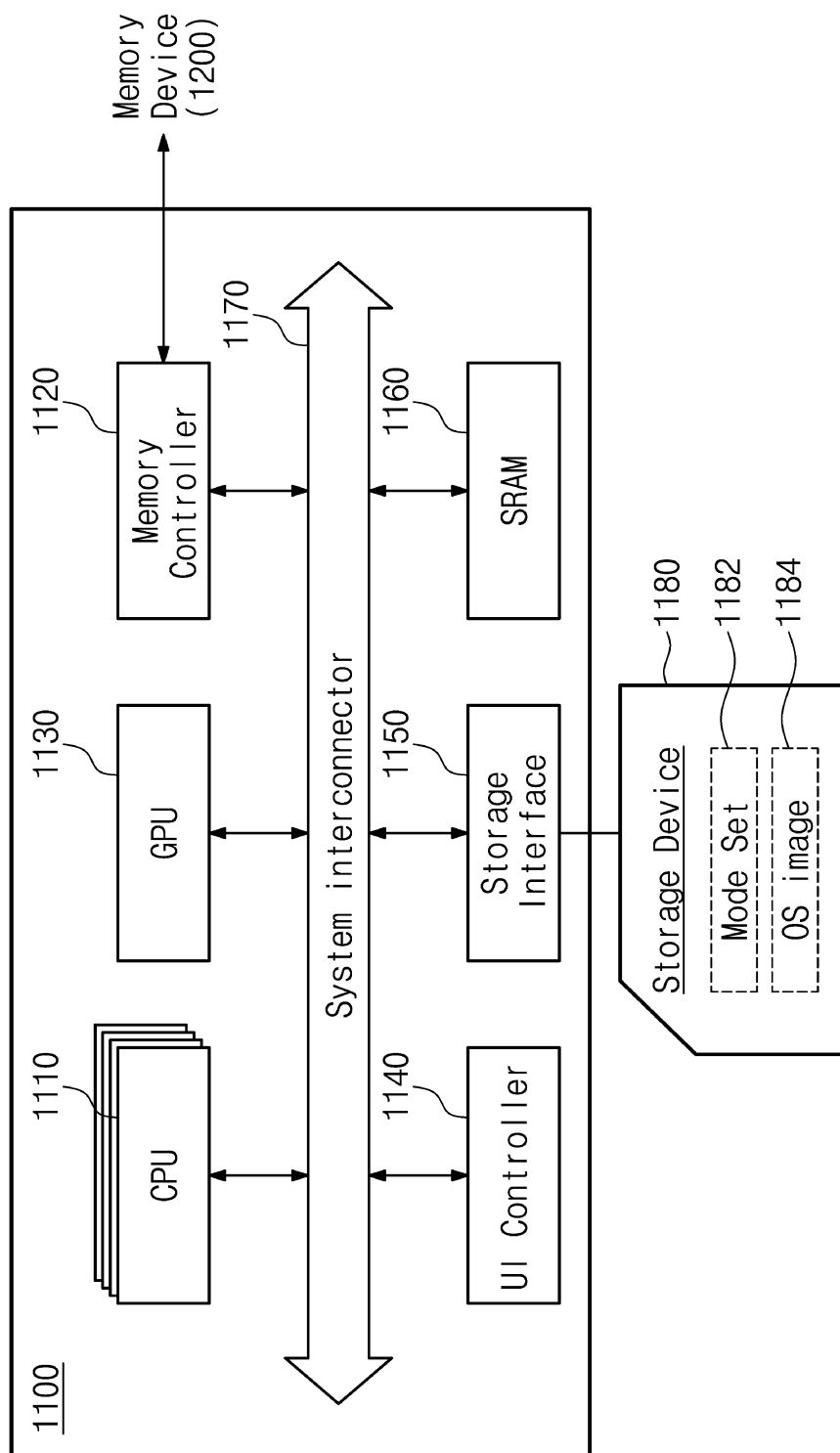


FIG. 3

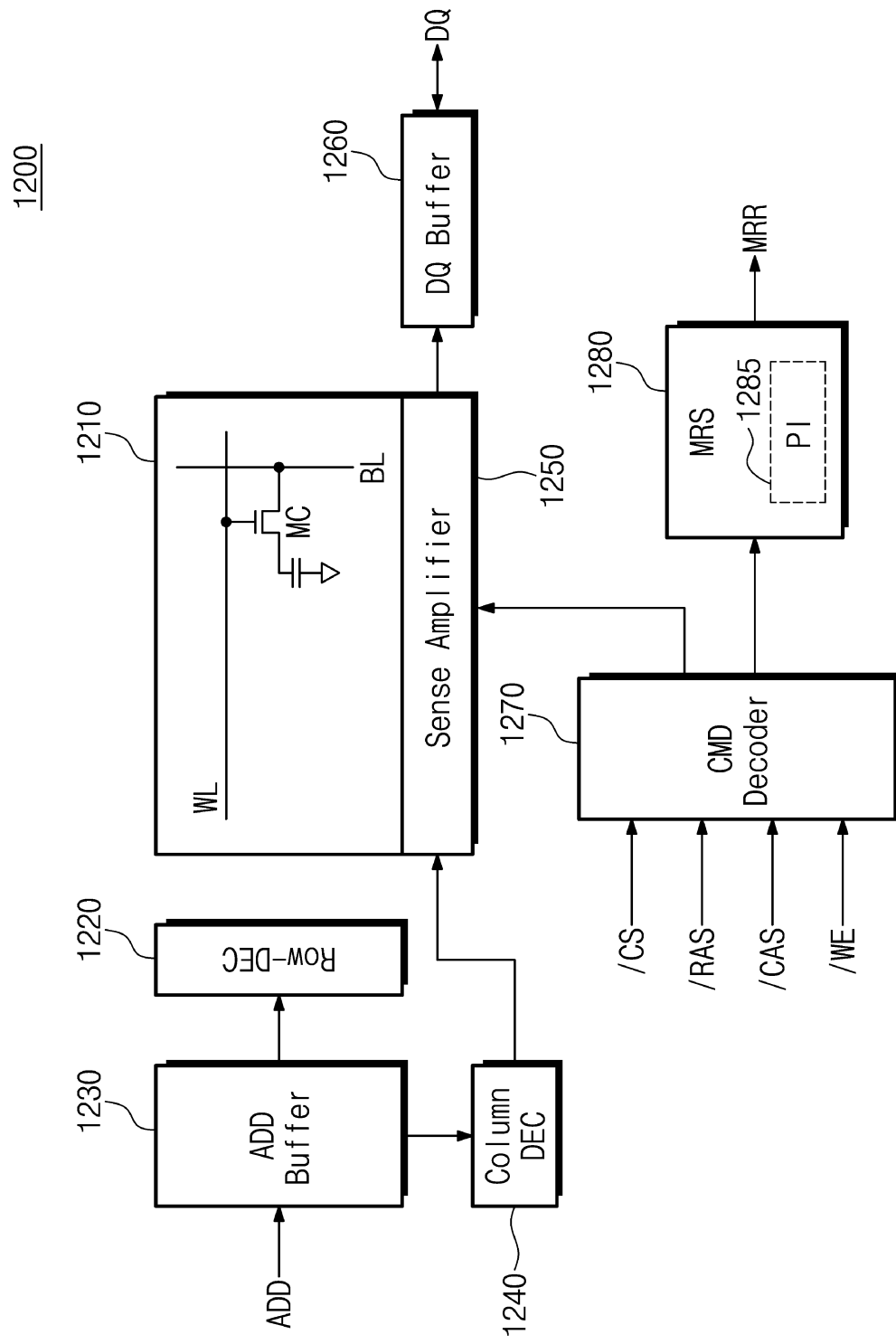


FIG. 4A

Mode set

OP	Mode	Latency	Voltage	Speed range	Remark
000	Mode1	1 x L	1 x VDD	~6400 Mbps	Normal Mode
001	Mode2	0.8 x L	1 x VDD	~7200 Mbps	High speed mode
010	Mode3	0.5 x L	1 x VDD	~8500 Mbps	Boost mode
011	Mode4	1 x L	0.8 x VDD	~3200 Mbps	Low PWR mode
100	Mode5	1 x L	0.6 x VDD	~1600 Mbps	Extreme Low PWR mode
⋮	⋮	⋮	⋮	⋮	⋮

FIG. 4B

Mode set

OP	Mode	Latency	Voltage	Speed range	Remark
000	Mode1	1 x L	1 x VDD	~6400 Mbps	Normal Mode
001	Mode2	0.8 x L	1 x VDD	~7200 Mbps	HS1 mode
010	Mode3	0.6 x L	1 x VDD	~8000 Mbps	HS2 mode
011	Mode4	0.5 x L	1 x VDD	~8500 Mbps	HS3 mode
100	Mode5	0.4 x L	1 x VDD	~9000 Mbps	HS4 mode
⋮	⋮	⋮	⋮	⋮	⋮

FIG. 4C

Mode set

OP	Mode	Latency	Voltage	Speed range	Remark
000	Mode1	1 x L	0.6 x VDD	~5400 Mbps	LPHS1 Mode
001	Mode2	0.8 x L	0.6 x VDD	~6200 Mbps	LPHS2 Mode
010	Mode3	0.6 x L	0.6 x VDD	~7000 Mbps	LPHS3 Mode
011	Mode4	0.5 x L	0.6 x VDD	~7500 Mbps	LPHS4 Mode
100	Mode5	0.4 x L	0.6 x VDD	~8000 Mbps	LPHS5 Mode
⋮	⋮	⋮	⋮	⋮	⋮

FIG. 4D

Mode set

OP	Mode	Latency	Voltage	Speed range	Remark
000	Mode1	1 x L	1 x VDD	~6400 Mbps	Normal Mode
001	Mode2	1 x L	0.9 x VDD	~6000 Mbps	LP1 mode
010	Mode3	1 x L	0.8 x VDD	~5500 Mbps	LP2 mode
011	Mode4	1 x L	0.7 x VDD	~5000 Mbps	LP3 mode
100	Mode5	1 x L	0.6 x VDD	~4500 Mbps	LP4 mode
⋮	⋮	⋮	⋮	⋮	⋮

FIG. 5

PI	Remark
0	Legacy Process
1	Enhanced Process

FIG. 6

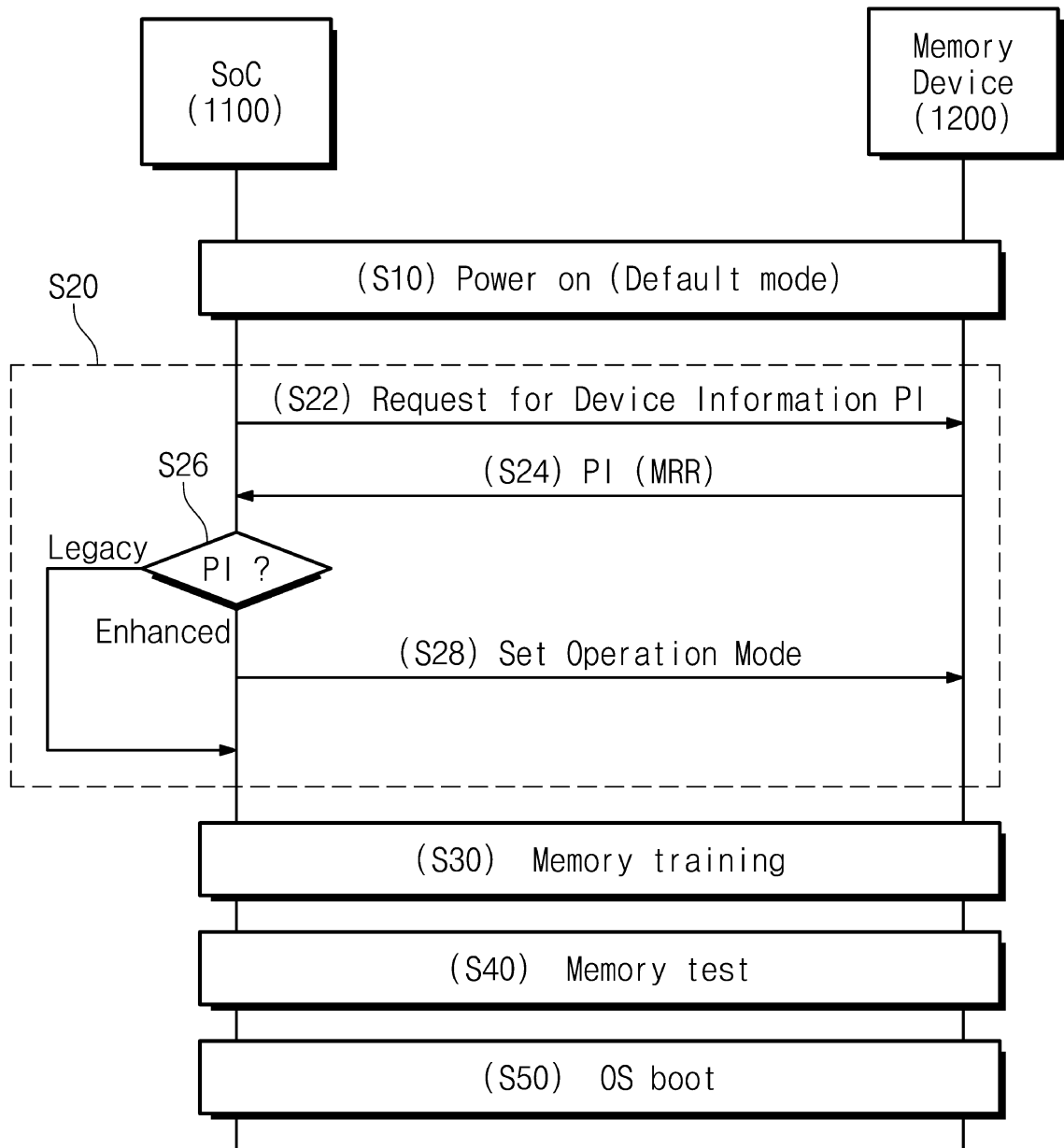


FIG. 7

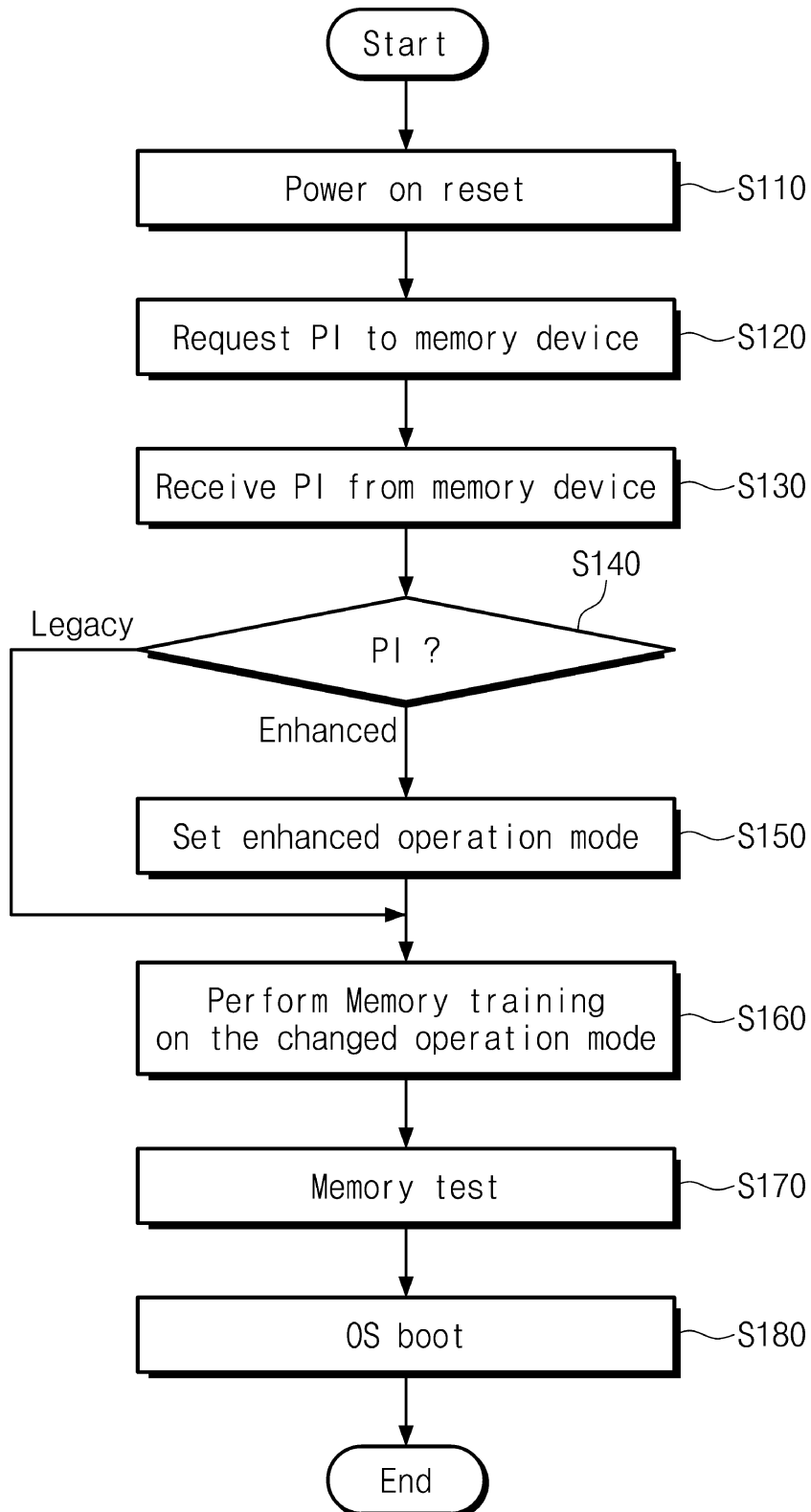


FIG. 8

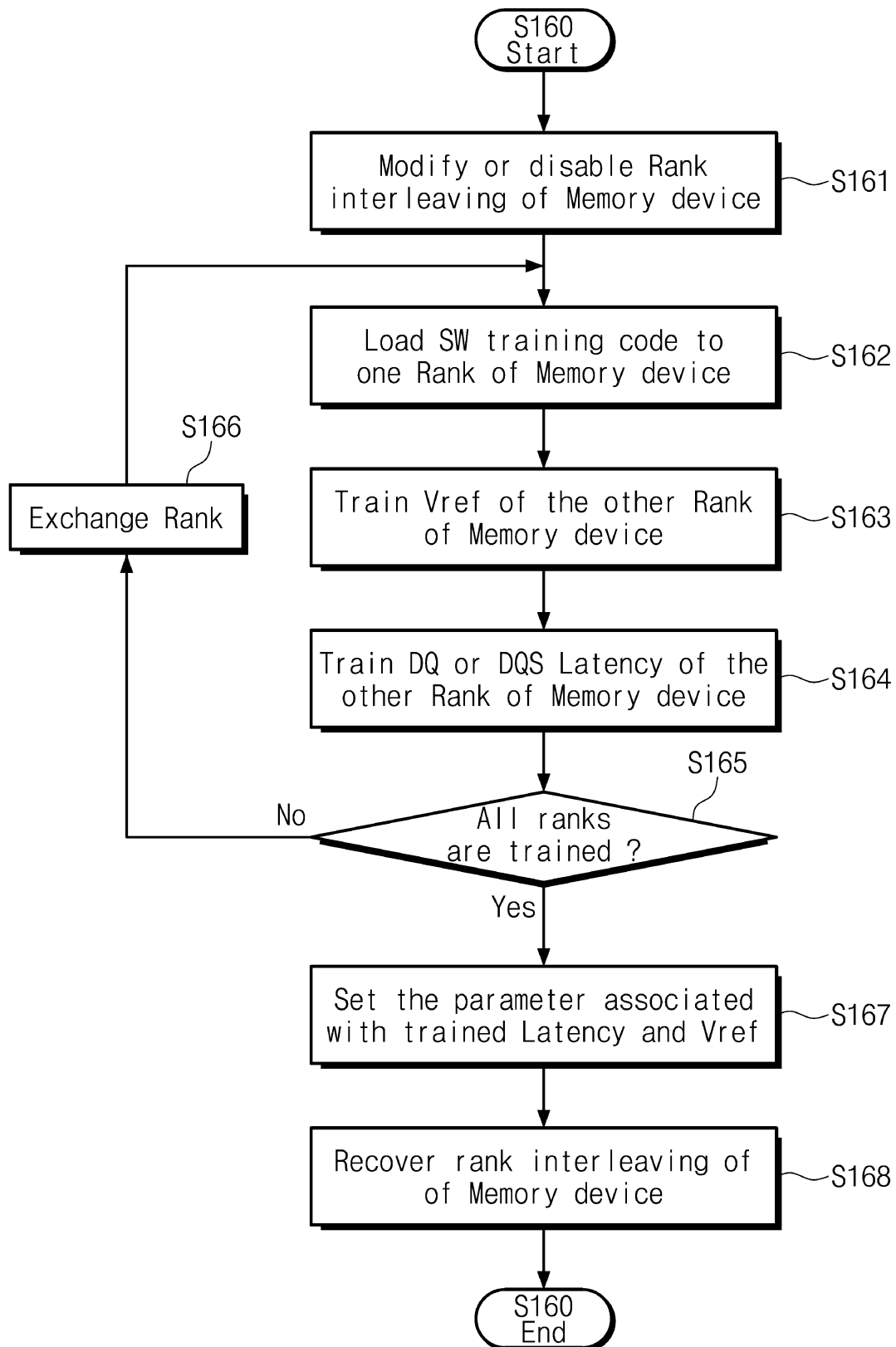


FIG. 9

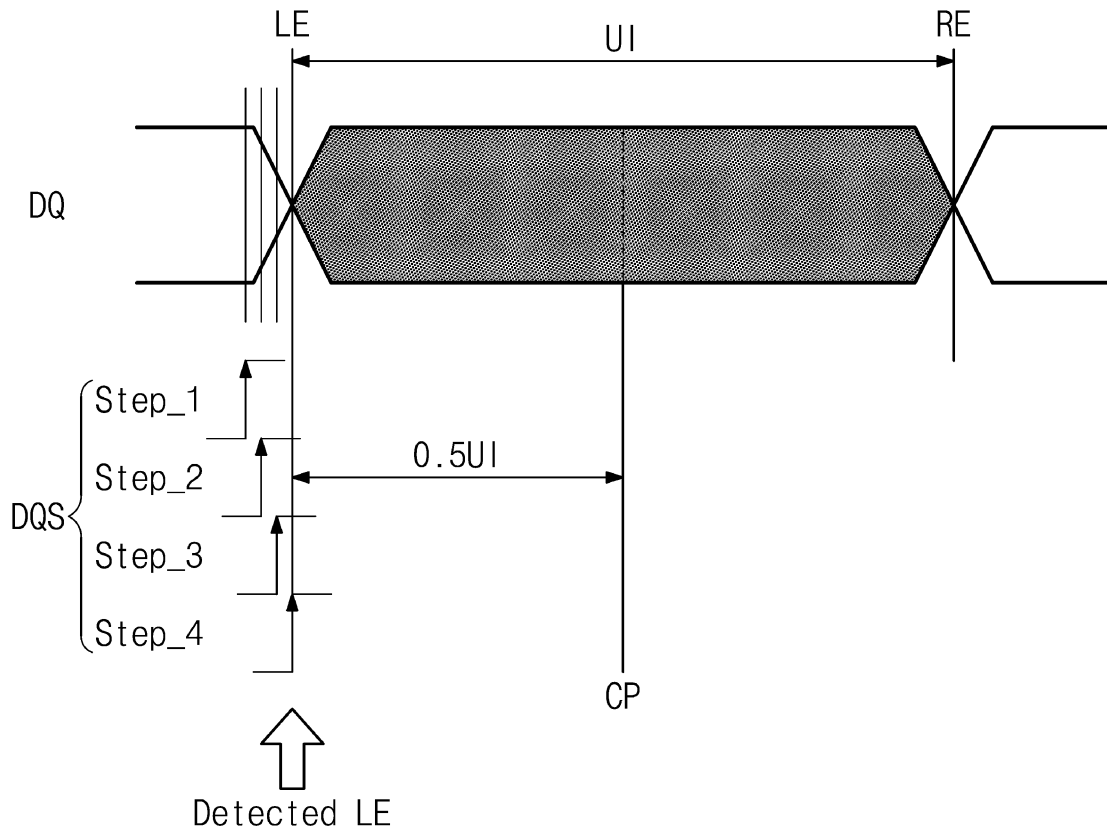


FIG. 10

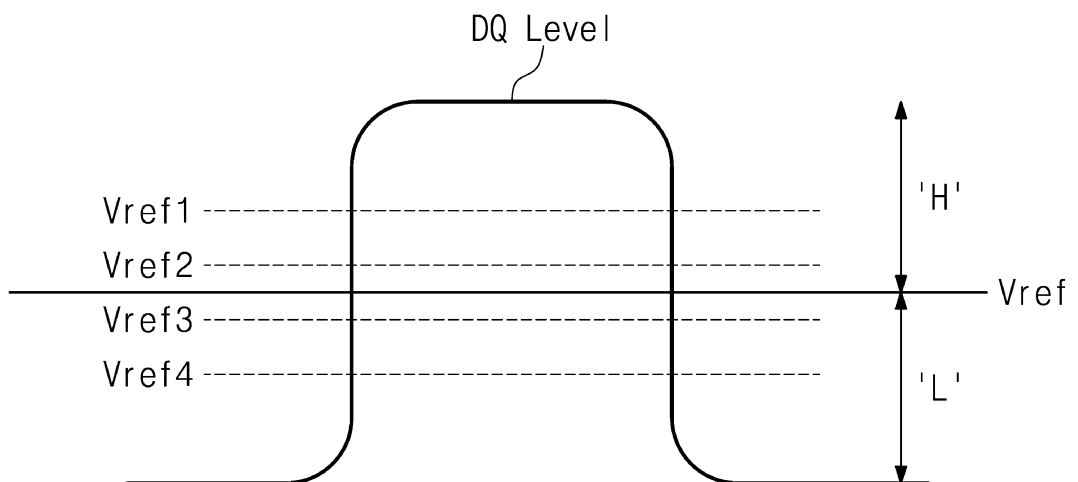


FIG. 11

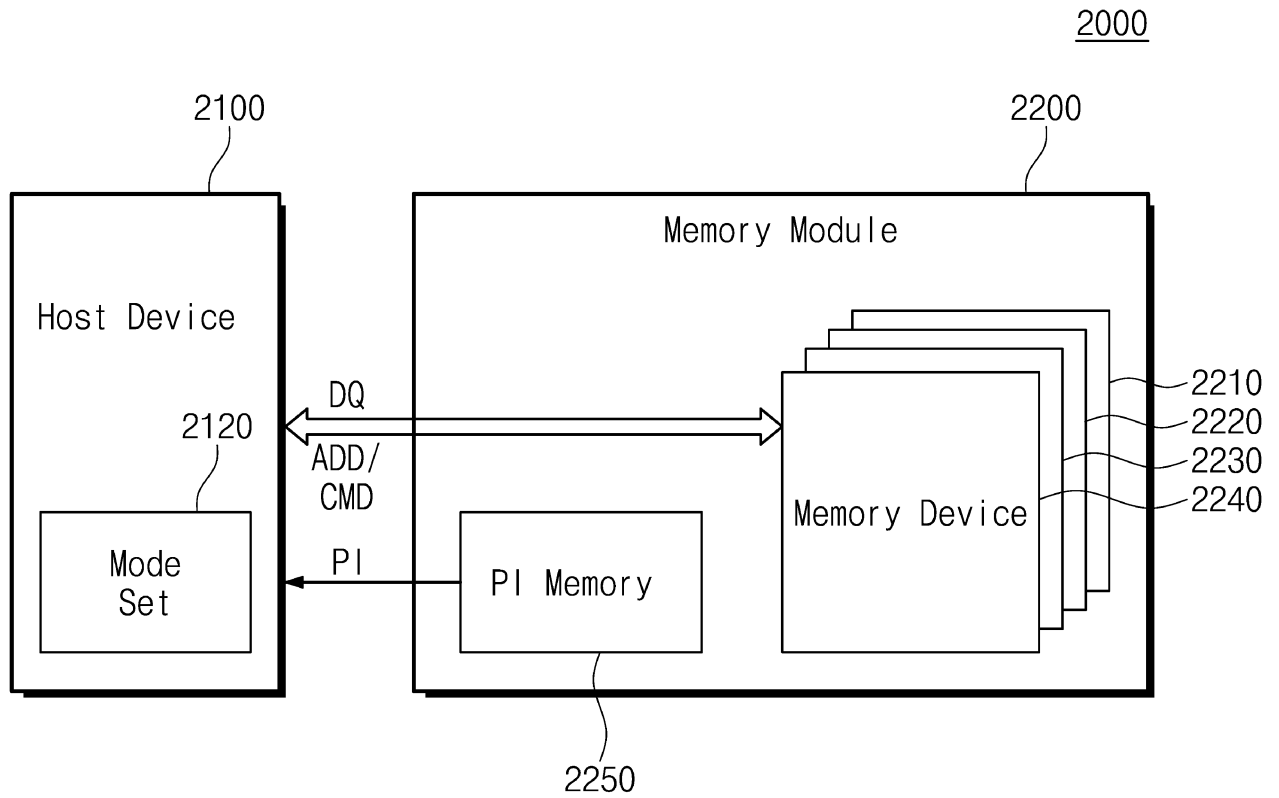


FIG. 12

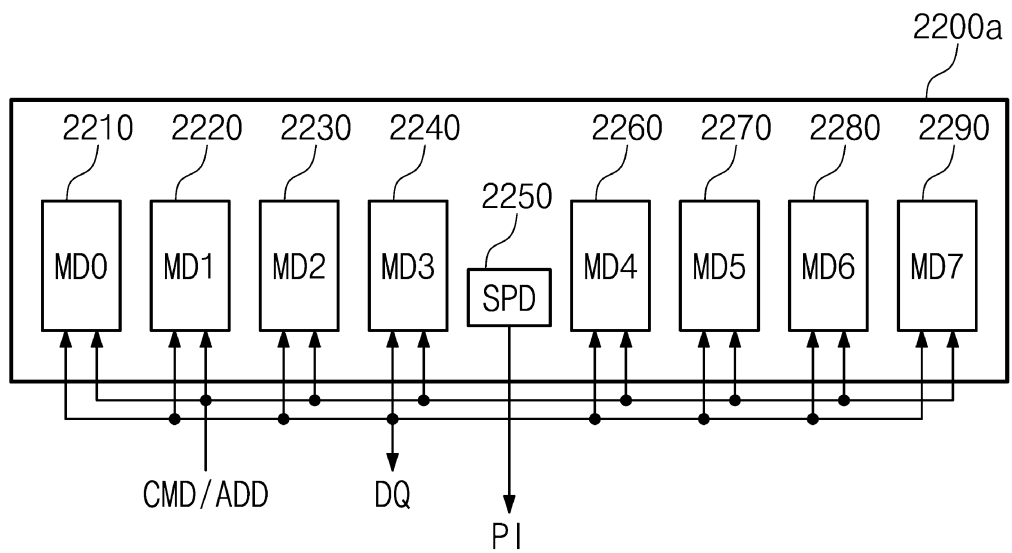


FIG. 13

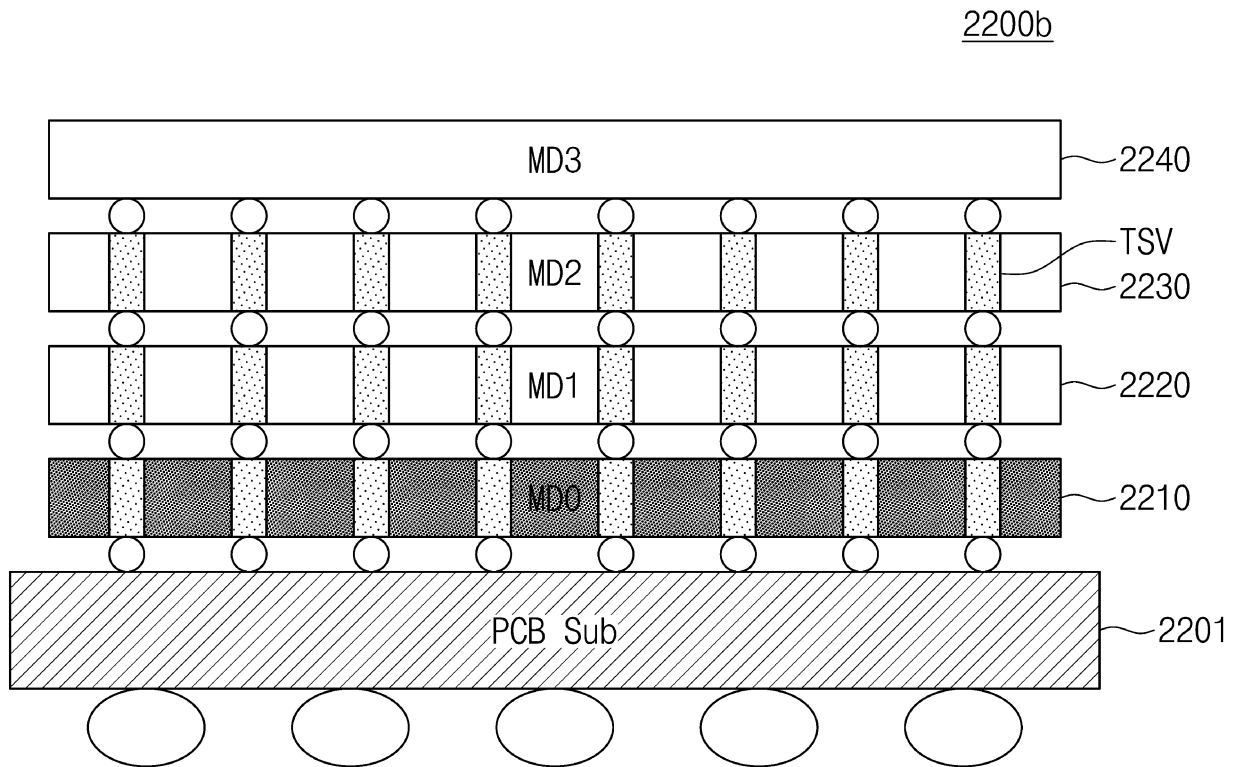


FIG. 14

