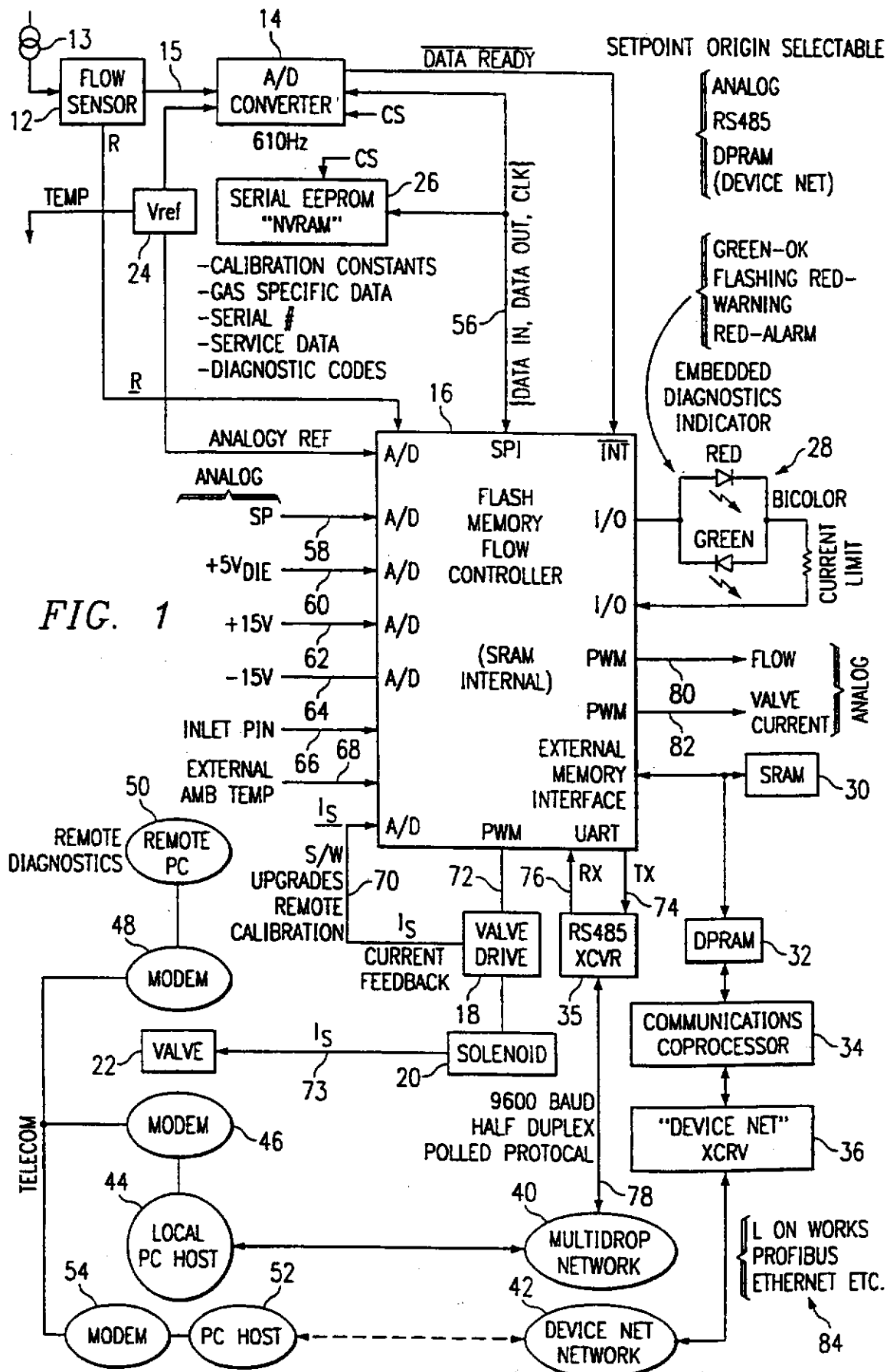




- 1) All "Data Processing" (for Controls, linearization, Totalizer, EEPROM, Diagnostics, e.t.c) is done here (within AB).
- 2)  $AB+BC=1.68\text{msec}$ .
- 3) As processing time grows AB will grow decreasing BC.
- 4)  $AC=1.68\text{msec}$  is generated by the external ADC.
- 5) No interrupts are allowed during AB.
- 6) If an interrupt occurs during AB it waits till AB is completed and then the interrupt is handled after B.
- 7) So far the max time consumed by AB is 30% of the full time AC (1.68msec)

- 1) No other interrupts are allowed till the end of the interrupt subroutine.
- 2) Any of these three interrupts can happen in any order and at any location.
- 3) Only data transfer is done here, the actual processing of data is done afterwards at AB
- 4) These ISRs typically consume 0.06% to 0.1% of the total 1.68msec

FIG. 2

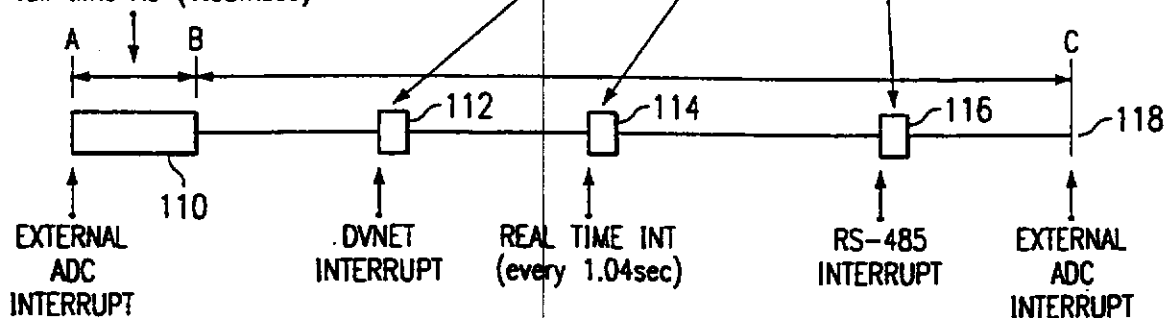
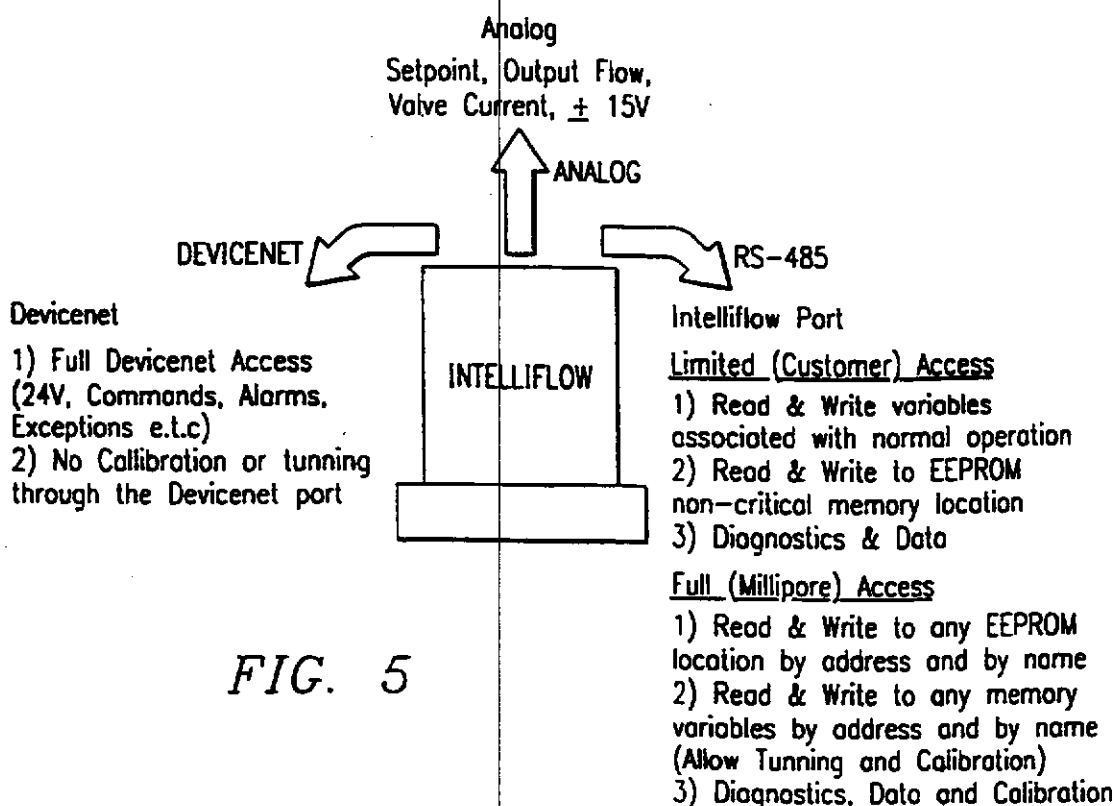


FIG. 5

FIG. 3

