

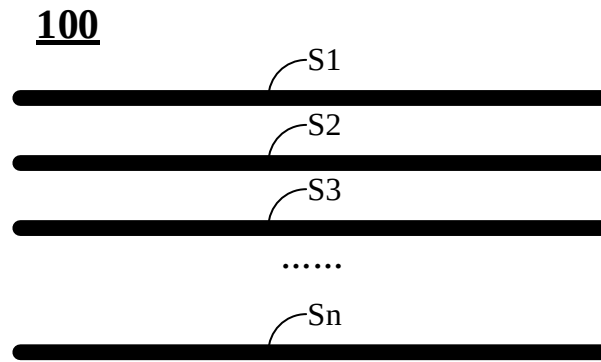


FIG. 1

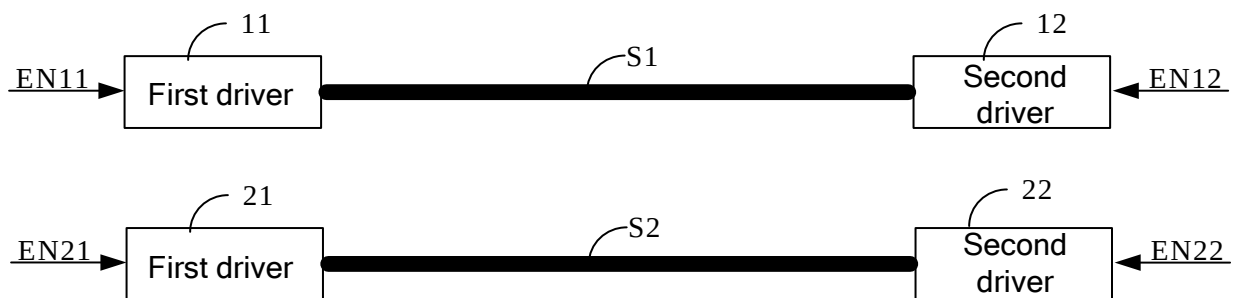


FIG. 2

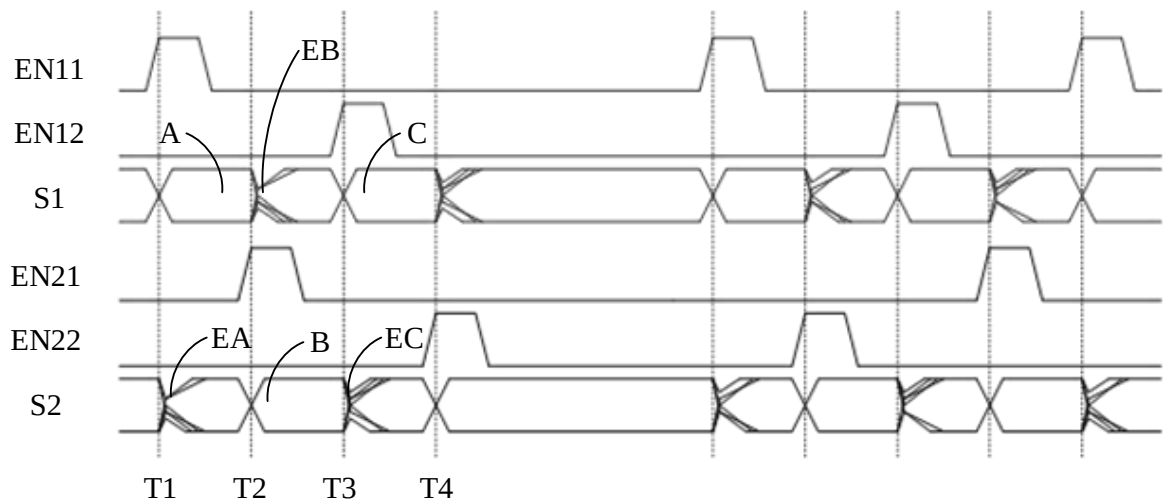


FIG. 3A

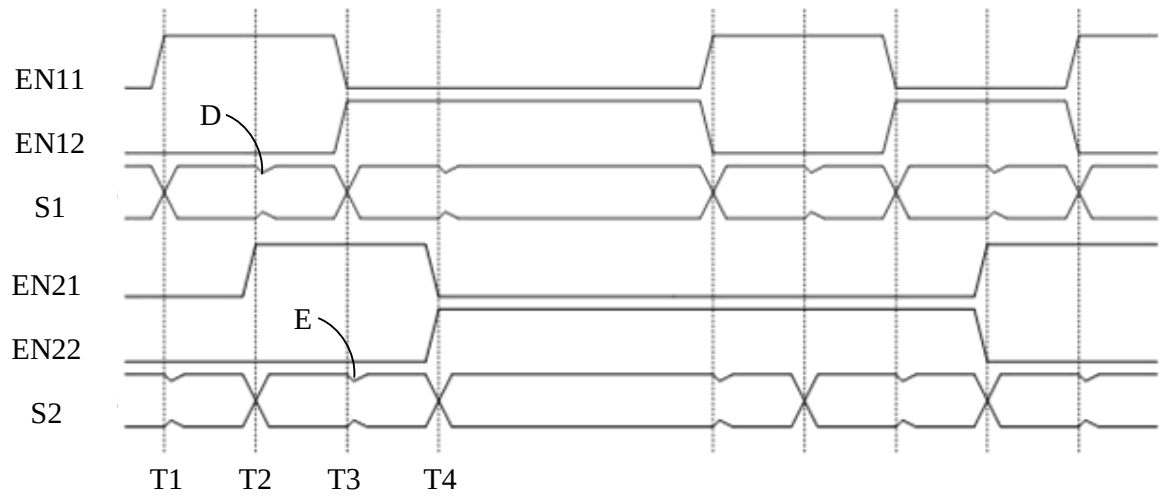


FIG. 3B

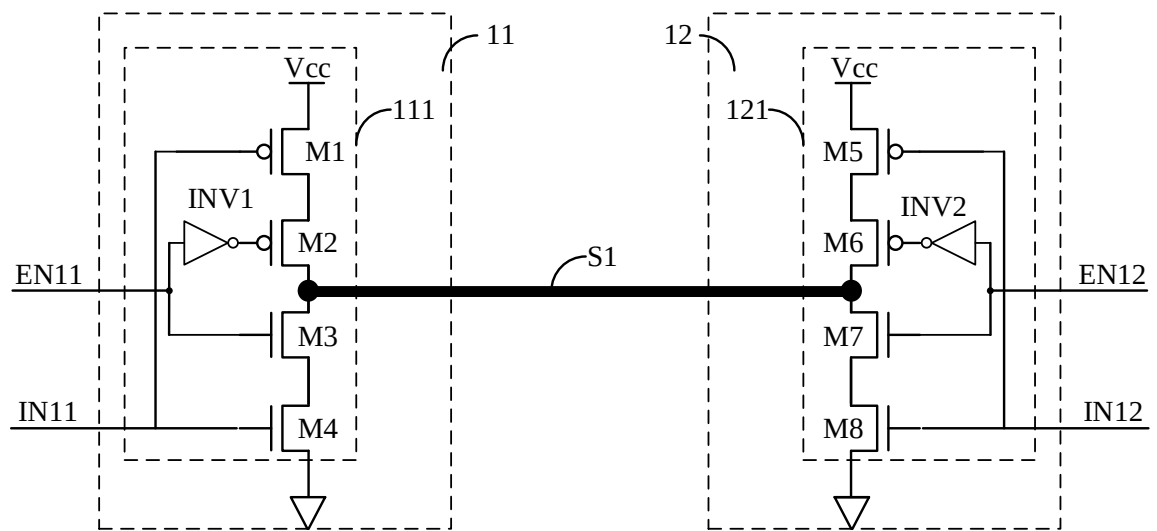


FIG. 4

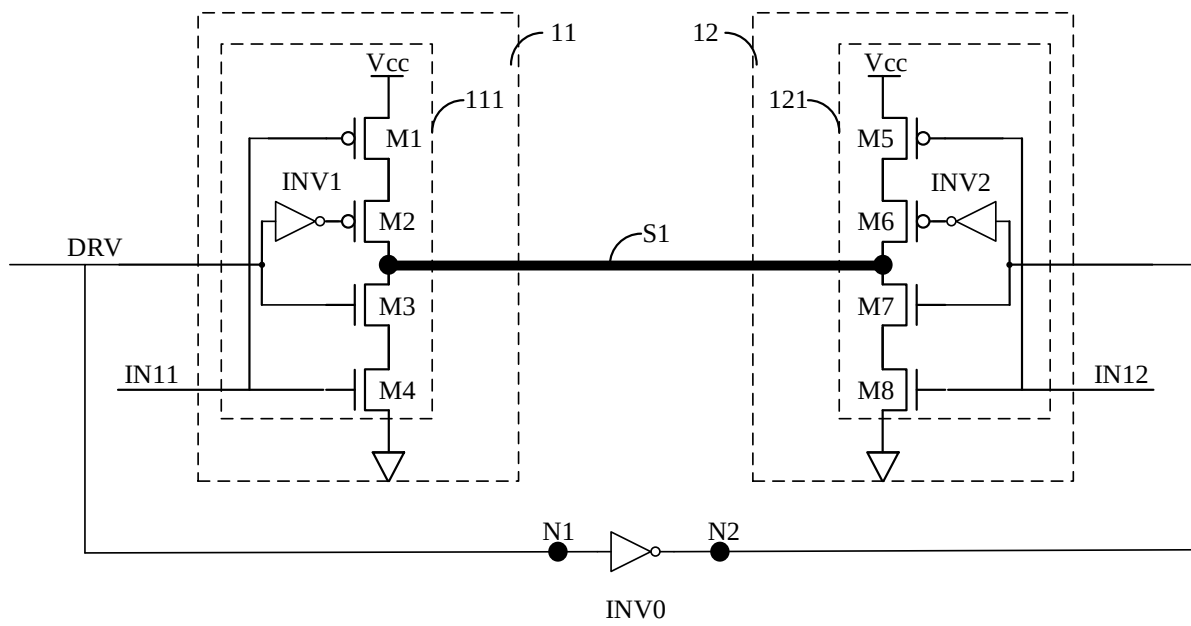


FIG. 5

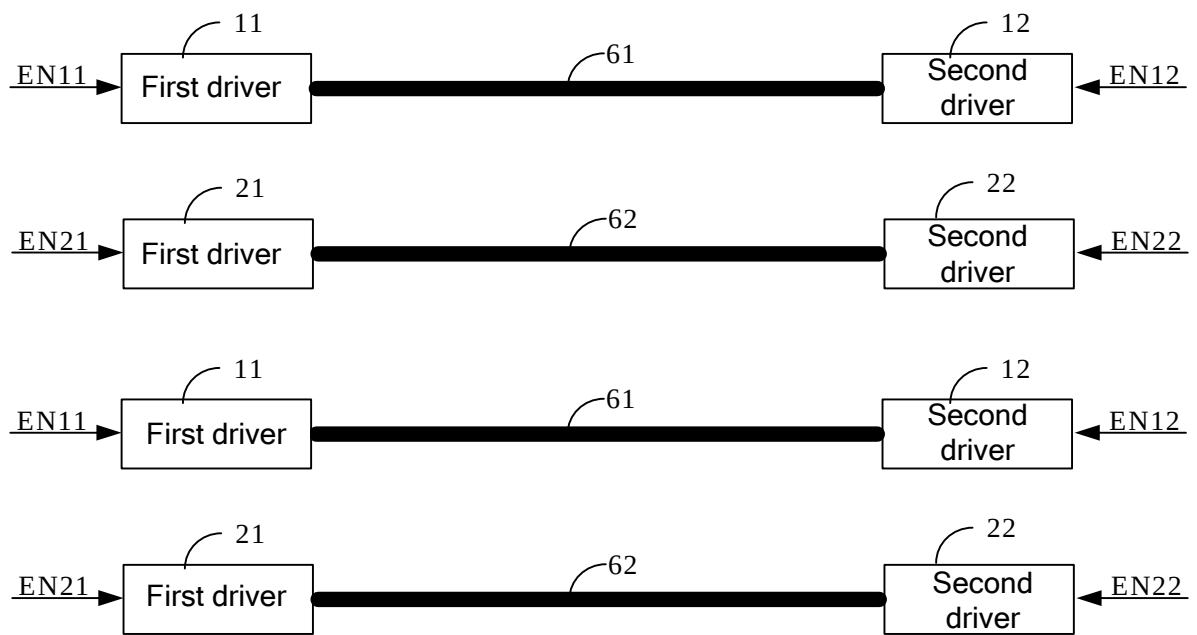


FIG. 6

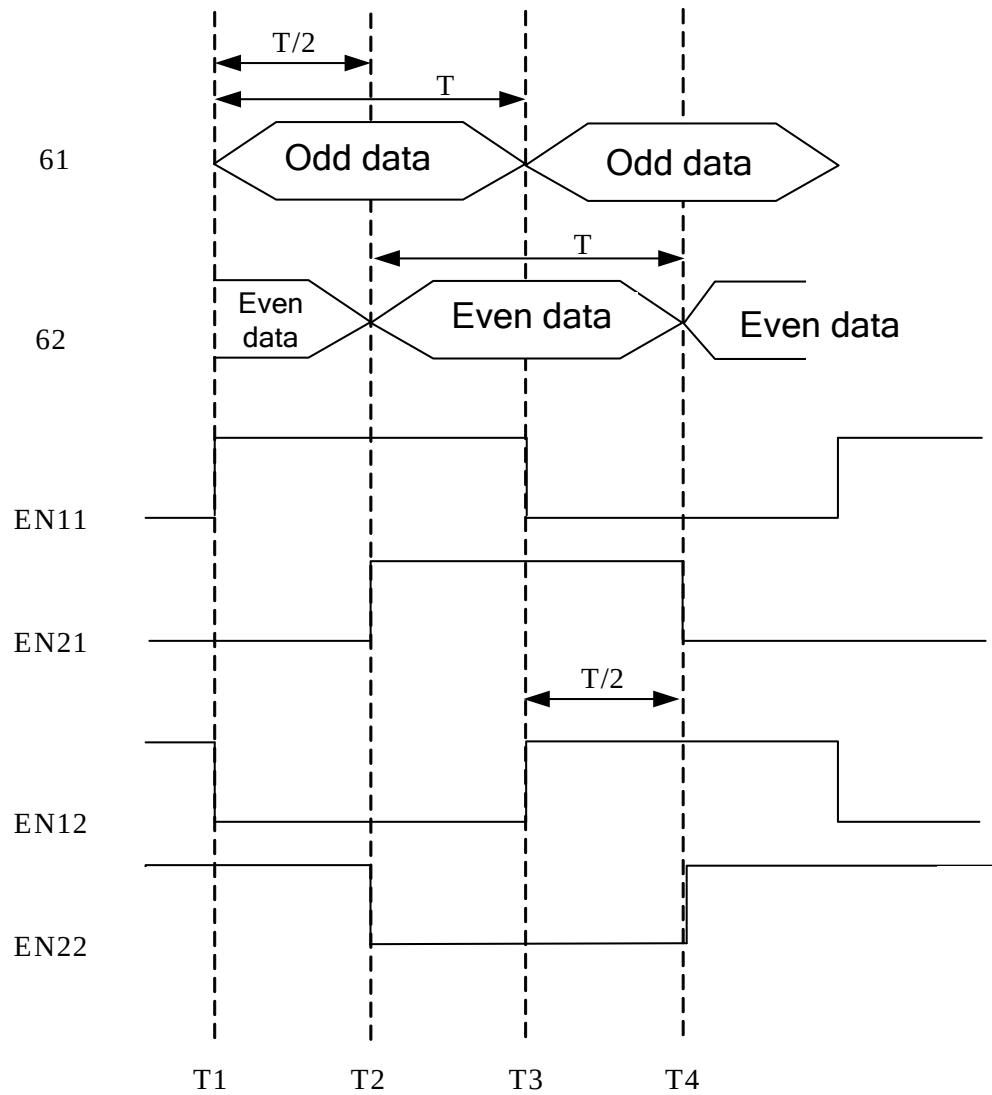


FIG. 7

800

Control a plurality of parallel signal lines
to be maintained in a drive state at any
time

S10

FIG. 8

900

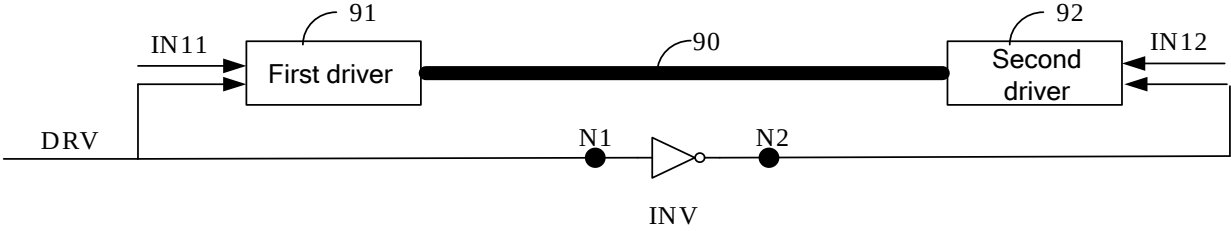


FIG. 9