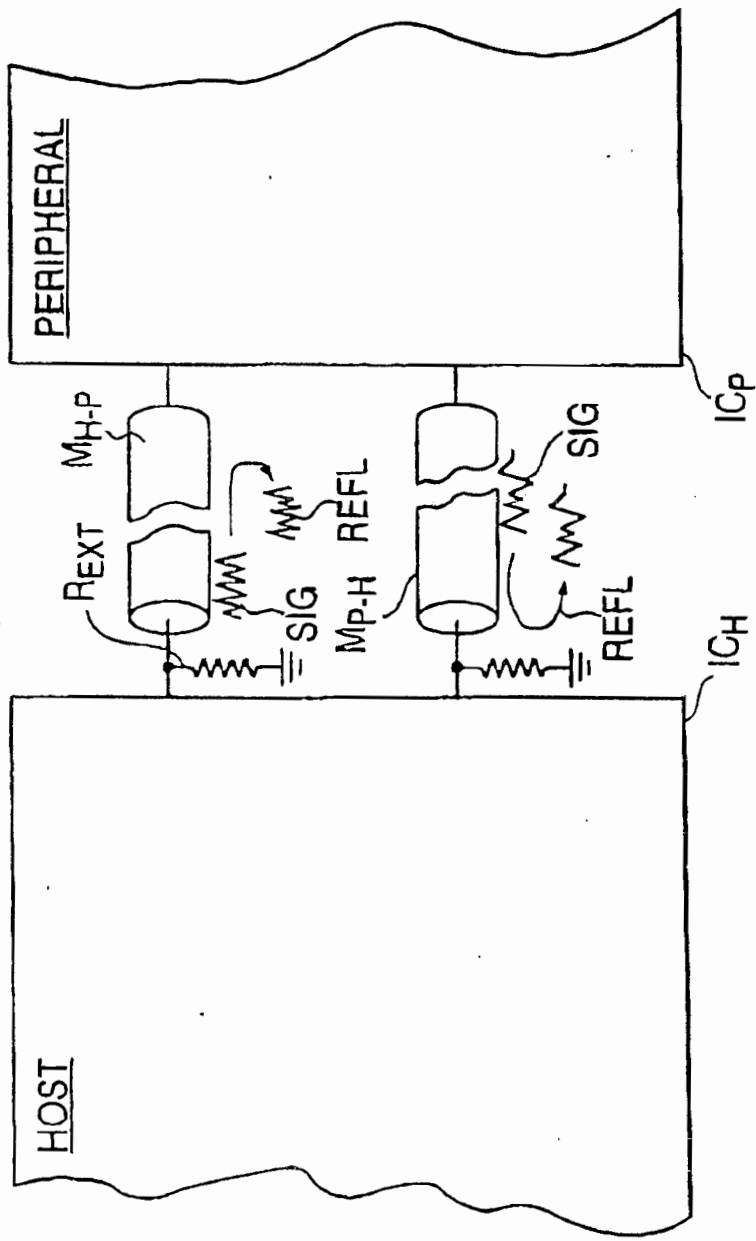


FIG. 1



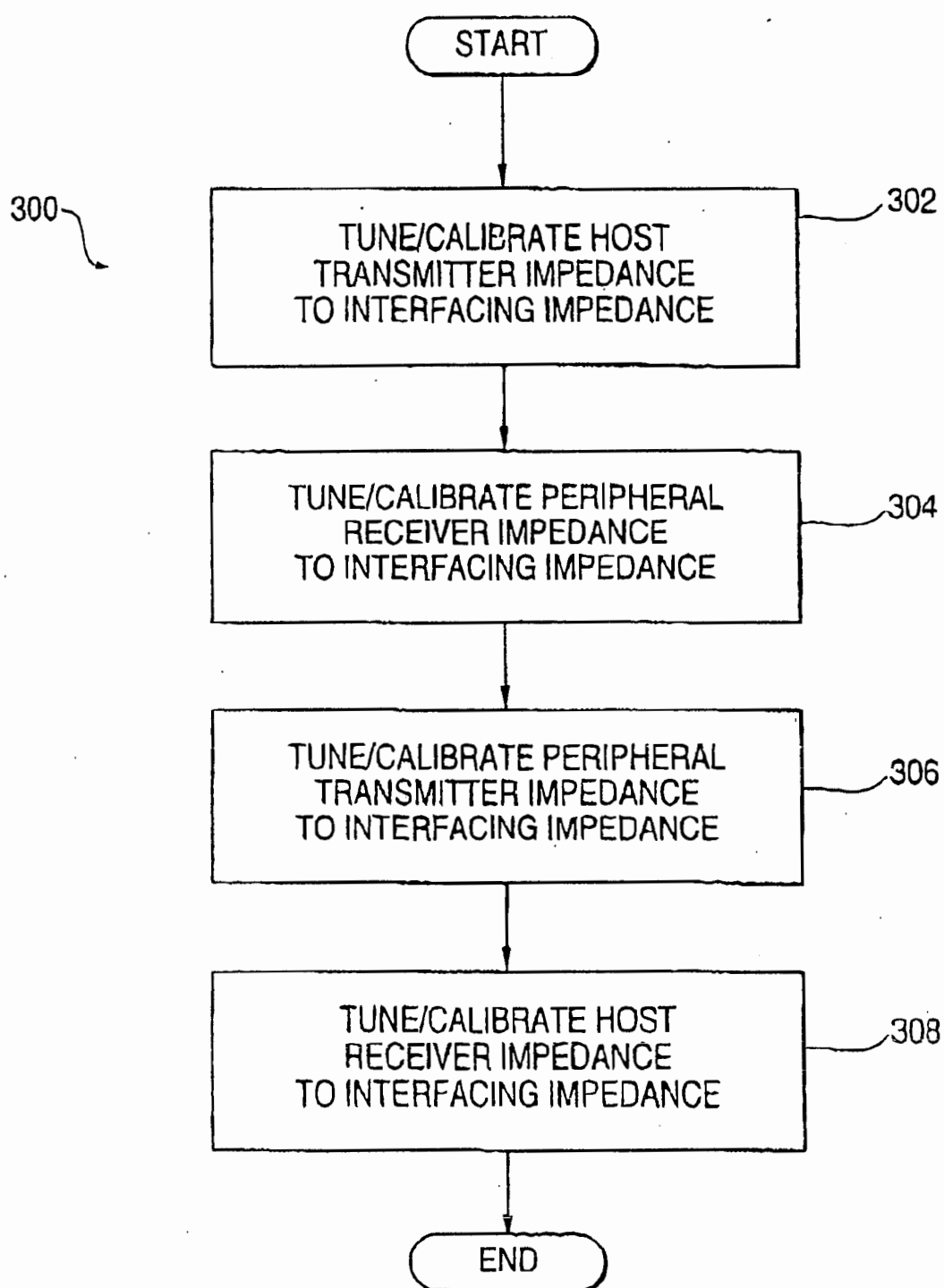
The figure consists of two schematic diagrams, (a) and (b), illustrating a differential signaling system. Both diagrams are divided into a 'Host' section on the left and a 'Peripheral' section on the right by a dashed vertical line.

Diagram (a): The Host section contains a differential driver (TX) with inputs 'Data' and 'Reset'. The Peripheral section contains a differential receiver (RX) with outputs 'Data' and 'Reset'. The TX and RX are connected via a mutual inductor M_{H-P} . Feedback resistors Z_{TH} and Z_{TP} are connected between the TX and RX inputs/outputs. The total resistance is labeled R_{TH} and R_{TP} . The mutual inductor is labeled M_{H-P} .

Diagram (b): The Host section contains a differential driver (TX) with inputs 'Data' and 'Reset'. The Peripheral section contains a differential receiver (RX) with outputs 'Data' and 'Reset'. The TX and RX are connected via a mutual inductor M_{P-H} . Feedback resistors Z_{TH} and Z_{TP} are connected between the TX and RX inputs/outputs. The total resistance is labeled R_{TH} and R_{TP} . The mutual inductor is labeled M_{P-H} .

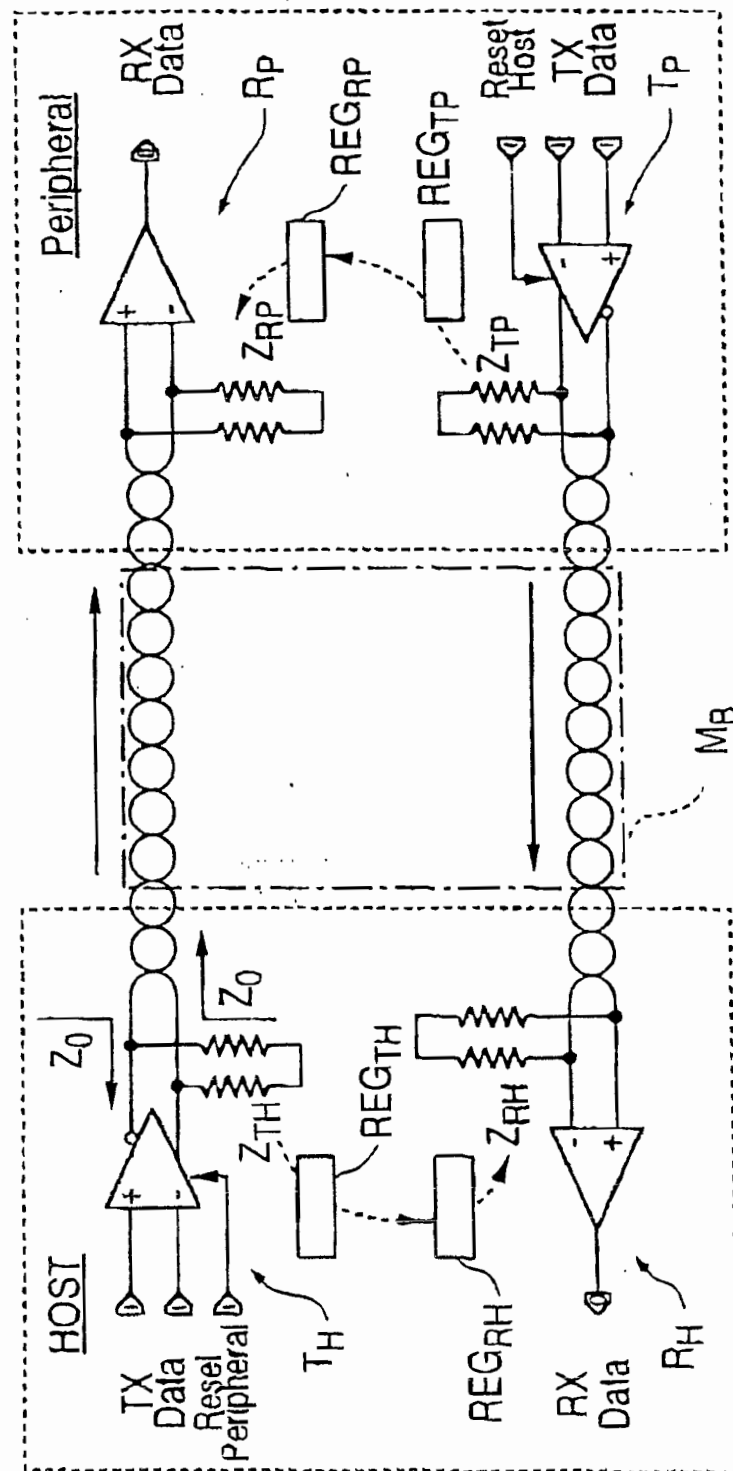
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FIG. 3



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FIG. 4



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FIG. 5

