ABSTRACT

STEPPING MOTOR DRIVE CIRCUIT AND ANALOG ELECTRONIC CLOCK

The present invention aims to generate a proper drive pulse for with a motor. A plurality of duty generating circuits each generates a signal at a duty ratio different from one another. A main drive pulse generating circuit uses an output signal of the plurality of the duty generating circuits to generate a drive pulse, and a motor driver circuit rotates and drives the stepping motor. The stepping motor drives the hands of a display unit. When a rotation detecting circuit detects that the stepping motor is not rotated, a correction drive pulse generating circuit uses a signal from one of the duty generating circuits to output a correction drive pulse, and the motor driver circuit forcedly rotates and drives the motor by the correction drive pulse.

Fig. 1.