

ABSTRACT

STEPPING MOTOR CONTROL CIRCUIT AND ANALOG ELECTRONIC TIMEPIECE

The present invention aims to prevent a nonrotation state from being brought about even when a drive allowance is changed by variations in a stepping motor or the like. A pulse down counter circuit 103 outputs a pulse down control signal DOWN for subjecting a main drive pulse P1 to control pulse down when time is counted for a predetermined time period. When a detecting signal exceeding a reference threshold voltage Vcomp detected by a rotation detecting circuit 109 is detected at a first detection section T1 at start of a rotation detecting time period, a control circuit 104 resets the pulse down counter circuit 103. Thereby, a main drive pulse generating circuit 105 is not subjected to control pulse down by the pulse down counter circuit 103, and therefore, it is prevented that the main drive pulse P1 is subjected to pulse down unnecessarily.

Fig. 1