

ABSTRACT

TEMPERATURE LEVEL DETECTION CIRCUIT

The invention concerns a temperature level detection circuit including means (B1, B2, B3, 11, 12, 21, 31, 32) for generating diode voltages (V_{BE1} to V_{BE5}) and calculating means including capacitive elements (51, 52, 53) and switching means (SW1 to SW4) arranged to connect selectively and sequentially, during first and
5 second phases, the capacitive elements (51, 52, 53) to the means generating said diode voltages (V_{BE1} to V_{BE5}). During the second phase, the calculating means generating a temperature signal representative of the temperature level being greater than or less than a determined temperature threshold (T_{limit}) defined as the temperature value for which the equation $\alpha_1 (V_{BE2} - V_{BE1}) + \alpha_2 (V_{BE3} + \alpha_3 (V_{BE5} - V_{BE4}))$
10 becomes zero, where α_1 , α_2 , and α_3 are first, second and third proportionality coefficients determined by the values of the capacitive elements.

Figure 2