

DATA SAMPLING CIRCUIT AND DATA SAMPLING DEVICE

ABSTRACT

The present disclosure relates to a data sampling circuit and a data sampling device. The sampling circuit includes: a first sampling module configured to respond to a signal from a data signal terminal and a signal from a reference signal terminal and to act on a first node and a second node; a second sampling module configured to respond to a signal from the first node and a signal from the second node and to act on a third node and a fourth node; a latch module configured to, according to a signal from the third node and a signal from the fourth node, input a high level signal to a first output terminal and input a low level signal to a second output terminal, or input the low level signal to the first output terminal and input the high level signal to the second output terminal; and a decision feedback equalization module connected in parallel to the second sampling module and configured to reduce intersymbol interference. The data sampling circuit provided in the present disclosure can reduce intersymbol interference and have lower power consumption.

[Fig. 1]