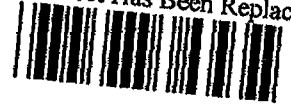


REPLACED SHEETS		SG
☒	Description : paged ...1... to paged ...20...	
☒	Claim(s) : paged ...21... to paged ...31...	
☒	Drawing(s) : paged ...1/2... to paged ...2 1/2...	
☒	Abstract : paged ...42... to paged ...	
DATE OF RECEIPT		

1100124-7 2703

This Sheet Has Been Replaced



ADAPTIVE COMB FILTERS AND METHODS OF
SEPARATING VIDEO SIGNAL COMPONENTS

5 This invention relates to adaptive comb filters and methods of separating video signal components.

Band-pass filters have been commonly utilized for separating luminance and chrominance signals of, for example, an NTSC composite colour video signal. However, such band-pass filters when used alone to effect signal separation are unable to distinguish between
10 chrominance signals and luminance signals falling within the chrominance frequency band, typically $3.58 \text{ MHz} \pm 500 \text{ kHz}$. This effect, commonly referred to as "cross colour", results in deterioration of the quality of an image.

Comb filters provide improved luminance and chrominance
15 signal separation by utilizing the general vertical correlation of video signals and, in the case of an NTSC or PAL composite colour video signal, the phase inversion of the chrominance sub-carrier signal which occurs with every new or every other new horizontal line of the video signal, respectively. A typical comb filter arrangement
20 for separating luminance signals (indicated as "Y") and chrominance signals (indicated as "C") from an NTSC composite colour video signal (indicated as "Y + C") is applied in Figure 1. The composite colour video signal ("Y + C") is applied to an input terminal 101 of the comb filter connected to an input terminal of a delay line (1H) 102 which
25 is operative to provide the composite colour video signal "Y + C" delayed by one horizontal line interval at an output terminal thereof. The "Y + C" signal received at the terminal 101 is also applied to a first input of a subtracting circuit 103 having a second input connected to the output terminal of the delay line 102. Since the
30 luminance signal components of successive horizontal lines are in-phase signals which generally possess vertical correlation, the subtracting circuit 103 serves to attenuate luminance signal components of the delayed composite colour video signal "Y + C" as it is provided at an output terminal thereof.

35 The output terminal of the subtracting circuit 103 is connected to an input of a band-pass filter (BPF) 104 having a pass band corresponding with the frequency range of the chrominance signal



"C" and, accordingly, provides a separated chrominance signal component "C" at an output terminal thereof which is connected to an output terminal 105 of the comb filter. The output terminal of the band-pass filter 104 is also connected to a first input of a further subtracting circuit 107. A delay line 106 has an input connected to the input terminal 101 to receive the composite colour video signal "Y + C" and is operative to delay the "Y + C" signal by an interval corresponding to the delays introduced by the delay line 102 and the band-pass filter 104. The delay line 106 provides the delayed composite colour video signal "Y + C" to a second input of the subtracting circuit 107. Accordingly, the subtracting circuit 107 serves to attenuate chrominance signal components present in the composite colour video signal "Y + C" and thereby provides a separated luminance signal component "Y" to an output terminal 108 of the comb filter.

However, vertical correlation of the chrominance signal is not always present. For example, where a change to a complementary colour occurs in the chrominance signal in a given scanning line, the phase of the chrominance signal becomes inverted, so that vertical correlation thereof with the chrominance signal of the preceding scanning line no longer exists. Not only does this result in the deterioration of the output chrominance signal, but the luminance signal "Y" at the output terminal 108 retains portions of the chrominance signal which otherwise would have been suppressed by the subtracting circuit 107, resulting in so-called "dot interference" in the reproduced video image.

On the other hand, where vertical correlation between luminance signals of successive line intervals is lacking, the chrominance signal from the subtracting circuit 103 possesses a residual component of the luminance signal therein. Accordingly, when this occurs such components may appear at the output of the BPF 104, resulting in a cross colour effect similar to that encountered in the use of a band-pass type signal separation technique of the kind described above.

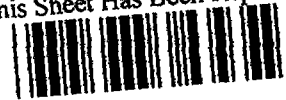
To overcome the effects resulting from vertical non-correlation, it has been proposed in our Japanese Patent Application No. 88/301762 that vertical correlation of luminance signals in the



pass-band of the chrominance signal be detected to carry out adaptive comb filtering techniques to avoid such unwanted effects. Figure 2 illustrates the overall circuit arrangement of the adaptive comb filter disclosed therein.

5 With reference to Figure 2, an NTSC composite colour video signal "Y + C" is supplied to an input terminal 1 thereof. A first delay line (1H) 2 has an input connected to the terminal 1 and is operative to delay the composite colour video signal "Y + C" by one horizontal line interval which it then provides at an output terminal
10 thereof. The output terminal of the delay line 2 is connected to an input of a second delay line (1H) 3 which is operative to delay the composite colour video signal "Y + C" from the output terminal of the delay line 2 by an additional horizontal line interval which it then provides at an output terminal thereof. A first subtracting circuit
15 4 has two inputs each connected with a respective one of the input terminal 1 and the output terminal of the delay line 2 and serves to provide a signal at an output terminal thereof representing the difference between the input composite colour video signal and a prior version thereof delayed by one horizontal line interval. A second
20 subtracting circuit 5 has two inputs each coupled with a respective one of the output terminals of the delay lines 2 and 3 and serves to provide a signal at an output terminal thereof representing the difference between the composite colour video signals respectively delayed by one and two horizontal line intervals with respect to the
25 signal at the input terminal 1.

Each of a plurality of BPFs 6, 7, 8, 9 and 10 has a pass band substantially corresponding to the frequency band of the chrominance signal component of the composite colour video "Y + C". Each of the BPFs 6 to 10 has a respective input correspondingly connected to a
30 respective one of the input terminal 1, the output terminal of the subtracting circuit 4, the output terminal of the delay line 2, the output terminal of the subtracting circuit 5 and the output terminal of the delay line 3. A first adding circuit 11 has a first input connected to an output terminal of the BPF 6 and a second input
35 connected to an output terminal of the BPF 8. A second adding circuit 12 has a first input connected to the output terminal of the band pass filter 8 and a second input connected to an output terminal of the

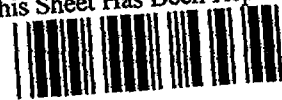


band pass filter 10.

Each of a pair of absolute value generating circuits (ABSs) 13 and 16 is operative to produce absolute value signals at a respective output terminal thereof representing absolute values of signals supplied thereto. The output terminal of the first adding circuit 11 is connected to the input of the ABS 13, while the output terminal of the adding circuit 12 is connected to the input of the ABS 16. Each of a pair of coding circuits 17 and 20 is operative to convert signals received at an input thereof to a respective binary code $\textcircled{X}$ and $\textcircled{Y}$ by comparison of the input signals with a predetermined voltage level. The output terminal of the ABS 13 is connected to the input of the coding circuit 17, while the output terminal of the ABS 16 is connected to the input of the coding circuit 20. Each of the coding circuits 17 and 20 has an output terminal connected to a respective input of a logic signal processor 21.

The BPFs 7 and 9 produce respective output signals $\textcircled{X}$ and $\textcircled{Y}$ which are produced in the same fashion as the output chrominance signal at the output terminal 105 of the comb filter of Figure 1. The output terminal of the BPF 8 is connected to an input of a further BPF 22 having a pass band similar to that of BPFs used alone without comb filtering to separate chrominance signals, as first described above. The BPF 22 produces an output signal Z corresponding to the output chrominance signal produced by the band-pass filtering technique. A switching circuit 24 has a first input terminal 23a connected to the output terminal of the BPF 7 to received the signal $\textcircled{X}$ therefrom, a second input terminal 23b connected to the output terminal of the BPF 9 to received the signal $\textcircled{Y}$ therefrom and a third input terminal 23c connected to an output terminal of the BPF 22 to receive the signal $\textcircled{Z}$ therefrom. The switching circuit 23 is operative to connect an output terminal thereof to one of the input terminals 23a to 23c under the control of the logic signal processor 21.

In addition, the output terminals of the BPFs 7 and 9 are each connected to an input of a respective ABS 14 and 15 which, like circuits 13 and 16, are each operative to produce output signals at an output terminal thereof representing an absolute value of signals input thereto. The output terminals of the ABSs 14 and 15 are each connected to an input of a respective one of third and fourth coding



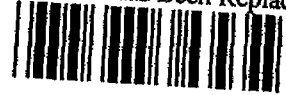
circuits 18 and 19 each of which is operative to convert the absolute value signal received thereby to a respective binary code $\textcircled{X}$ ' and $\textcircled{Y}$ ' by comparing the absolute value signal with a predetermined voltage level. An output terminal of the coding circuit 18 is connected to an input of the logic signal processor 21 to supply the signal $\textcircled{X}$ ' thereto, while the coding circuit 19 has an output terminal connected to an input of the logic signal processor 21 to supply the logic signal $\textcircled{Y}$ ' thereto.

The logic signal processor 21 logically compares the logic signals $\textcircled{X}$, $\textcircled{X}$ ' , $\textcircled{Y}$ ' and $\textcircled{Y}$ in accordance with Table I below such that the switching circuit 23 selects a respective one of the signals $\textcircled{X}$, $\textcircled{Y}$ and $\textcircled{Z}$ in accordance with the signal states indicated in Table I, wherein a dash represents an indefinite state.

TABLE I

$\textcircled{X}$	$\textcircled{Y}$	$\textcircled{X}$ '	$\textcircled{Y}$ '	selection of input signals by switching circuit 23
0	0	-	-	$\textcircled{Y}$
0	1	-	-	$\textcircled{X}$
1	0	-	-	$\textcircled{Y}$
1	1	-	0	$\textcircled{Y}$
1	1	0	1	$\textcircled{X}$
1	1	1	1	$\textcircled{Z}$

The output terminal of the switching circuit 23 is connected to an output terminal of the comb filter circuit to provide a chrominance signal component "C" of the input composite colour video signal "Y + C" thereto. To produce a luminance signal "Y" from the input composite colour video signal "Y + C", a third delay line 25 is provided having an input connected to the output terminal of the delay line 2 and is operative to delay the composite colour video signal at the output terminal of the delay line 2 by an interval corresponding to the signal delay produced by each of the BPFs 7 and 9. An output terminal of the delay line 25 is connected to a first input of a third subtracting circuit 26 having a second input connected to the output

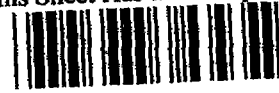


terminal 24. The subtracting circuit 26 is operative to produce the luminance signal "Y" by subtracting the output chrominance signal "C" supplied at the output terminal 24 from the delayed composite colour video signal "Y + C", and supplies the thus produced luminance signal
5 "Y" to a luminance signal output terminal 27 of the comb filter circuit.

With reference to Figure 3, the phases of exemplary output signals provided by the BPFs 6, 8 and 10 are illustrated therein by waveforms a, b and c, respectively, wherein the waveforms of
10 coincident signals are vertically aligned. Phases of other signals as well as logic levels of coded signals appearing at respective terminals of the Figure 2 circuit are also illustrated in Figure 3. In the example of Figure 3, a phase inversion of the chrominance signal representing a change to a complementary colour is represented
15 by a vertical dashed line in each of the waveforms a, b and c. In the example of Figure 3, the luminance signal is constant.

When the signals represented by the waveforms a, b and c are supplied as inputs to the adding circuits 11 and 12, they combine subtractively to cancel one another so long as their phases are
20 inverted. However, upon the occurrence of a colour change as described above, the phases of these signals on successive lines coincide so that they combine additively as shown for the waveforms "a + b" and "b + c" in Figure 3, thereupon resulting in a logic "one" output by the respective coding circuits 17 and 20 represented by the
25 signals $\textcircled{X}$ and $\textcircled{Y}$ in Figure 3. Conversely, the outputs $\textcircled{X}$ and $\textcircled{Y}$ provided respectively by the BPFs 7 and 9 from the outputs of the signal subtracting circuits 4 and 5 are eliminated upon the occurrence of a colour change.

When this takes place, the circuit of Figure 1 eliminates the
30 chrominance signal components of the horizontal line interval in which the colour change occurs, resulting in dot interference. To avoid, this the logic signal processor 21 is operative to control the switch 23 so that it selects the signal $\textcircled{X}$ at such times that $(\textcircled{X}, \textcircled{Y}) = (0, 1)$ indicating that a colour change has occurred between the signals
35 applied to the input terminals of the adding circuit 12, so that a drop-out of the chrominance signal "C" is avoided. It will also be seen from Table I that, upon the occurrence of a colour change between



the signals supplied to the adding circuit 11, resulting in the production of logic signals $(\textcircled{X}, \textcircled{Y}) = (0, 1)$, the signal $\textcircled{Y}$ is selected instead of the signal $\textcircled{X}$, such that chrominance signal drop-out likewise is avoided at such time. Accordingly, a reduction in the level of the chrominance signal "C" due to chrominance signal inversion is thus avoided so that the chrominance signal "C" produced at the output terminal 24 corresponds to the chrominance signal component in the composite colour video signal "Y + C" provided at the output of the delay line 25. Accordingly, a luminance signal "Y" will be provided at the output terminal 27 which is free of dot interference under these circumstances.

Referring now to Figure 4, the phases of further exemplary output signals from BPFs 6, 8 and 10 are illustrated therein by waveforms a, b and c respectively, where a chrominance signal first appears on a given horizontal line of the composite colour video signal "Y + C", while the level of other signals together with logic levels of coded signals appearing at respective terminals of the Figure 2 circuit are also illustrated in Figure 4. Where a composite colour video signal having these characteristics is applied to the comb filter shown in Figure 1, a deterioration in the level of the chrominance signal supplied by the subtracting circuit 103 occurs as shown in the case of the signals $\textcircled{X}$ and $\textcircled{Y}$ of Figure 4. The result is a deterioration in the vertical resolution of the image produced with the use of the signals output by the Figure 1 filter circuit. However, in the case of the adaptive comb filter of Figure 2, the partially attenuated signals $\textcircled{X}$ and $\textcircled{Y}$ are rejected thereby such that vertical resolution is improved under these conditions.

With reference to Figure 5, phases of still further exemplary output signals from the BPFs 6, 8 and 10 are represented by waveforms a, b and c, respectively, thereof to illustrate luminance signal components within the frequency band of the chrominance signal, such that the phases thereof coincide on successive horizontal scan lines. The corresponding amplitudes and states of signals appearing elsewhere in the circuit of Figure 2 simultaneously therewith likewise are illustrated in Figure 5. With reference again to Table I, it will be seen that the presence of such luminance signals at the output of the BPF 8 simultaneously with the presence thereof either at the output of



the BPF 6 or 10 results in a logic signal combination $(X, Y) = (1, 1)$, whereupon the signal Y supplied by the BPF 9 is selected by the switching circuit 23 whenever the signal $Y' = 0$ (to reject the residual luminance signal component concurrently present in the signal X), whereas the signal X is selected by the switching circuit 23 whenever the signal combination $(X', Y') = (0, 1)$ (to reject the residual luminance signal component then present in the signal Y). Accordingly, under the circumstances illustrated in Figure 5, a chrominance signal "C" free of such luminance signal components is produced at the output terminal 24.

Referring now to Figure 6, outputs from the BPFs 6, 8 and 10 are symbolized therein by the waveforms a, b and c, respectively, which represent the existence of a chrominance signal on only one particular scanning line, while the level of the luminance signal is simultaneously constant. The corresponding amplitudes and states of signals appearing elsewhere in the circuit of Figure 2 simultaneously therewith likewise are illustrated in Figure 6. It will be seen from Figure 6 that, whether the signal X or the signal Y is selected when this solitary chrominance signal appears at the output of the BPF 8, a deterioration in the level thereof is unavoidable. Accordingly, under these circumstances, the chrominance signal Z provided at the output terminal of the BPF 22 is selected by the switching circuit 23, as will be seen with reference to Table I under the conditions that X, Y, X' and Y' all equal a logic "one" level indicating that neither the luminance signal nor the chrominance signal possesses vertical correlation. Under these circumstances, the signal Z is provided at the output terminal 24 by the switching circuit 23 and is free of the signal deterioration then present in the signals X and Y .

In summary, the adding circuits 11 and 12 and the signal subtracting circuits 4 and 5 serve respectively to produce signals indicating vertical correlation of the luminance signal and of the chrominance signal, so that signals which strongly correlate may be selected to produce the output chrominance signal "C". On the other hand, when the signals do not correlate satisfactorily, the output from the conventional BPF circuit is instead selected to produce luminance and chrominance signals of high quality.

With reference now to Figure 7, wherein elements

corresponding to those illustrated in Figure 2 bear the same reference numerals, a third adding circuit 28 has a first input connected to the output terminal of the BPF 9 to receive the signal Y therefrom and a second input connected to the output terminal of the BPF 7 to receive the signal X therefrom. The adding circuit 28 is operative to provide an output signal W in accordance with the following relationship:

$$\textcircled{W} = (\textcircled{X} + \textcircled{Y})/2 = [(b-a) + (b-c)]/2 \quad \dots (1)$$

Accordingly, the adding circuit 28 produces a signal $\textcircled{W}$ based on the correlation of three horizontal scan lines and corresponding to the output of a so-called 2H comb filter. An output terminal of the adding circuit 28 is connected to a fourth input 23d of the switching circuit 23.

In the adaptive comb filter of Figure 7, a modified logic signal processor 100 is operative to control the operation of the switching circuit in accordance with the logic signals $\textcircled{X}$, $\textcircled{Y}$, $\textcircled{X}'$ and $\textcircled{Y}'$ input thereto in accordance with Table II below, wherein the logic signal states are expressed in the same manner as in Table I:

TABLE II

20

25

$\textcircled{X}$	$\textcircled{Y}$	$\textcircled{X}'$	$\textcircled{Y}'$	selection of input signals by switching circuit 23
0	0	-	-	$\textcircled{W}$
0	1	-	-	$\textcircled{X}$
1	0	-	-	$\textcircled{Y}$
1	1	-	0	$\textcircled{Y}$
1	1	0	1	$\textcircled{X}$
1	1	1	1	$\textcircled{Z}$

30

With reference to Table II, it will be seen that, when signal correlation is present between the signals appearing on the three successive horizontal scanning lines represented by the waveforms a, b and c, the logic signals $\textcircled{X}$ and $\textcircled{Y}$ assume a low or "0" logic level such that the signal $\textcircled{W}$ at the output of the adding circuit 28 is selected by the switching circuit 23. Under the remaining conditions listed in Table II, the selection of the input signals by the switching circuit 23 corresponds to that of the Figure 2 adaptive comb

35



filter circuit as expressed in Table I.

Referring now to Figure 8, exemplary chrominance signal waveforms a, b and c appearing at the output terminals of BPFs 6, 8 and 10, respectively, are illustrated therein where a complementary colour change resulting in a phase inversion occurs at successively later intervals differing by one-half of the colour sub-carrier wavelength from line to line at the output terminals of the BPFs 6, 8 and 10. The corresponding amplitudes and phases of signals appearing elsewhere in the circuit of Figure 7 therewith simultaneously likewise are illustrated in Figure 8. Under these circumstances, the signal $\textcircled{W}$ is selected except at such times that the signals of two successive lines correspond in phase, whereupon either the signal $\textcircled{X}$ or the signal $\textcircled{Y}$ is selected to avoid the concurrent deterioration in the level of the signal W. Accordingly, in the adaptive comb filter of Figure 7, when the signals a, b and c from successive horizontal scan lines exhibit good correlation, the adaptive comb filter acts as a 2H comb filter which provides superior separation of the chrominance signal. When the chrominance signal derived in this manner is subtractively combined with the composite colour video signal "Y + C" in the subtracting circuit 26, dot interference can be reduced by 6 dB, thus providing improved image quality.

In the adaptive comb filter circuits of Figures 2 and 7, the coding circuits 17 to 20 compare the input absolute value signals with a predetermined voltage level. In the event that the levels of the input signals change, the coding circuits 17 to 20 may be ineffective to encode properly the input signals if the level thereof does not reach the predetermined voltage level. If the predetermined voltage level is reduced in an attempt to enable proper encoding of weaker signals, the probability that noise present in the input signal will interfere with proper operation of the adaptive filter circuit is increased.

According to the present invention there is provided an adaptive comb filter for separating at least one component of a video signal, the filter comprising:

a filter input terminal for receiving said video signal;
first and second signal delay means for delaying signals supplied to respective input terminals thereof and providing respective delayed

input signals at respective output terminals thereof, the input terminal of the first signal delay means being coupled to said filter input terminal and the output terminal thereof being coupled to the input terminal of the second delay means;

- 5 first and second signal subtracting means for each producing output difference signals at a respective output terminal thereof representing a difference between signals supplied to a respective plurality of input terminals thereof, first and second input terminals of the first signal subtracting means each being coupled to a
10 respective one of the input and output terminals of said first signal delay means and first and second input terminals of the second signal subtracting means each being coupled to a respective one of the input and output terminals of said second signal delay means;
- first signal adding means for producing a summation signal at an
15 output terminal thereof representing a summation of signals received at first and second input terminals thereof, said first and second input terminals of said first signal adding means being respectively coupled with said output terminals of said first and second signal subtracting means;
- 20 first switching means for selectively providing one of a plurality of input signals received at respective input terminals thereof to an output terminal thereof, said first switching means having first and second input terminals respectively coupled to said output terminals of said first and second signal subtracting means, a third input
25 terminal coupled to the output terminal of said first signal adding means and a fourth input terminal coupled to the output terminal of said first signal delay means, the output terminal of said switching means being operative to provide a first component of said video signal;
- 30 second and third signal adding means for each producing a summation signal at a respective output terminal thereof representing a summation of signals received at respective first and second input terminals thereof, said first input terminal of each of said second and third signal adding means being coupled to the output terminal of
35 said first signal delay means, said second input terminal of said second signal adding means being coupled to said filter input terminal, and said second input terminal of said third signal adding



means being coupled to the output terminal of said second signal delay means;

first and second absolute value means for each producing an output signal at a respective output terminal thereof representing an absolute value of a signal received at a respective input terminal thereof, the input terminals of said first and second absolute value means being respectively coupled with the output terminals of said second and third signal adding means, respectively; and
controlling means for controlling the selective provision of input signals by said first switching means to its output terminal based upon a relative value of the output signals produced at the output terminals of said first and second absolute value means.

According to the present invention there is also provided an adaptive comb filter for separating at least one component of a video signal, the filter comprising:

a filter input terminal for receiving said video signal;
first delay means for delaying said video signal by a first interval to produce a first delayed video signal;
second delay means for delaying said video signal by a second interval to produce a second delayed video signal;
first difference means for producing a first difference signal proportional to a difference between said video signal and said first delayed video signal;
second difference means for producing a second difference signal proportional to a difference between said first delayed video signal and said second delayed video signal;
first summing means for producing a first summation signal proportional to a summation of said first difference signal and said second difference signal;
first signal selecting means for controllably selecting one of said first difference signal, said second difference signal and said first summation signal as said at least one component of said video signal;
second summing means for producing a second summation signal proportional to a summation of said video signal and said first delayed video signal;
third summing means for producing a third summation signal proportional to a summation of said first delayed video signal and

said second delayed video signal; and
 control means for controlling the selection of one of said first
 difference signal, said second difference signal and said first
 summation signal by said first signal selecting means based upon
 5 relative magnitudes of said second summation signal and said third
 summation signal.

According to the present invention there is also provided a
 method of separating a chrominance signal from a composite colour
 video signal, the method comprising the steps of :
 10 delaying said composite colour video signal by a first interval to
 produce a first delayed video signal;
 delaying said composite colour video signal by a second interval to
 produce a second delayed video signal;
 producing a first difference signal proportional to a difference
 15 between said composite colour video signal and said first delayed
 video signal;
 producing a second difference signal proportional to a difference
 between said first delayed video signal and said second delayed video
 signal;
 20 producing a first summation signal proportional to a summation of said
 first difference signal and said second difference signal;
 producing a second summation signal proportional to a summation of
 said composite colour video signal and said first delayed video
 signal;
 25 producing a third summation signal proportional to a summation of said
 first delayed video signal and said second delayed video signal; and
 selecting one of said first difference signal, said second difference
 signal and said first summation signal as said chrominance signal
 based upon a comparison of relative magnitudes of said second
 30 summation signal and said third summation signal.

In accordance with an aspect of the present invention, an
 adaptive comb filter for separating at least one component of a video
 signal comprises a filter input terminal for receiving said video
 signal; first and second signal delay means for delaying signals
 35 supplied to respective input terminals thereof and providing the
 delayed input signals at respective output terminals thereof, the
 input terminal of the first signal delay means being connected to said





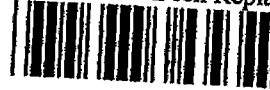
filter input terminal and the output terminal of the first signal delay means being connected to the input terminal of the second signal delay means; first and second signal subtracting means for producing output difference signals at respective output terminals thereof representing differences between signals supplied to respective first and second input terminals thereof, the first and second input terminals of the first signal subtracting means being connected to the input and output terminals, respectively, of the first signal delay means, the first and second input terminals of the second signal subtracting means being connected to the input and output terminals, respectively, of the second signal delay means; first signal adding means for producing a summation signal at an output terminal thereof representing a summation of signals received at first and second input terminals thereof, the first and second input terminals of the first signal adding means being respectively coupled with the output terminals of the first and second signal subtracting means, respectively; switching means for selectively providing one of a plurality of input signals received at respective input terminals thereof to an output terminal thereof, the switching means having first and second input terminals coupled with the output terminals of the first and second signal subtracting means, respectively, a third input terminal connected to the output terminal of the first signal adding means and a fourth input terminal connected to the output terminal of the first signal delay means, the output terminal of the switching means being operative to provide a first component of the video signal; second and third signal adding means for producing summation signals at respective output terminals thereof representing summations of signals received at first and second input terminals of the respective signal adding means, the first input terminals of the second and third signal adding means being connected to the output terminal of the first signal delay means, the second input terminal of the second signal adding means being connected to the filter input terminal, and the second input terminal of the third signal adding means being connected to the output terminal of the second signal delay means; first and second absolute value means for producing output signals at respective output terminals thereof representing absolute values of signals received at respective input terminals

thereof; the input terminals of the first and second absolute value means being connected to the output terminals of the second and third signal adding means, respectively; and controlling means for controlling the selective provision of input signals by the switching means to its output terminal based upon a relative value of the output signals produced at the output terminals of the first and second absolute value means.

In accordance with another aspect of this invention, an adaptive comb filter for separating at least one component of a video signal comprises a filter input terminal for receiving said video signal; first delay means for delaying the video signal by a first interval to produce a first delayed video signal; second delay means for delaying the video signal by a second interval to produce a second delayed video signal; first difference means for producing a first difference signal proportional to a difference between the video signal and first delayed video signal; second difference means for producing a second difference signal proportional to a difference between the first and second delayed video signals; first summing means for producing a first summation signal proportional to a summation of the first and second difference signals; first signal selecting means for controllably selecting one of the first difference signal, the second difference signal and the first summation signal as the at least one component of the video signal; second summing means for producing a second summation signal proportional to a summation of the video signal and the first delayed video signal; third summing means for producing a third summation signal proportional to a summation of the first delayed video signal and the second delayed video signal; and control means for controlling the selection of one of the first difference signal, the second difference signal and the first summation signal by the first signal selecting means based upon relative magnitudes of said second and third summation signals.

In accordance with a further aspect of this invention, a method of separating a chrominance signal from a composite colour video signal comprises the steps of delaying the composite colour video signal by a first interval to produce a first delayed video signal; delaying the composite colour video signal by a second interval to produce a second delayed video signal; producing a first





difference signal proportional to a difference between the composite colour video signal and the first delayed video signal; producing a second difference signal proportional to a summation of the first and second difference signals; producing a first summation signal proportional to a summation of the first and second difference signals; producing a second summation signal proportional to a summation of the composite colour video signal and the first delayed video signal; producing a third summation signal proportional to a summation of the first and second delayed video signals; and selecting one of the first difference signal, the second difference signal and the first summation signal as the chrominance signal based upon a comparison of relative magnitudes of the second and third summation signals.

The invention will now be described by way of example with reference to the accompanying drawings, throughout which like parts are referred to by like references, and in which:

Figures 1, 2 and 7 are circuit block diagrams of examples of previously proposed comb filters for separating luminance and chrominance signal components of an NTSC composite colour video signal;

Figures 3 to 6 and 8 are waveform diagrams referred to in connection with Figures 2 and 7;

Figures 9 to 11 are respective circuit block diagrams of first to third embodiments of adaptive comb filter in accordance with the present invention for separating the luminance and chrominance signal components of an NTSC composite colour video signal;

Figure 12, comprising Figures 12A and 12B, is a circuit block diagram of a fourth embodiment of adaptive comb filter in accordance with the present invention for separating luminance and chrominance signal components of an NTSC composite colour video signal;

Figures 13 and 14 are waveform diagrams referred to in connection with Figure 12;

Figure 15, comprising Figures 15A and 15B, is a circuit block diagram of a fifth embodiment of adaptive comb filter in accordance with the present invention for separating luminance and chrominance signal components of an NTSC composite colour video signal;

Figure 16, comprising Figures 16A and 16B, is a circuit block

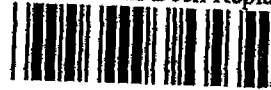


diagram of a sixth embodiment of adaptive comb filter in accordance with the present invention for separating luminance and chrominance signal components of a PAL composite colour video signal;

Figures 17 to 20 are waveform diagrams referred to in connection with Figure 16; and

Figure 21 is a waveform diagram of input colour video signals illustrated in Figure 20.

In the first embodiment of adaptive comb filter, shown in Figure 9, first to fourth low-pass filters (LPFs) 31 to 34 are each operative to attenuate high frequency components of signals supplied at a respective input terminal thereof and having a filter characteristic selected as:

$$(1 + 2Z^{-1} + Z^{-2})/4 \quad \dots (2)$$

where Z represents the sampling cycle. The output terminals of the absolute value generating circuits (ABSs) 13 to 16 are each connected to the input terminal of a respective one of the LPFs 31 to 34. the LPFs 31 to 34 are each operative to produce a respective correlation signal x, X, Y and y at a respective output terminal thereof. Each of the output terminals of the LPFs 31 to 34 is connected to an input of a signal correlation comparator 35. the correlation comparator 35 is operative to compare the signals x, X, Y and y supplied thereto for controlling the selection of the input signals (W), (X), (Y) and (Z) by the switching circuit 23. To do so, the correlation comparator 35 compares the relative magnitudes of the correlation signals x, X, Y, y and determines the selection of the input to the switching circuit 23 in accordance with the relationships of the input correlation signals set forth in the left-hand column of Table III below.

TABLE III

Relationship of input correlation signals	Selection of input signals by switching circuit 23
$y > 2x$	(X)
$y < \frac{1}{2}x$	(Y)
$\frac{1}{2}y \leq x \leq 2y$ and $\frac{1}{2}Y \leq X \leq 2Y$ and $x, y, X, Y \neq 0$ and $X < 2x$ and $Y < 2y$	(Z)
Cases other than those described above	(W)

It has been observed that when $y > 2x$, non-correlation frequently exists between the signals b and c ; accordingly, as indicated in Table III above, the correlation comparator 35 is then operative to control the switching circuit 23 to select the input signal X . Furthermore, it has been observed that when $\frac{1}{2}y \leq x \leq 2y$ and $\frac{1}{2}Y \leq X \leq 2Y$, generally the chrominance signal exists only on one scanning line. In that case, when in addition the correlation signals x, y, X and $Y \neq 0$ and $X < 2x$ and $Y < 2y$ which ensures a sufficient signal level, the signal ② is selected by the switching circuit 23 under the control of the correlation comparator 35. In all other cases, it is found that the correlation of the input signal is sufficiently high, so that the signal ④ is selected.

Since the correlation comparator 35 compares the relative magnitudes of the correlation signals x, X, Y and y for determining whether non-correlation exists therebetween, this determination is unaffected by a change in the absolute levels of the correlation signals, thus enabling the Figure 9 embodiment reliably to separate the luminance and chrominance signal components "Y" and "C" despite such variations in signal levels.

In the adaptive comb filter of the Figure 10 embodiment, ABSs 36, 37 and 38 are each operative to produce signals at a respective output terminal thereof proportional to the absolute value of corresponding signals received at a respective input terminal thereof. The input of the ABS 36 is connected to the output terminal of the ABS 6 to received the signal "a" therefrom and provides an absolute value signal $|a|$ at its output terminal which connected to an input of a signal correlation comparator 110. The input of the ABS 37 is connected to the output of the BPF 8 to received the signal "b" therefrom and provides an absolute value signal $|b|$ at its output terminal which is connected to a further input of the correlation comparator 110. The input of the ABS 38 is connected to the output terminal of the BPF 10 to receive the signal "c" therefrom and provides an output signal $|c|$ at its output terminal which is connected to a further input of the correlation comparator 110. The correlation comparator 110 is operative to control the selection of the input signals ④, ①, ③ and ② by the switching circuit 23 based upon the signals $x, y, |a|, |b|$ and $|c|$ to effect the selection of



the input signals by the switching circuit 23 in accordance with the input correlation signal relationships expressed in the left-hand column of Table IV below.

5

TABLE IV

10	Relationship of input correlation signals		Selection of input signals by switching circuit 23
	$y > 2x$		ⓧ
	$y < \frac{1}{2}x$		Ⓨ
	$\frac{1}{2}y \leq x \leq 2y$	$ a + c < \frac{1}{2} b $	Ⓩ
		In other cases	Ⓦ

As in the case of the Figure 9 embodiment, the adaptive comb filter of Figure 10 is operative to select the signal ⓧ as the output chrominance signal component "C" whenever $y > 2x$, and is operative to select the signal Ⓨ as the output chrominance signal "C" whenever $y < \frac{1}{2}x$. It has been observed that when $\frac{1}{2}y \leq x \leq 2y$ either the signal correlation is higher than a predetermined level or else non-correlation exists. Under these conditions, it has been observed that when $|a| + |c| \leq |b|$, the chrominance signal generally exists only on one scanning line, that is, at the output of the BPF 8. In accordance with this observation, and in order to minimize the effects of signal noise, the correlation comparator 110 is operative to cause the switching circuit 23 to select the input signal Ⓩ when both $\frac{1}{2}y \leq x \leq 2y$ and $|a| + |c| \leq \frac{1}{2}|b|$. In all other cases, when the signal correlation exceeds a predetermined level, that is, when $\frac{1}{2} \leq x \leq 2y$, the signal Ⓦ is selected by the switching 23 circuit under the control of the correlation comparator 110.

As in the case of the Figure 9 embodiment, the adaptive comb filter of Figure 10 is capable of provided reliable separation of luminance and chrominance signal components from the input composite colour video signal despite changes in the absolute levels of the input video signal, since the correlation comparator 110 compares the relationship of the absolute values of the signals a, b and c. In addition, since the selection of the band-pass filtered chrominance signal Ⓩ is conditioned upon the relationship $|a| + |c| \leq \frac{1}{2}|b|$,



the probability that the adaptive comb filter of the Figure 10 embodiment will erroneously select the band-pass filtered signal ② due to noise is substantially reduced. Accordingly, it is unnecessary to provide low-pass filtering circuitry for removing noise from the output signals provided by the absolute value generating circuits 36, 37 and 38 in the Figure 10 embodiment, so that the circuitry thereof can be considerably simplified.

In the embodiment of Figure 11, BPFs 6 to 10 of the Figure 10 embodiment have been eliminated and in place thereof a single BPF 39 is utilized having the same characteristics as those of the BPFs 6 to 10. An input terminal of the BPF 39 is connected to the input terminal 1 and an output terminal thereof is connected to respective inputs of the delay line 2, the subtracting circuit 4 and the adding circuit 11 to supply the band-pass filtered composite colour video signal "a" thereto. Since the luminance signal components of the signal at the output terminal of the delay line 2 have been largely eliminated by the BPF 39, a further delay line 40 operative to delay signals received at an input terminal thereof by one horizontal line period delay of the composite colour video signal "Y + C" provided by the delay line 2 in the preceding embodiments. Accordingly, the input of the delay line 40 is connected to the input terminal 1 to receive the composite colour video signal "Y + C" and an output terminal thereof is connected to the input of the delay line 25. It will be appreciated that the adaptive comb filter of Figure 11 is further simplified with respect to that of Figure 10.

In the embodiment illustrated in Figure 12, BPFs 121, 122 and 123 having the same characteristics of the BPFs, 6 to 10 of the Figure 10 embodiment are provided. An input of the BPF 121 is connected to the input terminal 1 and an output terminal thereof is connected to a respective input terminal of each of the signal subtracting circuit 4, the adding circuit 11 and the ABS 36. The input of the BPF 122 is connected to the output terminal of the delay line 2 and an output terminal thereof is connected to a respective input terminal of each of the subtracting circuits 4 and 5, the adding circuits 11 and 12 and the ABS circuit 37. The input of the BPF 123 is connected to the output terminal of the delay line 3 and an output terminal thereof is connected to respective inputs of each of the signal subtracting



circuit 5, the adding circuit 12 and the ABS 38.

The embodiment of Figures 12 also includes a horizontal correlation detecting logic circuit which is operative to detect horizontal correlation of luminance signal components present in the chrominance signal frequency band of the composite colour video signal "Y + C". The horizontal correlation detecting logic circuit includes an exclusive-OR gate 51 having a first input S1 connected to the output terminal of the adding circuit 11 and a second input S2 connected to the output terminal of the adding circuit 12. An output terminal of the exclusive-OR gate 51 is connected to an input of a one sample cycle delay circuit (D) 52. An output terminal of the delay circuit 52 is connected to an input terminal of an OR gate 53 and the input of the delay circuit 52 is connected to a second input of the OR gate 53. An output terminal of the OR gate 53 is connected to an input of a further delay circuit (D) 54 which also provides a one sampling cycle delay, and an output terminal of the delay circuit 54 is connected to an input of a still further delay circuit (D) 55 providing a further one sampling cycle delay. A NAND gate 56 having three inputs is provided such that its first input is connected to the output terminal of the OR gate 53, its second input is connected to the output terminal of the delay circuit 54 and its third input is connected to the output terminal of the delay circuit 55.

The output terminal of the delay circuit 52 is also connected to an input of another delay circuit 57 which provides a one sampling cycle delay and has an output terminal connected to an input terminal of an exclusive-OR gate 58. The second input of the exclusive-OR gate 58 is connected to the input of the delay circuit 52. Still another delay circuit (D) 59 providing a one sampling cycle delay has an input connected to the output terminal of the exclusive-OR gate 58 and an output terminal connected to an input of another delay circuit (D) 60 providing a further one sampling cycle delay. A three input NOR gate 61 has its first input connected to the output terminal of the exclusive-OR gate 58, its second input connected to the output terminal of the delay circuit 59 and its third input connected to an output terminal of the delay circuit 60. A two input AND gate 62 has its first input connected to the output of the NAND gate 56 to receive an output



signal P therefrom and its second input connected to the output terminal of the NOR gate 61 to receive an output signal Q therefrom.

The embodiment of Figures 12 further includes a signal correlation comparator 120 having inputs each connected with a respective one of the output terminals of the LPFs 31 and 34 and of the ABSs 36, 37 and 38 to receive the signals x , y , $|a|$, $|b|$ and $|c|$, respectively, therefrom. The correlation comparator 120 has a further input connected to an output terminal of the AND gate 62 to receive an output signal α therefrom. The correlation comparator 120 is operative to control the selection of the input signals $\textcircled{W}$, $\textcircled{X}$, $\textcircled{Y}$ and $\textcircled{Z}$ by the switching circuit 23 in accordance with the correlation signal relationships expressed in the left-hand column of Table V below.

TABLE V

Relationship of input correlation signals		Selection of input signals by switching circuit 23
$y > 2x$		$\textcircled{X}$
$y < \frac{1}{2}x$		$\textcircled{Y}$
$\frac{1}{2}y \leq x \leq 2y$	$ a + c < \frac{1}{2} b $ and $\alpha = 1$	$\textcircled{Z}$
	In other cases	$\textcircled{W}$

With reference to Table V, in the embodiment of Figures 12, like that of Figure 10, the correlation comparator 120 is operative to control the switching circuit 23 to select the input signal $\textcircled{X}$ when $y > 2x$, and is operative to control the switching circuit 23 to select the signal $\textcircled{Y}$ when $y < \frac{1}{2}x$. When either the correlation is higher than the predetermined level or non-correlation exists, such that $\frac{1}{2}y \leq x \leq 2y$, the correlation comparator 120 controls the switching circuit 23 to select either the signal $\textcircled{Z}$ or the signal $\textcircled{W}$. As in the case of the Figure 10 embodiment, a condition for the selection of the signal $\textcircled{Z}$ is that $|a| + |c| < \frac{1}{2}|b|$, indicating generally that the chrominance signal exists only at the output of the BPF 122. The second condition for the selection of the signal Z is that $\alpha = 1$ indicating the absence of horizontal correlation, which is explained below in connection with the examples of Figures 13 and 14.

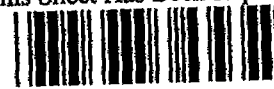


With reference now to Figure 13, waveforms a, b and c represent signals appearing at the output terminals of the BPFs 121, 122 and 123, respectively, to illustrate luminance signal components having a frequency of approximately 3.58 MHz. Under these conditions, the signals a + b (supplied to the input S1 of the exclusive-OR gate 51) and b + c (supplied to the input S2 of the exclusive-OR gate 51) supplied respectively by the adding circuits 11 and 12 will appear as shown in Figure 13. The signals S1 and S2 are sampled at the points indicated by the cross marks superimposed on their illustrated waveforms, so that the output $S1 \oplus S2$ of the exclusive-OR circuit 51 will have the logic values indicated in Figure 13 beneath the waveform S2 in correspondence with each pair of sampling points. Since the output of the exclusive-OR gate 51 alternates between logic "1" and "0" levels with each successive sampling interval, the output of the OR gate 53 remains at a logic "1" level so long as the signals a, b and c continue as shown in Figure 13. Accordingly, the output P of the NAND gate 56 at the same time will remain at a logic "0" level.

Referring to Figure 14, the waveforms a, b and c represent luminance signal components having a frequency of approximately 6 MHz. The signals S1 and S2 corresponding to the outputs a + b and b + c, respectively, produced by the adding circuits 11 and 12 are illustrated in Figure 14 wherein signal sampling points are represented by open circles superimposed on the waveforms of the signals S1 and S2. The S1 and S2 signal waveforms of Figure 14 represent "0" levels at points beneath the horizontal axis thereof and represent logic "1" levels at points on or above the horizontal axis. The logic level output $S1 \oplus S2$ produced by the exclusive-OR gate 51, accordingly, assumes the values indicated beneath the waveform S2 in correspondence with each pair of the sample signals S1 and S2. It will be seen from the illustrated values of $S1 \oplus S2$ as shown in Figure 14 that, so long as luminance signal components having the characteristics represented by the waveforms a, b and c as shown in Figure 14 are provided at the inputs of the adding circuits 11 and 12, the output signal Q from the NOR gate 61 will remain at a logic "0" level.

It will be seen that, under the conditions illustrated in Figures 13 and 14, the output signal supplied by the AND gate 62 is





maintained at a logic "0" level and, with reference to Table V above, the correlation comparator 120 will be operative, under these conditions, to prevent the selection of the signal ②. Stated differently, the signal ② is selected only when middle and high band components of the luminance signal, such as sweep signal components and the like, are not sufficiently large to enable their detection by the horizontal correlation detecting logic circuit. Where, however, the condition $\frac{1}{2}y \leq x \leq 2y$ obtains, and either $|a| + |c| \geq \frac{1}{2}|b|$ or $\angle = 0$, the correlation of the signals is sufficiently high so that the signal ④ is selected by the switch 23 under the control of the correlation comparator 120.

Since the adaptive comb filter of Figure 12 requires that the relative magnitudes of the signals a, b and c satisfy the condition $|a| + |c| < \frac{1}{2}|b|$ before the correlation comparator 120 thereof will control the switching circuit 23 to select the signal ② as the output chrominance signal "C", the probability that the signal ② will be selected erroneously due to noise is substantially reduced. Consequently, it unnecessary to provide means for removing noise from the signals $|a|$, $|b|$ and $|c|$, such as a LPF, so that the adaptive comb filter of Figure 12 can be considerably simplified.

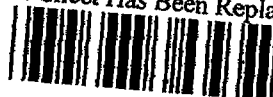
The BPFs 121, 122 and 123 of the adaptive comb filter embodiment of Figures 12 have relatively wide pass bands ensuring that the frequency bands of the luminance and chrominance signal components transmitted thereby will overlap. Accordingly, in order to increase the resolution of the luminance signal "Y" at the output terminal 27 of the adaptive comb filter, the BPF 22 is provided with a relatively narrower pass band than that of BPFs 121 to 123. Presently, the chrominance signal can be limited to a bandwidth of ± 500 KHz while still providing an acceptable image. However, broad band chrominance signals are required for newly developed imaging systems. If the bandwidth of the BPFs 121, 122 and 123 are broadened accordingly, however, the resolution of the output luminance signal "Y" will deteriorate. In addition, the selection of the signal ② by the switch 23 of the adaptive comb filter of Figure 12 results in a cross colour effect which is generally inconspicuous since it occurs relatively infrequently. Nevertheless, it is desirable from an aesthetic viewpoint to provide the greatest possible degree of signal

separation.

Figures 15 shows a fifth embodiment of adaptive comb filter in which the above described shortcomings and disadvantages encountered in the operation of the adaptive comb filter of Figures 12 can be substantially minimized.

In the embodiment of Figure 15, a BPF 71 possessing a desirably narrow pass band for improving the resolution of the luminance signal "Y" has an input connected to the output terminal of the switching circuit 23 and an output terminal connected to the second input of the subtracting circuit 26 to provide the separated chrominance signal thereto for producing the luminance signal "Y". Accordingly, the BPF 22 is unnecessary in the embodiment of Figure 15. A further switching circuit 72 is provided in parallel with the switching circuit 23 and has a first input connected to the output terminal of the subtracting circuit 4 to receive the signal $\textcircled{X}$ therefrom, a second input connected to the output terminal of the subtracting circuit 5 to receive the signal $\textcircled{Y}$ therefrom and a third input connected to the output terminal of the adding circuit 28 to receive the signal $\textcircled{W}$ therefrom. The switching circuit 72 has an output terminal connected to the output terminal 24 of the adaptive comb filter circuit to supply the chrominance signal "C" thereto. The switching circuit 72 is operative under the control of a correlation comparator 130 to select one of the input signals $\textcircled{W}$, $\textcircled{X}$ or $\textcircled{Y}$ to be supplied to the output terminal 24 as the output chrominance signal "C". The correlation comparator 130 operates in the same fashion as the correlation comparator 120 of the embodiment of Figure 12, except that when the correlation comparator 130 controls the switching circuit 23 to select the input signal $\textcircled{Z}$ it simultaneously controls the switching circuit 72 to select the input signal $\textcircled{W}$ to be supplied as the chrominance signal "C" at the output terminal 24.

It will be seen, therefore, that in the embodiment of Figure 15 the pass band of the BPF 71 may be selected to improve the resolution of the luminance signal accordingly without simultaneously narrowing the bandwidth of the chrominance signal at the output terminal 24. Therefore, the pass bands of the BPFs 121, 122 and 123 may then be broadened to accommodate a chrominance signal of broader bandwidth. At the same time, the 2H comb filter output signal $\textcircled{W}$ is



selected by the switching circuit 72 as the output chrominance signal is lieu of the signal ② desirably to suppress the cross colour effect present in the signal ①.

Referring to Figure 16, it is noted initially that the phases of the chrominance signal components of the PAL composite colour video signal differ by 90° from each horizontal line to the next, so that a delay of two horizontal line periods thereof will provide a chrominance signal which is phase inverted with respect to a newly received chrominance signal, provided that the signals exhibit vertical correlation. In the embodiment of Figures 16, a first pair of signal delay circuits 2a and 2b each provides a signal delay substantially equal to one horizontal line interval. An input of the delay circuit 2a is connected to the input terminal 1 and an output terminal thereof is connected to an input of the delay circuit 2b is connected to the input of the delay line 25 as well as to the input of the BPF 122.

A second pair of delay circuits 3a and 3b is provided each serving to delay signals provided at a respective input thereof by one horizontal line interval. An input of the delay circuit 3a is connected to the output terminal of the delay circuit 2b and an output terminal of the delay circuit 3a is connected to an input of the delay circuit 3b. An output terminal of the delay circuit 3b is connected to the input terminal of the BPF 123. Accordingly, the first pair of delay circuits 2a and 2b together serve to delay a PAL composite colour video signal received at the input terminal 1 by two horizontal line intervals. Furthermore, the second pair of delay circuits 3a and 3b is operative to delay the PAL composite colour video signal at the output terminal of the delay circuit 2b by a further two horizontal line intervals, so the signal at the output terminal of the delay circuit 3b is delayed with respect to the input signal by four horizontal line intervals.

A signal correlation comparator 140 of the adaptive comb filter of Figure 16 serves to compare the values x , y , $|a|$, $|b|$ and $|c|$ input thereto to determine the selection of an input signal by each of the switching circuits 23 and 72 in accordance with the relationships set forth in Table VI below:



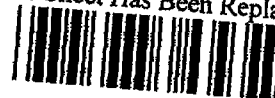
TABLE VI

Relationship of input correlation signals		Selection of input signals by switching circuits 23 and 72
5	$y > 4x$	ⓧ
	$y < \frac{1}{4}x$	Ⓨ
	$\frac{1}{4}y \leq x \leq 4y$	Switch 23 selects signal Ⓣ Switch 72 selects signal Ⓢ
10	In other cases	Ⓢ

With reference to Table VI, it will be seen that the relative ratios employed by the correlation comparator 140 are selected as $\frac{1}{4}$ and 4, instead of $\frac{1}{2}$ and 2, in order more effectively to eliminate cross colour effects in the chrominance signal component of the PAL composite colour video signal.

The polarity of the colour sub-carrier of the PAL composite colour video signal differs from that of the NTSC signal. Since the signal correlation comparator 140 compares absolute values of the signals a, b and c, however, modifications need not be made to the correlation comparator 130 of the embodiment of Figure 15 to adapt it for use as the correlation comparator 140 of the Figure 16 embodiment specifically to accommodate such polarity differences.

With reference now to Figure 17, the amplitudes and phases of exemplary output signals from the BPFs 121, 122 and 123, are represented by the waveforms a, b and c, respectively, in the case where a chrominance signal first appears on a given horizontal line of the PAL composite colour video signal supplied at the input terminal 1, while the level of the luminance component thereof is constant. With the exception of the logic signals ⓧ and Ⓨ (described below), the remaining waveforms illustrated in Figure 17 represent the phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. It will be seen from Figure 17 that when such chrominance signal components are applied to only one input of either of the subtracting circuits 4 and 5, their respective output signals ⓧ or Ⓨ are attenuated with respect to the input signal. Consequently, the selection of either of the signals ⓧ



or $\textcircled{Y}$ without the use of an adaptive comb filtering technique in accordance with the operation of the embodiment of Figure 16 results in a loss of vertical resolution of the image produced with the use of either of the signals $\textcircled{X}$ or $\textcircled{Y}$ alone. However, the adaptive comb filter of Figure 16 advantageously selects the unattenuated one of signals $\textcircled{X}$ or $\textcircled{Y}$ under these circumstance such that vertical resolution is thereby improved. The enhanced separation of the chrominance signal component under these condition likewise results in a reduction of dot interference in the luminance signal component "Y" supplied at the output terminal 27.

The logic levels x and y of Figure 18 represent the signals $\textcircled{X}$ and $\textcircled{Y}$ which would be produced by an adaptive comb filter of the type illustrated Figure 2, but modified to separate components of PAL composite colour video signals, to provide a comparison of the operation of the embodiment of Figure 16 therewith. It will be seen that the Figure 16 embodiment provides the same degree of signal separation under these conditions as that of Figure 2, while providing the ability to operate reliably despite variations in input signal levels.

Referring to Figure 18, the phases of further exemplary output signals from the BPFs 121, 122 and 123 are represented by the waveforms a, b and c therein illustrated. In the example of Figure 18, a phase inversion of the chrominance signal components representing a change to a complementary colour is represented by a vertical dashed line in each of the waveforms a, b and c. The logic levels $\textcircled{X}$ and $\textcircled{Y}$ of Figure 18 represent signals which would be produced in the same fashion therefor as in the case of Figure 17 and the remaining waveforms represent the phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. In the example of Figure 18, the luminance signal component is constant.

When signals having the phases represented by the waveforms a, b and c of Figure 18 are supplied as inputs to the adding circuits 11 and 12, they combine subtractively to cancel one another so long as their phases are inverted. Since the phases of the chrominance signal components of the PAL composite colour video signal differ by 90° from each horizontal line to the next, chrominance signal components



exhibiting good vertical correlation will possess a 180° phase difference when separated by two horizontal line intervals. However, upon the occurrence of a colour change as described above, the phases of these signals when separated by two horizontal line intervals coincide, so that when such signals are applied simultaneously to the inputs of the subtracting circuits 4 and 5, their respective output signals, $\textcircled{X}$ and $\textcircled{Y}$, exhibit signal drop-out. When this occurs, it is detected by the correlation comparator 140 by a comparison of the magnitudes of the signals x and y so that, in accordance with the relationships therebetween expressed in the left-hand column of Table VI, the signal $\textcircled{X}$ or $\textcircled{Y}$ which does not exhibit drop-out is thereupon selected, so that the chrominance signal "C" output by the switch 72 under the control of the correlation comparator 140 likewise is then free of signal drop-out. Since the switching circuit 23 selects the same chrominance signal component under these circumstances, dot interference effects which would otherwise appear in the luminance signal "Y" as a consequence of such chrominance signal drop-outs are avoided.

With reference to Figure 19, the amplitudes and phases of exemplary output signals from the BPFs 121, 122 and 123, are represented by the waveforms a, b and c, respectively, thereof which depict luminance signal components within the frequency band of the chrominance signal, such that the phases thereof coincide on successive horizontal scan lines. The logic levels $\textcircled{X}$ and $\textcircled{Y}$ of Figure 19 represent signals which would be produced in the same fashion therefor as in the case of Figure 17 and the remaining waveforms represent phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. In the example of Figure 19, the luminance signal components appear on four successive scan lines, such that for four horizontal line periods during which the luminance signal components appear at the output terminal of the BPF 122 represented by the signal b in Figure 19, the conditions for the selection of the signal W obtain, so that a chrominance signal "C" as shown in Figure 19 is provided at the output terminal 24. Accordingly, under the circumstances illustrated in Figure 19, the adaptive comb filter of Figure 16 is able to derive a chrominance signal "C" in which cross colour effects produced by



middle and high band components of the luminance signal are minimized, while possessing the ability to operate reliably despite changes in input signal levels.

With reference now to Figure 20, the waveforms a, b and c therein represent the existence of a chrominance signal on only one particular scanning line, while the level of the luminance signal is simultaneously constant. The logic levels (X) and (Y) of Figure 20 represent signals which would be produced in the same fashion therefor as in the case of Figure 17 and the remaining waveforms represent phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. Figure 21 illustrates an exemplary waveform of an input signal giving rise to the chrominance signal component illustrated in Figure 20. It will be seen from Figure 20 that, whether the signal (X) or (Y) is selected when this solitary chrominance signal appears at the output of the BPF 122, a deterioration in the level thereof is unavoidable. Accordingly, the use of either the signal (X) or the signal (Y) for producing the chrominance signal "Y" will necessarily result in dot interference.

With reference to Table VI, it will be seen that, when the chrominance signal appears at the output of the BPF filter 122 as the signal b illustrated in Figure 20, the conditions for the selection of the signal (Z) by the switching circuit 23 are fulfilled, so that a luminance signal "Y" is produced at the output terminal 27 in which dot interference is substantially reduced.

Since embodiments of adaptive comb filter are operative to compare the relative magnitudes of input signal values, they are enabled reliably to determine the optimum mode of filter operation despite variations in input signal levels, while at the same time providing separated signal components of high quality in which cross effects are minimized,

It will be appreciated that the embodiments of adaptive comb filter may be implemented in whole or in part using either analogue or digital circuitry, and that all or part of the signal processing functions thereof may be carried out either by hardwired circuits or with the use of a microprocessor, microcomputer or the like.



CLAIMS

1. An adaptive comb filter for separating at least one component of a video signal, the filter comprising:
- 5 a filter input terminal (1) for receiving said video signal; first and second signal delay means (2, 3) for delaying signals supplied to respective input terminals thereof and providing respective delayed input signals at respective output terminals thereof, the input terminal of the first signal delay means (2) being
- 10 coupled to said filter input terminal (1) and the output terminal thereof being coupled to the input terminal of the second delay means (3);
- first and second signal subtracting means (4, 5) for each producing output difference signals at a respective output terminal thereof
- 15 representing a difference between signals supplied to a respective plurality of input terminals thereof, first and second input terminals of the first signal subtracting means (4) each being coupled to a respective one of the input and output terminals of said first signal delay means (2) and first and second input terminals of the second
- 20 signal subtracting means (5) each being coupled to a respective one of the input and output terminals of said second signal delay means (3); first signal adding means (28) for producing a summation signal at an output terminal thereof representing a summation of signals received at first and second input terminals thereof, said first and second
- 25 input terminals of said first signal adding means (28) being respectively coupled with said output terminals of said first and second signal subtracting means (4, 5);
- first switching means (23) for selectively providing one of a plurality of input signals received at respective input terminals
- 30 thereof to an output terminal thereof, said first switching means (23) having first and second input terminals respectively coupled to said output terminals of said first and second signal subtracting means (4, 5), a third input terminal coupled to the output terminal of said first signal adding means (28) and a fourth input terminal coupled to
- 35 the output terminal of said first signal delay means (2), the output terminal (24) of said switching means (23) being operative to provide a first component of said video signal;





second and third signal adding means (11, 12) for each producing a summation signal at a respective output terminal thereof representing a summation of signals received at respective first and second input terminals thereof, said first input terminal of each of said second and third signal adding means (11, 12) being coupled to the output terminal of said first signal delay means (2), said second input terminal of said second signal adding means (11) being coupled to said filter input terminal (1), and said second input terminal of said third signal adding means (12) being coupled to the output terminal of said second signal delay means (3);

first and second absolute value means (13, 16) for each producing an output signal at a respective output terminal thereof representing an absolute value of a signal received at a respective input terminal thereof, the input terminals of said first and second absolute value means (13, 16) being respectively coupled with the output terminals of said second and third signal adding means (11, 12), respectively; and controlling means (35) for controlling the selective provision of input signals by said first switching means (23) to its output terminal based upon a relative value of the output signals produced at the output terminals of said first and second absolute value means (13, 16).

2. A filter according to claim 1 further comprising first, second and third band-pass filtering means (6, 8, 10) for providing output signals at respective output terminals thereof representing components of input signals received at the input terminals of the respective band-pass filtering means (6, 8, 10), such that components of said input signals outside respective pass bands of said first, second and third band-pass filtering means (6, 8, 10) are attenuated at the respective output terminals thereof, the input terminal of the first band-pass filtering means (6) being coupled to said filter input terminal (1) and the output terminal thereof being coupled to the second input terminal of said second signal adding means (11), the input terminal of said second band-pass filtering means (8) being coupled to the output terminal of said first signal delay means (2) and the output terminal thereof being coupled to the first input terminal of each of said second and third signal adding means (11, 12).





12), and the input terminal of the third band-pass filtering means (10) being coupled to the output terminal of the second signal delay means (3) and the output terminal of said third band-pass filtering means (10) being coupled to the second input terminal of the third signal adding means (12).

3. A filter according to claim 1 further comprising first and second low-pass filtering means (31, 34) for providing output signals at respective output terminals thereof representing components of input signals received at respective input terminals thereof such that high frequency components of said input signals are attenuated at the respective output terminals of said low-pass filtering means (31, 34), the input terminals of said first and second low-pass filtering means (31, 34) being respectively coupled to the output terminals of said first and second absolute value means (13, 16) respectively, and the output terminals of said first and second low-pass filtering means (31, 34) being coupled to said controlling means (35) to provide thereto low-pass filtered versions of the output signals produced at the output terminals of said first and second absolute value means (13, 16).

4. A filter according to claim 1 further comprising third signal delay means (25) for delaying signals supplied to an input terminal thereof and providing the delayed input signals at an output terminal thereof, the input terminal of said third signal delay means (25) being coupled to the filter input terminal (1), and third signal subtracting means (26) for producing output difference signals at an output terminal thereof representing a difference between signals supplied to first and second input terminals thereof, said first input terminal of said third signal subtracting means (26) being coupled to the output terminal of said third signal delay means (25) and said second input terminal of said third signal subtracting means (26) being coupled to the output terminal of said first switching means (23), said third signal subtracting means (26) being operative to provide a second component of said video signal at the output terminal thereof (27).

This Sheet Has Been Replaced



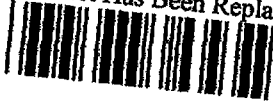


5. A filter according to claim 4 further comprising band-pass filtering means (39) for providing output signals at an output terminal thereof representing components of input signals received at an input terminal of said band-pass filtering means (39) such that
5 components of said input signals outside a pass band of said band-pass filtering means (39) are attenuated at the output terminal thereof, the input terminal of said band-pass filtering means (39) being coupled to the filter input terminal (1) and the output terminal of said band-pass filtering means (39) being coupled to the input
10 terminal of said first signal delay means (2).

6. A filter according to claim 1 wherein said first switching means (23) is operative to provide a chrominance signal component at the output terminal thereof as said first component of said video
15 signal, and further comprising chrominance band-pass filtering means (22) for providing output signals at an output terminal thereof representing components of input signals received at an input terminal of said chrominance band-pass filtering means (22) such that components of said input signals outside of a predetermined pass band
20 for a chrominance signal component of said video signal are attenuated at said output terminal of said chrominance band-pass filtering means (22), the input terminal of said chrominance band-pass filtering means (22) being coupled to the output terminal of said first signal delay means (2), and the fourth input terminal of said first switching means
25 (23) being coupled to the output terminal of said chrominance band-pass filtering means (22) such that the output terminal of said first signal delay means (2) is coupled to said fourth input terminal of said first switching means (23) through said chrominance band-pass filtering means (22).

7. A filter according to claim 6 wherein said controlling means (35) includes correlation detecting means for detecting horizontal correlation of luminance signal components in the summation signals produced at the output terminals of said second and third signal
35 adding means (11, 12) and wherein said controlling means (35) is operative to control said first switching means (23) to inhibit selection thereby of input signals from said chrominance band-pass





filtering means (22) upon detection of said horizontal correlation of luminance signal components in said summation signals.

8. A filter according to claim 1 wherein said first switching means (23) is operative to provide a chrominance signal component at the output terminal thereof as said first component of said video signal, said adaptive comb filter further comprising luminance separation means for separating a luminance component of said video signal, said luminance separation means including:

10 third signal delay means (25) for delaying said video signal by an interval substantially equal to a signal delay between said video signal and said chrominance signal component provided at the output terminal of said first switching means (23) to provide a delayed video signal at an output terminal of said third signal delay means (25);

15 and

signal subtraction means (26) for attenuating chrominance signal components in said delayed video signal by subtractive combination therewith of said chrominance signal component provided at the output terminal of said first switching means (23) to provide a separated

20 luminance signal component of said video signal at an output terminal (27) of said signal subtraction means (26).

9. A filter according to claim 8 further comprising second switching means (72) for selectively providing one of a plurality of

25 input signals received at respective input terminals thereof to an output terminal (24) of said second switching means (72), said second switching means (72) having first and second input terminals coupled to the output terminals of said first and second signal subtracting means (4, 5), respectively, and a third input terminal coupled to the

30 output terminal of said first signal adding means (28), said second switching means (72) being operative to provide a chrominance signal component of said video signal at the output terminal (24) thereof; and

wherein said controlling means (130) is operative to control said

35 second switching means (72) to select an input signal received at the third input terminal thereof as said chrominance signal component of said video signal when said controlling means (130) controls said

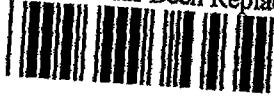


first switching means (23) to select an input signal received at the fourth input terminal thereof.

10. A filter according to claim 1 wherein said controlling means (130) is operative to control the selective provision of input signals by said first switching means (23) to its output terminal based upon a comparison of the magnitudes of the output signals produced at the output terminals of said first and second absolute value means (13, 16).

10

11. An adaptive comb filter for separating at least one component of a video signal, the filter comprising:
a filter input terminal (1) for receiving said video signal;
first delay means (2) for delaying said video signal by a first interval to produce a first delayed video signal;
15 second delay means (3) for delaying said video signal by a second interval to produce a second delayed video signal;
first difference means (4) for producing a first difference signal proportional to a difference between said video signal and said first delayed video signal;
20 second difference means (5) for producing a second difference signal proportional to a difference between said first delayed video signal and said second delayed video signal;
first summing means (28) for producing a first summation signal proportional to a summation of said first difference signal and said second difference signal;
25 first signal selecting means (23) for controllably selecting one of said first difference signal, said second difference signal and said first summation signal as said at least one component of said video signal;
30 second summing means (11) for producing a second summation signal proportional to a summation of said video signal and said first delayed video signal;
third summing means (12) for producing a third summation signal proportional to a summation of said first delayed video signal and said second delayed video signal; and
35 control means (35) for controlling the selection of one of said first



difference signal, said second difference signal and said first summation signal by said first signal selecting means (23) based upon relative magnitudes of said second summation signal and said third summation signal.

5

12. A filter according to claim 11 wherein said control means (35) further comprises absolute value means (13, 16) for producing first and second absolute value signals proportional to the absolute values of the magnitudes of said second summation signal and said
10 third summation signal, respectively, and wherein said control means (35) is operative to compare the magnitudes of said first and second absolute value signals for controlling the selection of said at least one component of said video signal.

15 13. A filter according to claim 12 further comprising low-pass filtering means (31, 34) for attenuating high frequency components of said first and second absolute value signals.

14. A filter according to claim 11 wherein said control means
20 (35) is operative to control the selection of said at least one component of said video signal based upon relative magnitudes of said first difference signal and said second difference signal.

15. A filter according to claim 11 wherein said control means
25 (35) is operative to control the selection of said at least one component of said video signal based upon relative magnitudes of said video signal, said first delayed video signal and said second delayed video signal.

30 16. A filter according to claim 11 wherein said first signal selecting means (23) is operative to provide a chrominance signal as said at least one component of said video signal.

17. A filter according to claim 16 wherein said first delay means
35 (21) is operative to delay said video signal by an interval substantially equal to one horizontal line interval of said video signal, and said second delay means (3) is operative to delay said



video signal by an interval substantially equal to two horizontal line intervals of said video signal.

18. A filter according to claim 16 wherein said first delay means
5 (2) is operative to delay said video signal by an interval substantially equal to two horizontal line intervals of said video signal, and said second delay means (3) is operative to delay said video signal by an interval substantially equal to four horizontal line intervals of said video signal.

10

19. A filter according to claim 16 further comprising first band-pass filtering means (8) for filtering said first delayed video signal to attenuate components thereof outside a first chrominance signal pass band to produce a band-pass filtered signal, and wherein said
15 first signal selecting means (23) is operative to select one of said band-pass filtered signal, said first difference signal, said second difference signal and said first summation signal as said chrominance signal.

20. A filter according to claim 19 further comprising luminance separation means for separating a luminance component of said video signal, said luminance separation means including:
second band-pass filtering means (39) for filtering said chrominance signal to attenuate components thereof outside a second chrominance
25 signal pass band to produce a band-pass filtered chrominance signal, said second chrominance signal pass band being relatively narrower than said first chrominance signal pass band of said first band-pass filtering means (6);
third delay means (25) for delaying said video signal by a third
30 interval substantially equal to a signal delay between said video signal and said band-pass filtered chrominance signal to produce a delayed video signal; and
subtractive combining means (26) for producing said luminance component by attenuating chrominance signal components of said delayed
35 video signal with the use of said band-pass filtered chrominance signal.





21. A filter according to claim 16 further comprising luminance separation means for separating a luminance component of said video signal, said luminance separation means including:
third delay means (25) for delaying said video signal by a delay interval substantially equal to a signal delay between said video signal and said chrominance signal to produce a third delayed video signal; and
subtractive combining means (26) for producing said luminance component by attenuating chrominance signal components of said third delayed video signal with the use of the chrominance signal provided by said first signal selecting means (23).
22. A filter according to claim 21 wherein said third delay means (25) is operative further to delay said first delayed video signal to provide said third delayed video signal.
23. A filter according to claim 21 further comprising first band-pass filtering means (22) for filtering said video signal received at said filter input terminal (1) to attenuate components thereof outside a chrominance signal pass band to produce a band-pass filtered signal, said first band-pass filtering means (22) being coupled to said first delay means (2), said second delay means (3) and said first difference means (4) to provide said band-pass filtered signal thereto as said video signal, and wherein said third delay means (25) is coupled to said filter input terminal to receive said video signal therefrom.
24. A filter according to claim 19 wherein said control means (35) includes correlation detecting means for detecting horizontal correlation of luminance signal components in said second summation signal and said third summation signal, and wherein said control means (35) is operative to control said first signal selecting means to inhibit selection of said band-pass filtered signal upon detection of said horizontal correlation of luminance signal components.
25. A filter according to claim 16 further comprising first, second and third band-pass filtering means (121, 122, 123) for attenuating components of said video signal, said first delayed video



signal and said second delayed video signal, respectively, which are outside a chrominance signal pass band, the first band-pass filtering means (121) having an input terminal coupled to said filter input terminal (1) and an output terminal coupled with said difference means (4) and said second summing means to provide the band-pass filtered video signal thereto, the second band-pass filtering means (122) having an input terminal couple to said first delay means (21) and an output terminal coupled to said first difference means (4), said second difference means (5), said second summing means (11) and said third summing means (12) to provide the band-pass filtered first delayed video signal thereto, and the third band-pass filtering means (123) having an input terminal coupled to said second delay means (3) and an output terminal coupled to said second difference means (5) and said third summing means (121) to provide the band-pass filtered second delayed video signal thereto.

26. A filter according to claim 21 wherein said first signal selecting means (23) is operative to select one of said first delayed video signal, said first difference signal, said second difference signal and said first summation signal as said chrominance signal, said adaptive comb filter further comprising second signal selecting means (72) for controllably selecting one of said first difference signal, said second difference signal and said first summation signal as an output chrominance signal to be supplied by said adaptive comb filter; and wherein said control means (130) is operative to control said second signal selecting means (72) to select said first summation signal as said output chrominance signal when said control means (130) controls said first signal selecting means (23) to select said first delayed video signal.

27. A filter according to claim 26 further comprising band-pass filtering means (71) for attenuating components of said chrominance signal provided by said first signal selecting means (23) outside of a chrominance signal pass band and coupled to said subtractive combining means (26) to supply the band-pass filtered chrominance signal thereto.





28. A method of separating a chrominance signal from a composite colour video signal, the method comprising the steps of :
- delaying said composite colour video signal by a first interval to produce a first delayed video signal;
- 5 delaying said composite colour video signal by a second interval to produce a second delayed video signal;
- producing a first difference signal proportional to a difference between said composite colour video signal and said first delayed video signal;
- 10 producing a second difference signal proportional to a difference between said first delayed video signal and said second delayed video signal;
- producing a first summation signal proportional to a summation of said first difference signal and said second difference signal;
- 15 producing a second summation signal proportional to a summation of said composite colour video signal and said first delayed video signal;
- producing a third summation signal proportional to a summation of said first delayed video signal and said second delayed video signal; and
- 20 selecting one of said first difference signal, said second difference signal and said first summation signal as said chrominance signal based upon a comparison of relative magnitudes of said second summation signal and said third summation signal.



ABSTRACTADAPTIVE COMB FILTERS AND METHODS OF
SEPARATING VIDEO SIGNAL COMPONENTS

5

An adaptive comb filter for separating at least one component of a video signal includes a filter input terminal (1) for receiving the video signal, a first delay device (2) for delaying the video signal by a first interval to produce a first delayed video signal, a
10 second delay device (3) for delaying the video signal by a second interval to produce a second delayed video signal, a first signal subtractor (4) for producing a first difference signal proportional to a difference between the video signal and the first delayed video signal, a second subtractor (5) for producing a second difference
15 signal proportional to a difference between the first delayed video signal and the second delayed video signal, a first signal summing device (28) for producing a first summation signal proportional to a summation of the first difference signal and the second difference signal, a signal selecting device (23) for controllably selecting one
20 of the first difference signal, the second difference signal and the first summation signal as the separated component of the video signal, a second summing device (11) for producing a second summation signal proportional to a summation of the video signal and the first delayed video signal, a third summing device (12) for producing a third
25 summation signal proportional to a summation of the first delayed video signal and the second delayed video signal, and a controller (35) for controlling the selection of the separated component of the video signal by the signal selecting device based upon relative magnitudes of the second summation signal and the third summation
30 signal. In addition a method of separating a chrominance signal from a composite colour video signal is provided.

This Sheet Has Been Replaced



FIG. 1

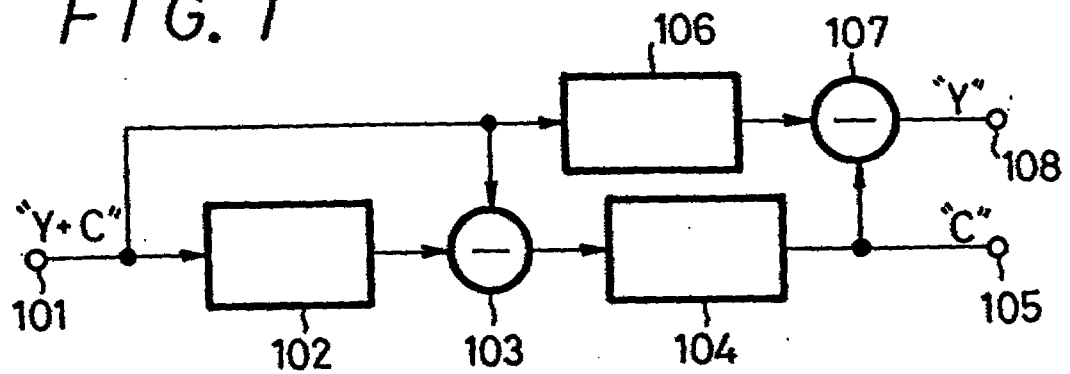


FIG. 2

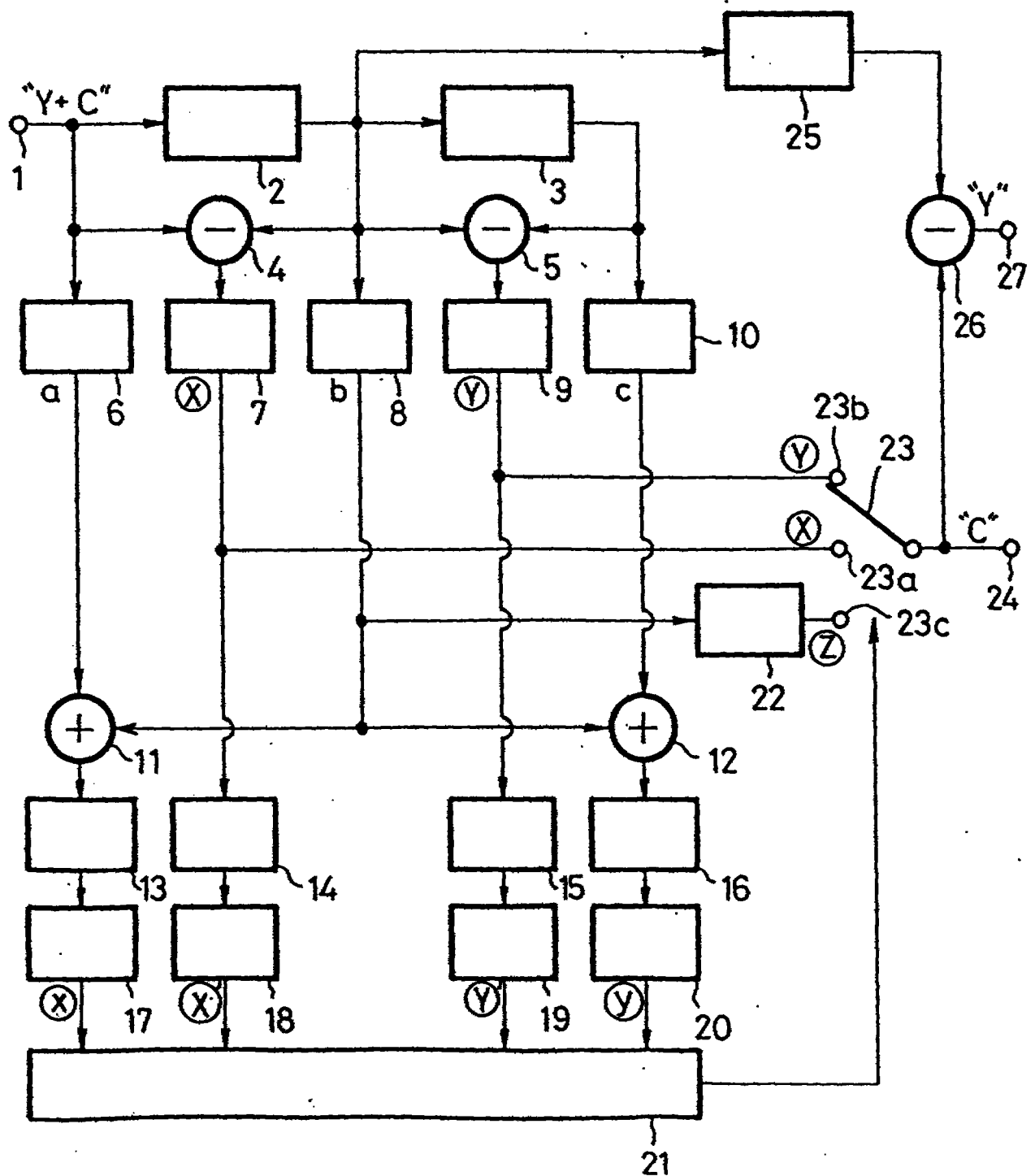
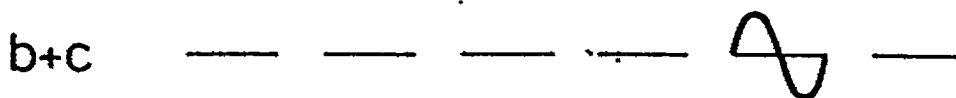
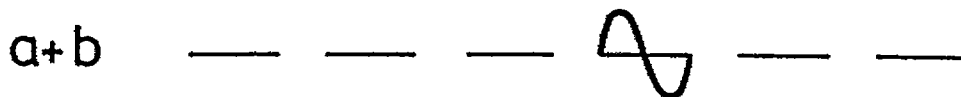
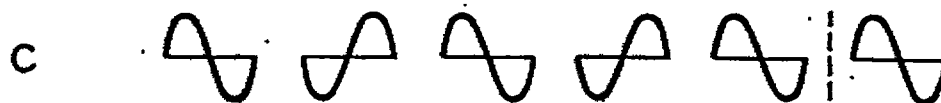
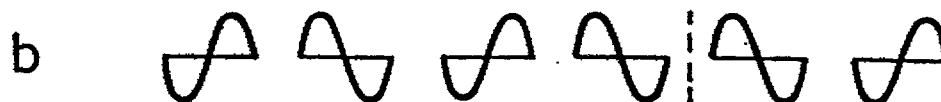


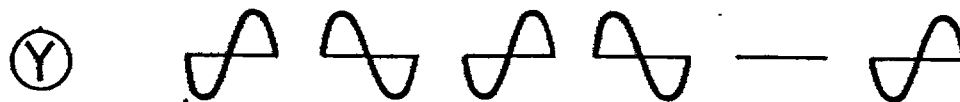
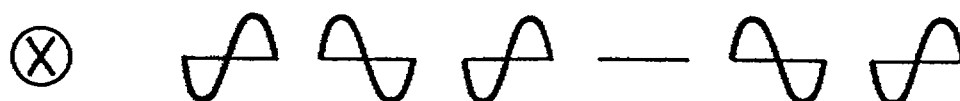


FIG. 3



(X)	0	0	0	1	0	0
-----	---	---	---	---	---	---

(Y)	0	0	0	0	1	0
-----	---	---	---	---	---	---



(X')	1	1	1	0	1	1
------	---	---	---	---	---	---

(Y')	1	1	1	1	0	1
------	---	---	---	---	---	---

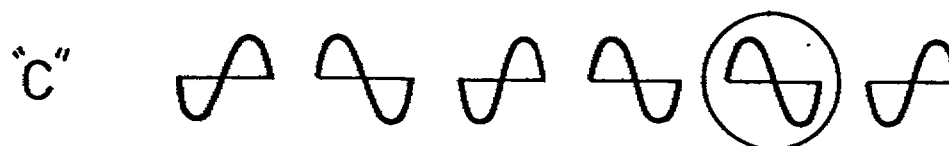


FIG. 4

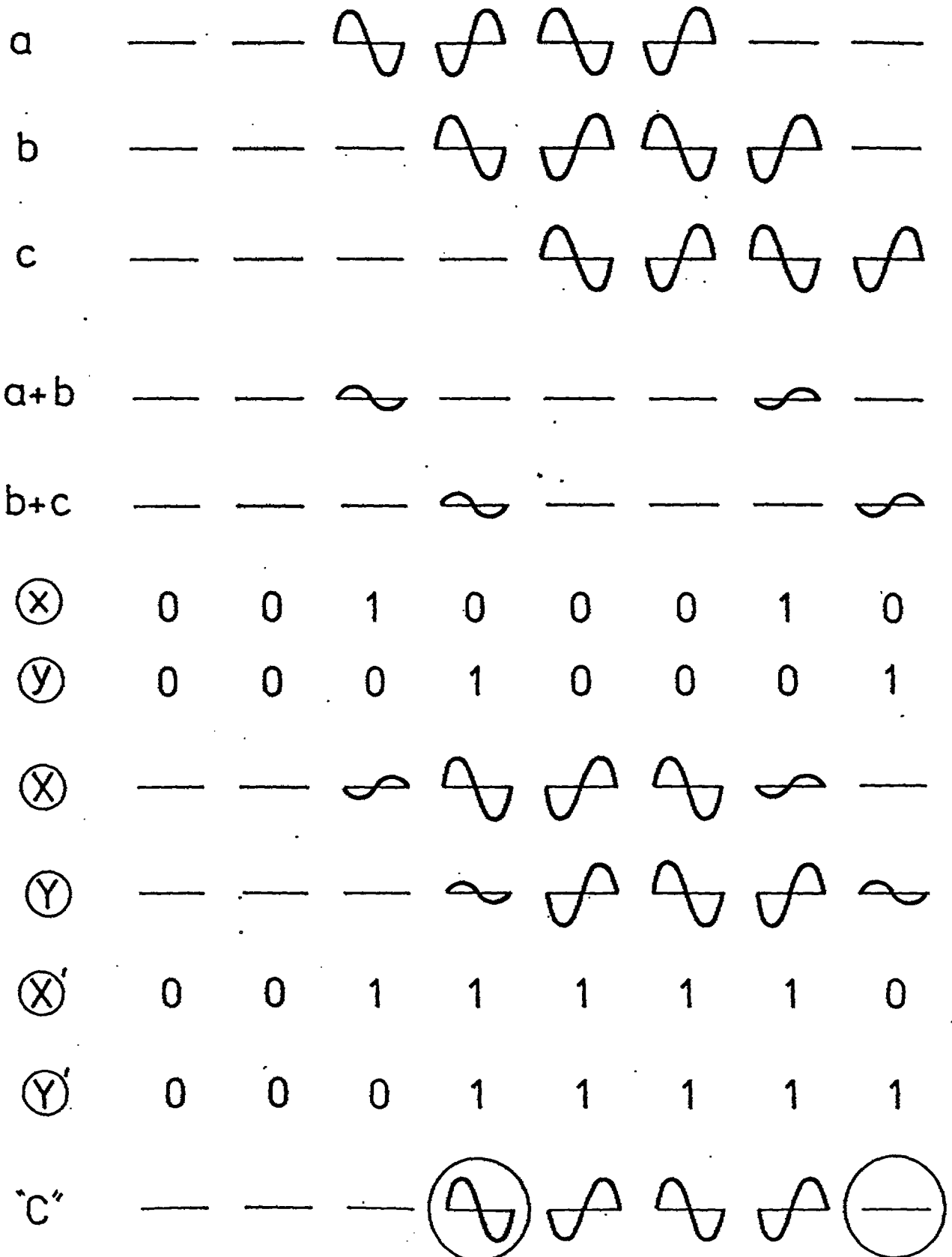


FIG. 5

This Sheet Has Been Replaced

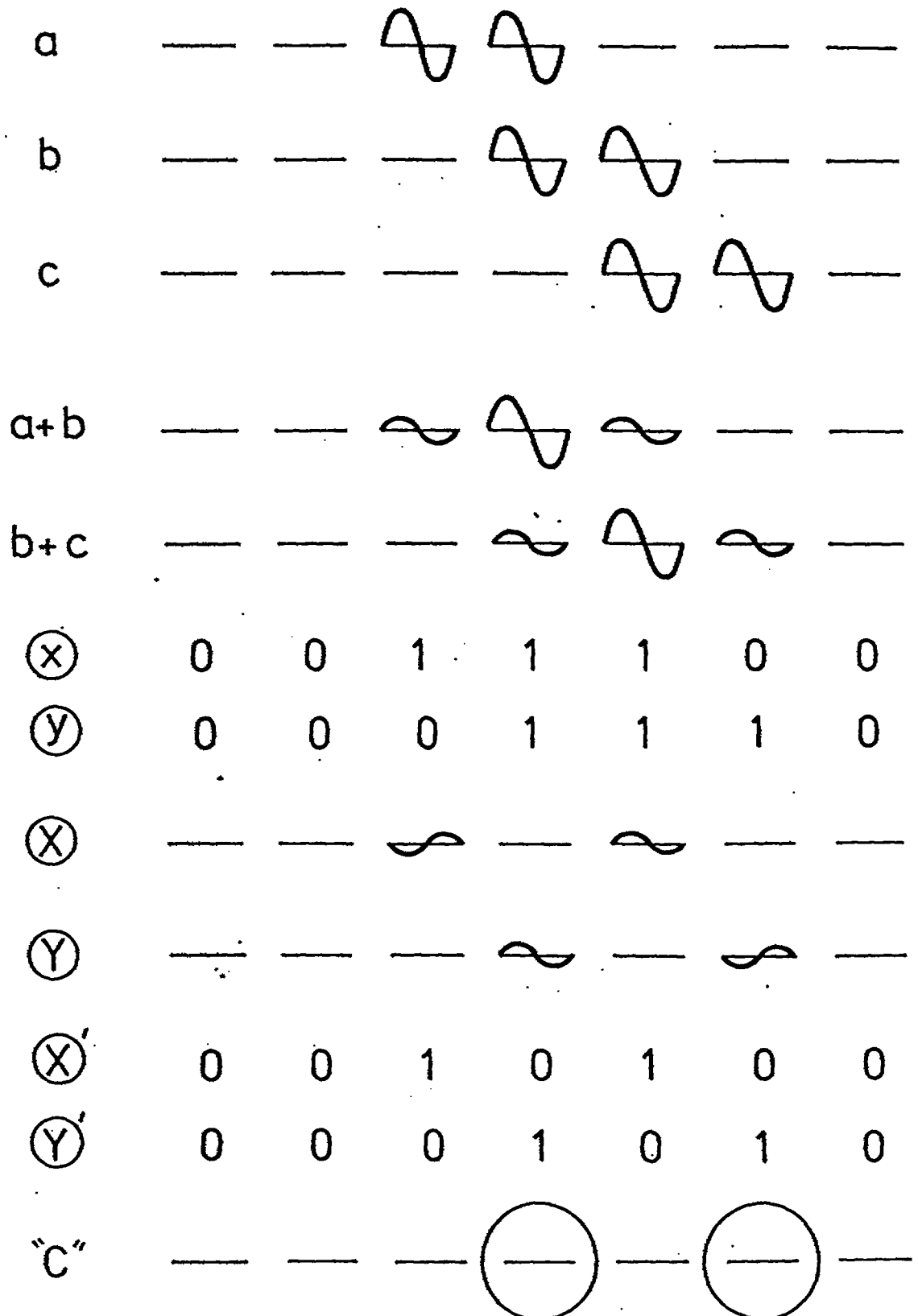




FIG. 6

a —  — — —

b — —  — —

c — — —  —

a + b —   — —

b + c — —   —

(X) 0 1 1 0 0

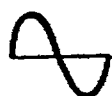
(Y) 0 0 1 1 1

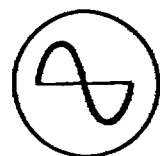
(X) —   — —

(Y) — —   —

(X)' 0 1 1 0 0

(Y)' 0 0 1 1 0

(Z) — —  — —

"C" — —  — —

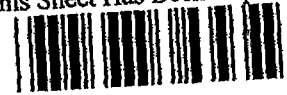
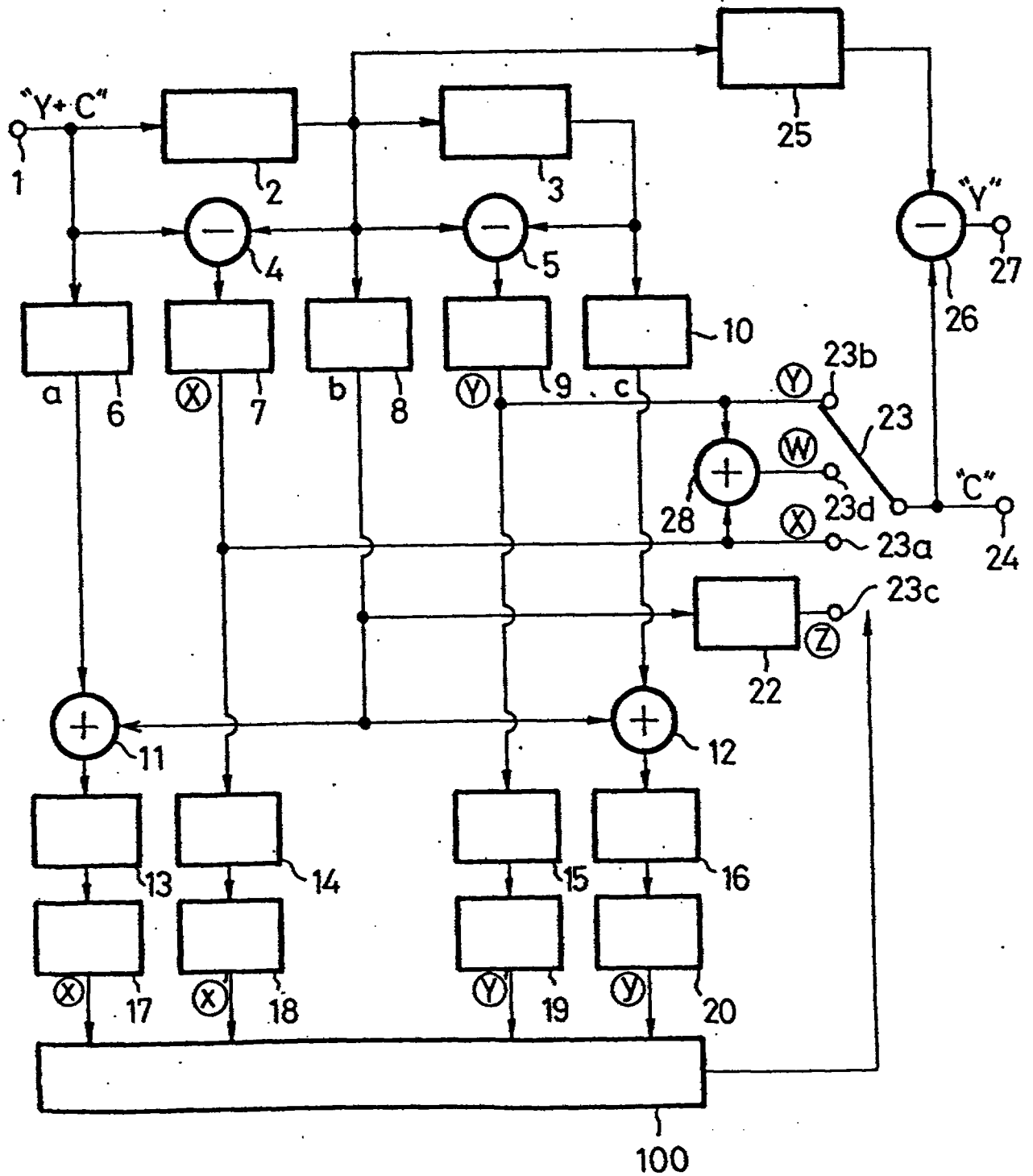


FIG. 7



This Sheet Has Been Replaced

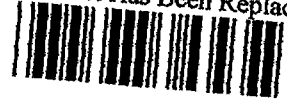
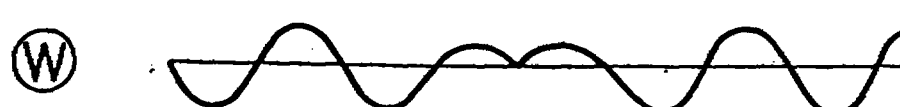
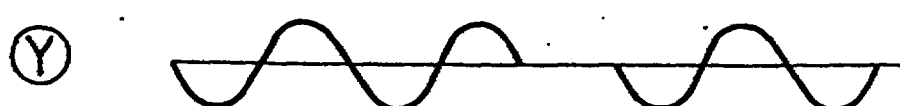
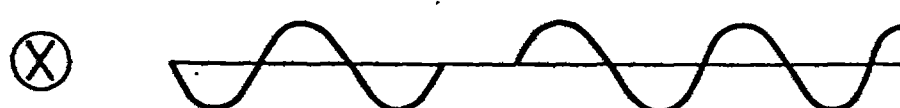
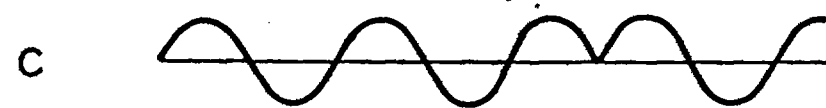
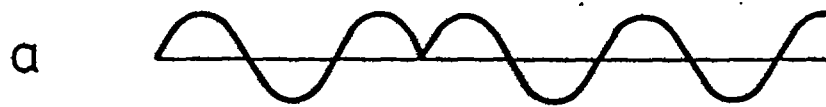
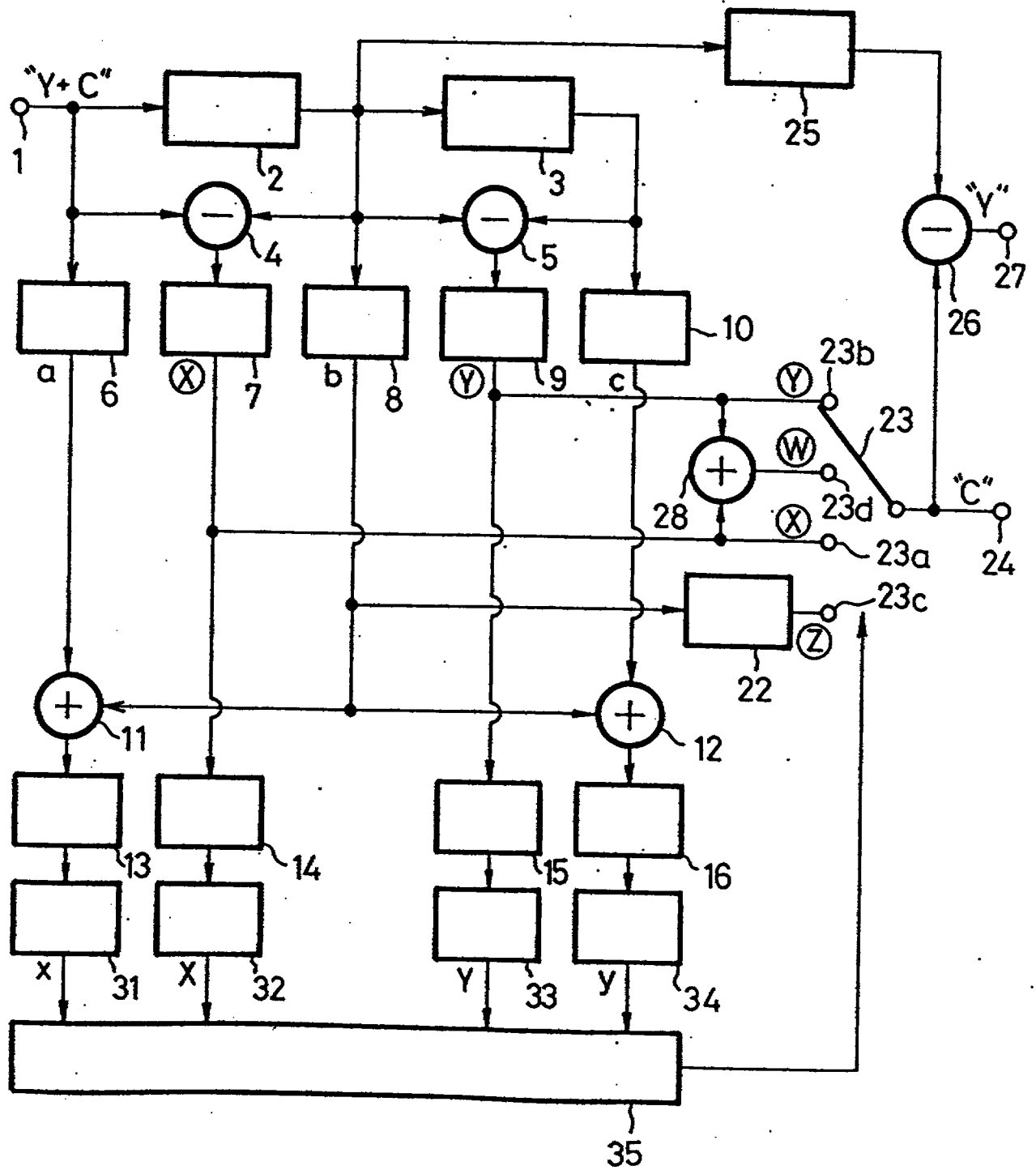


FIG. 8



This Sheet Has Been Replaced

FIG. 9



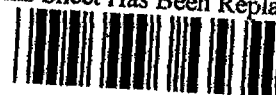
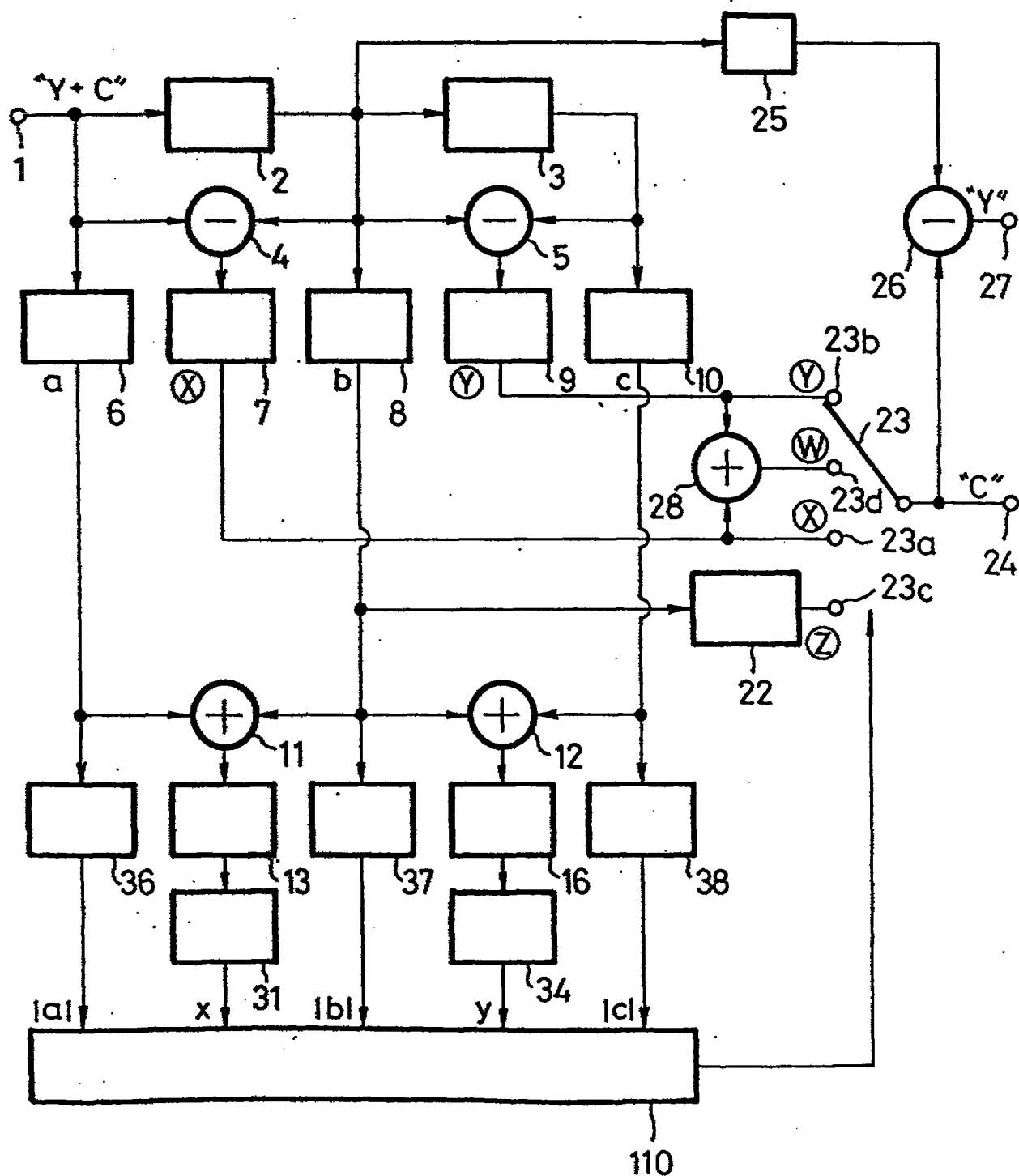


FIG. 10



This Sheet Has Been Replaced

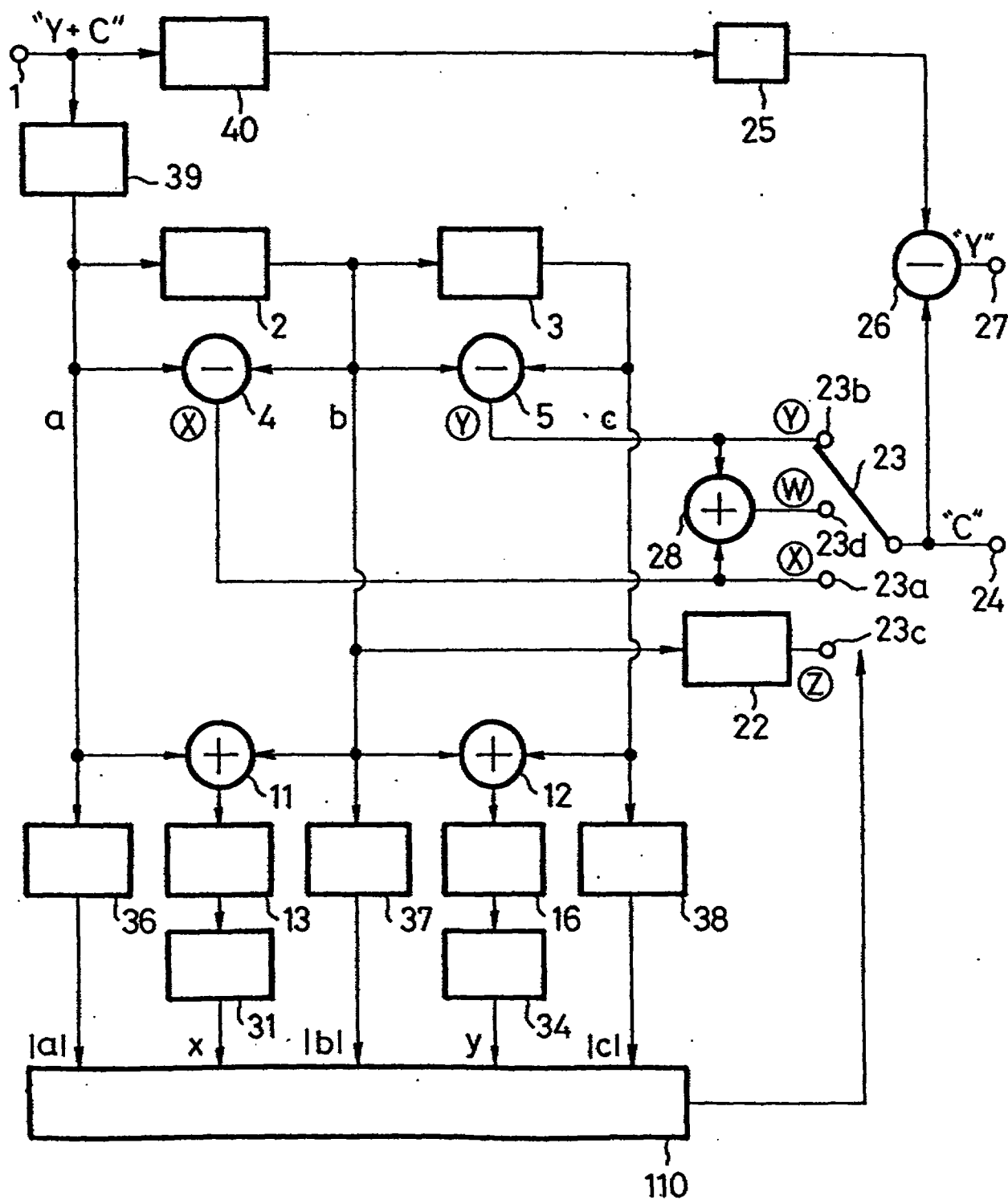


FIG. 12A

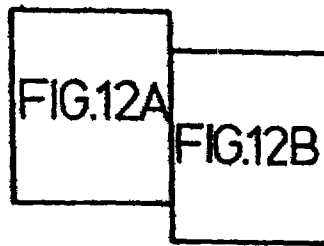
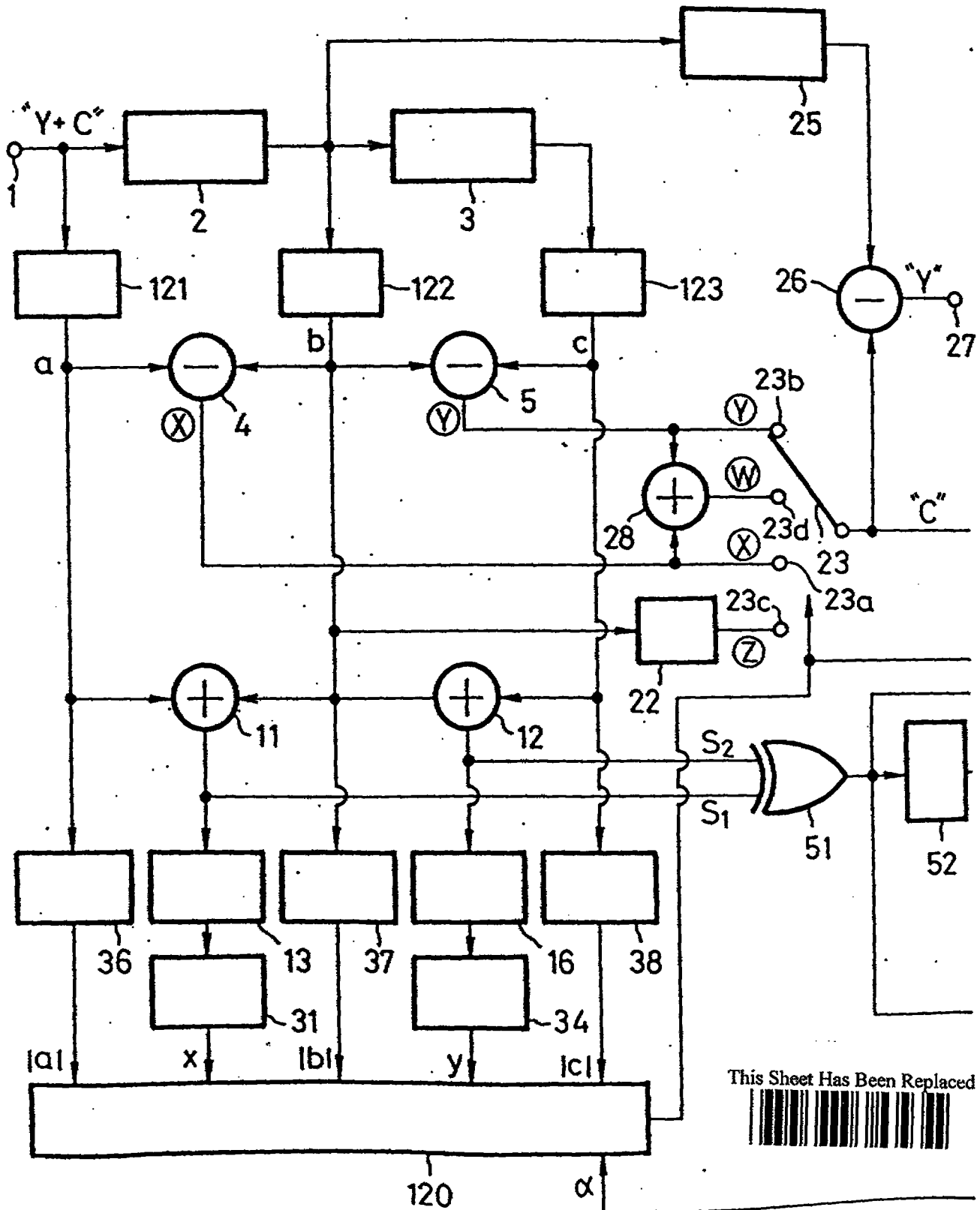


FIG. 12



This Sheet Has Been Replaced



FIG. 12B

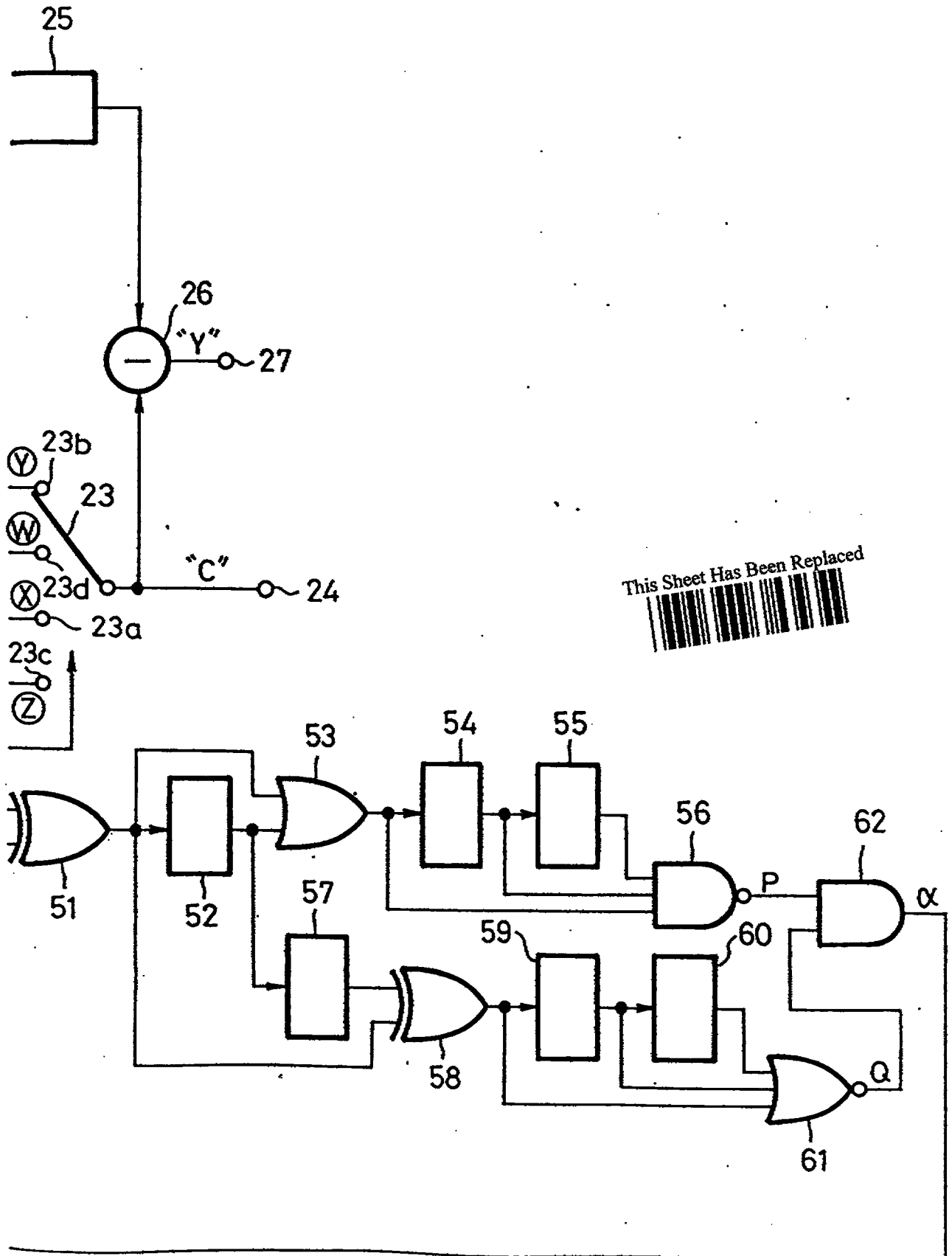
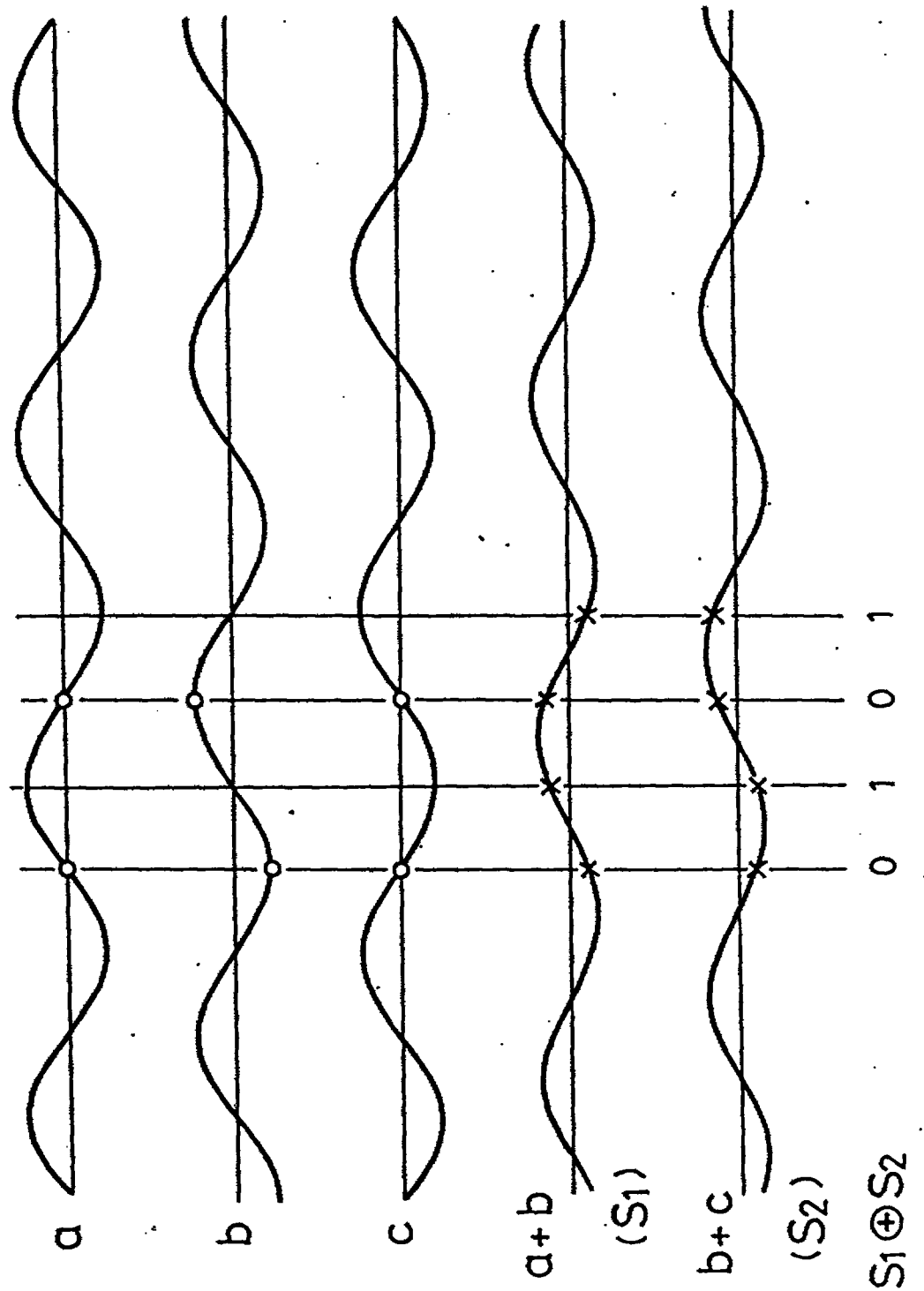




FIG. 13



This Sheet Has Been Replaced



FIG. 14

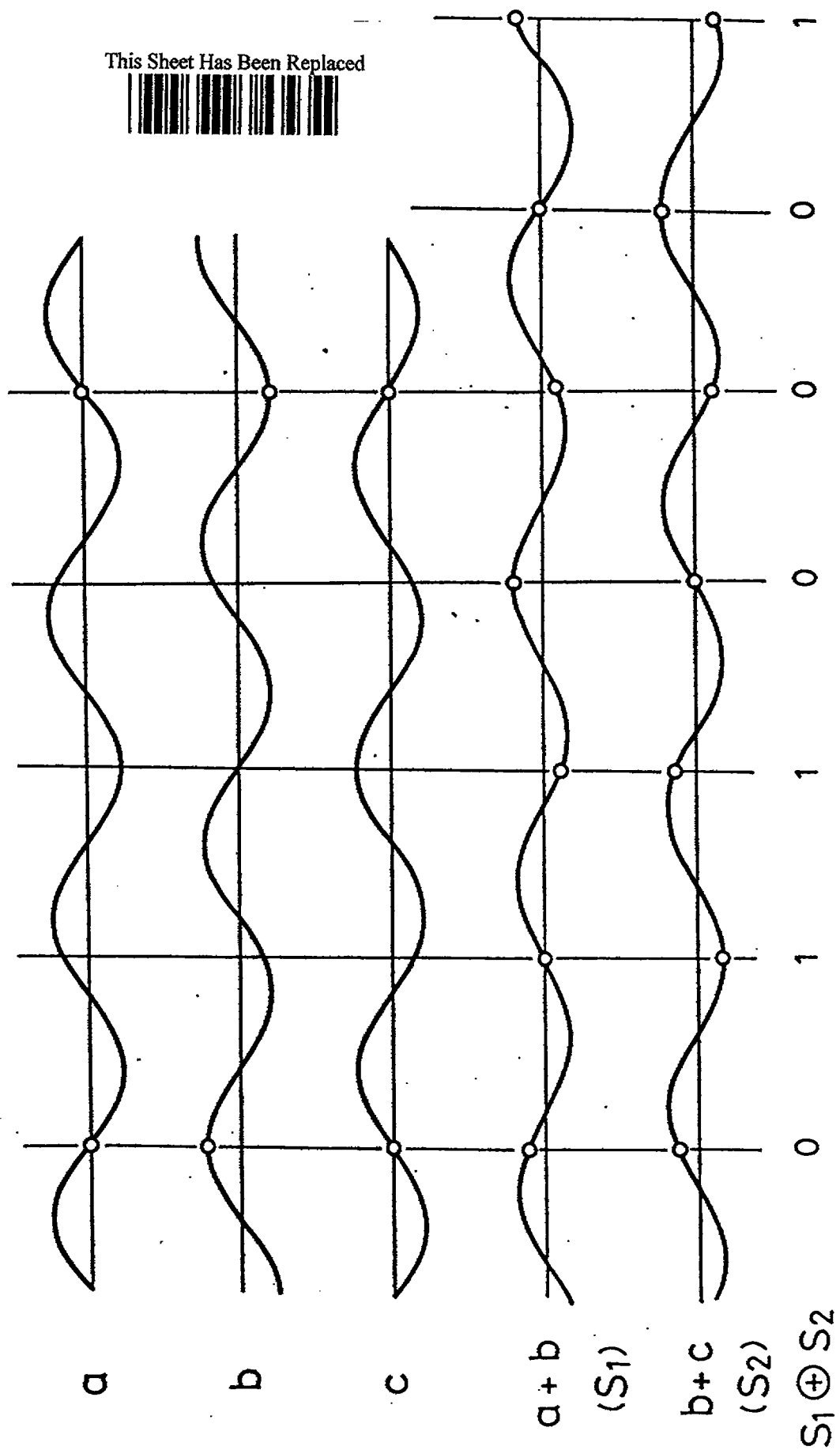
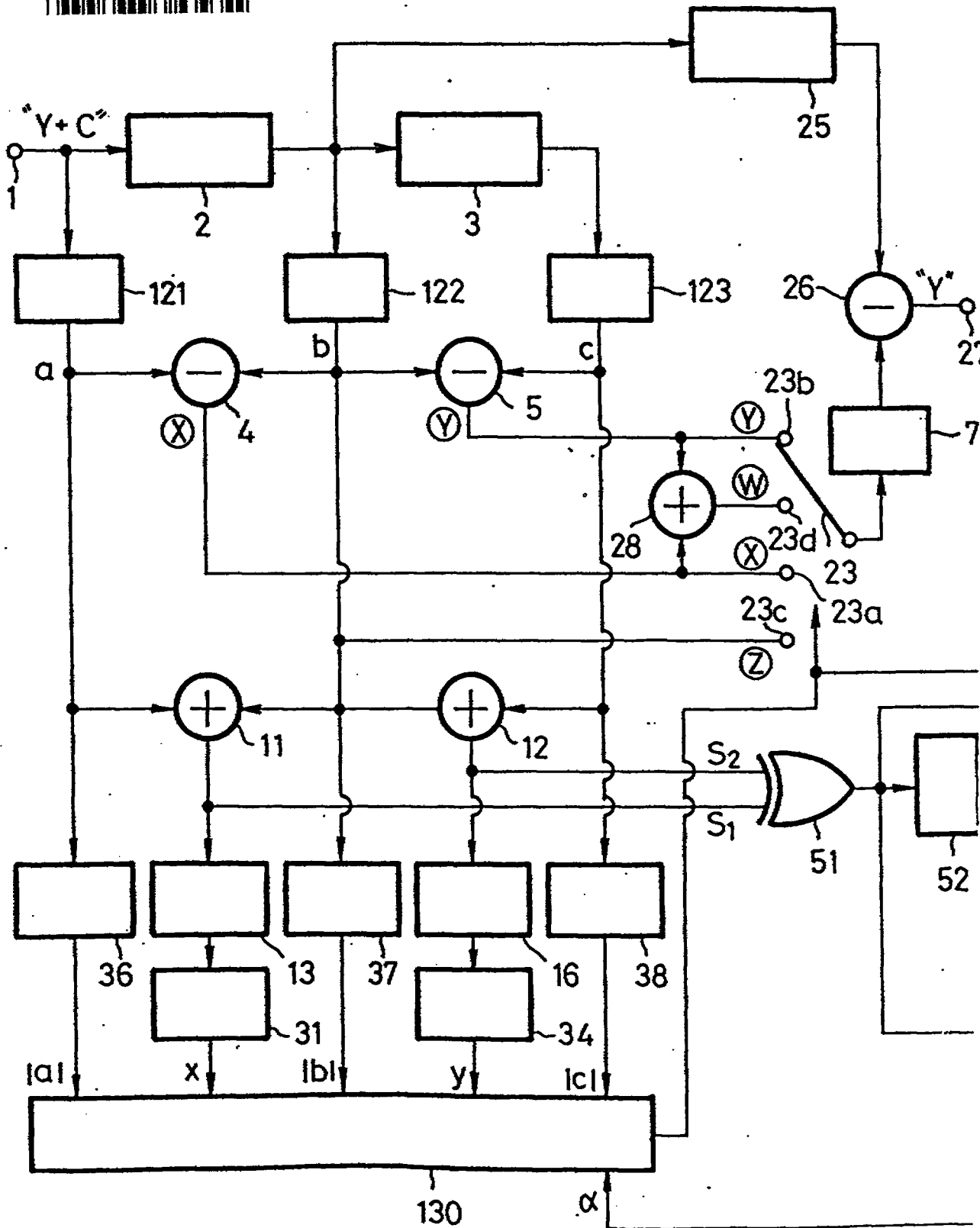
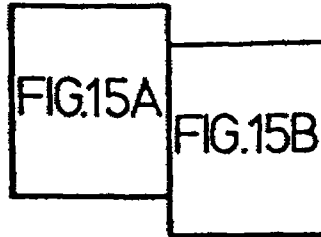


FIG. 15A

FIG. 15

This Sheet Has Been Replaced



This Sheet Has Been Replaced



FIG. 16A

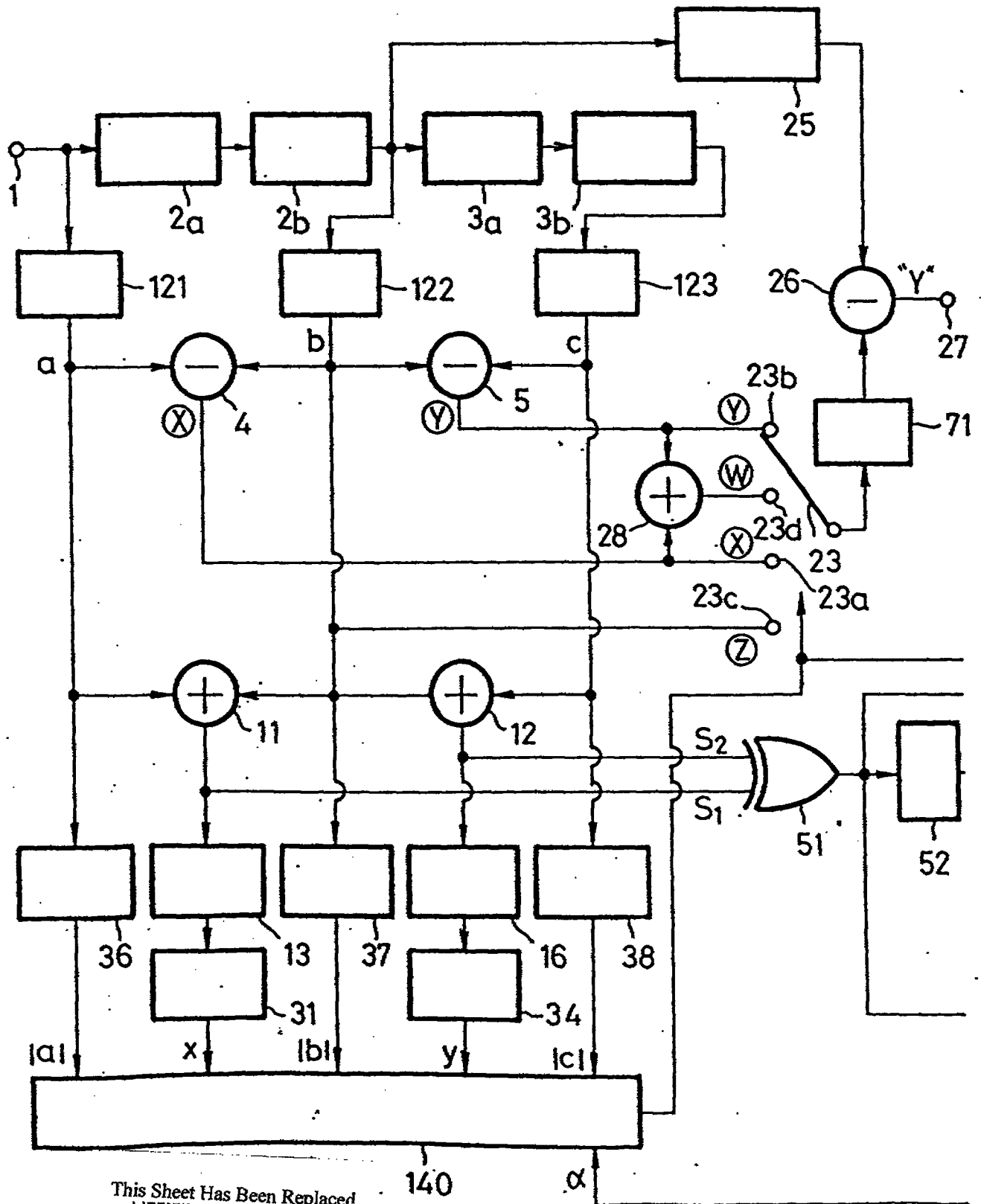
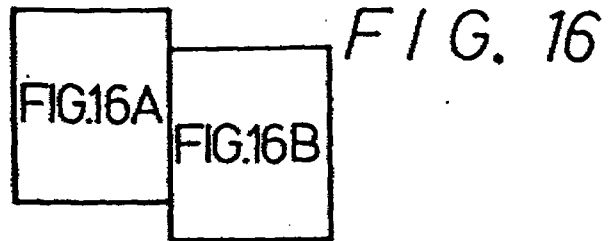
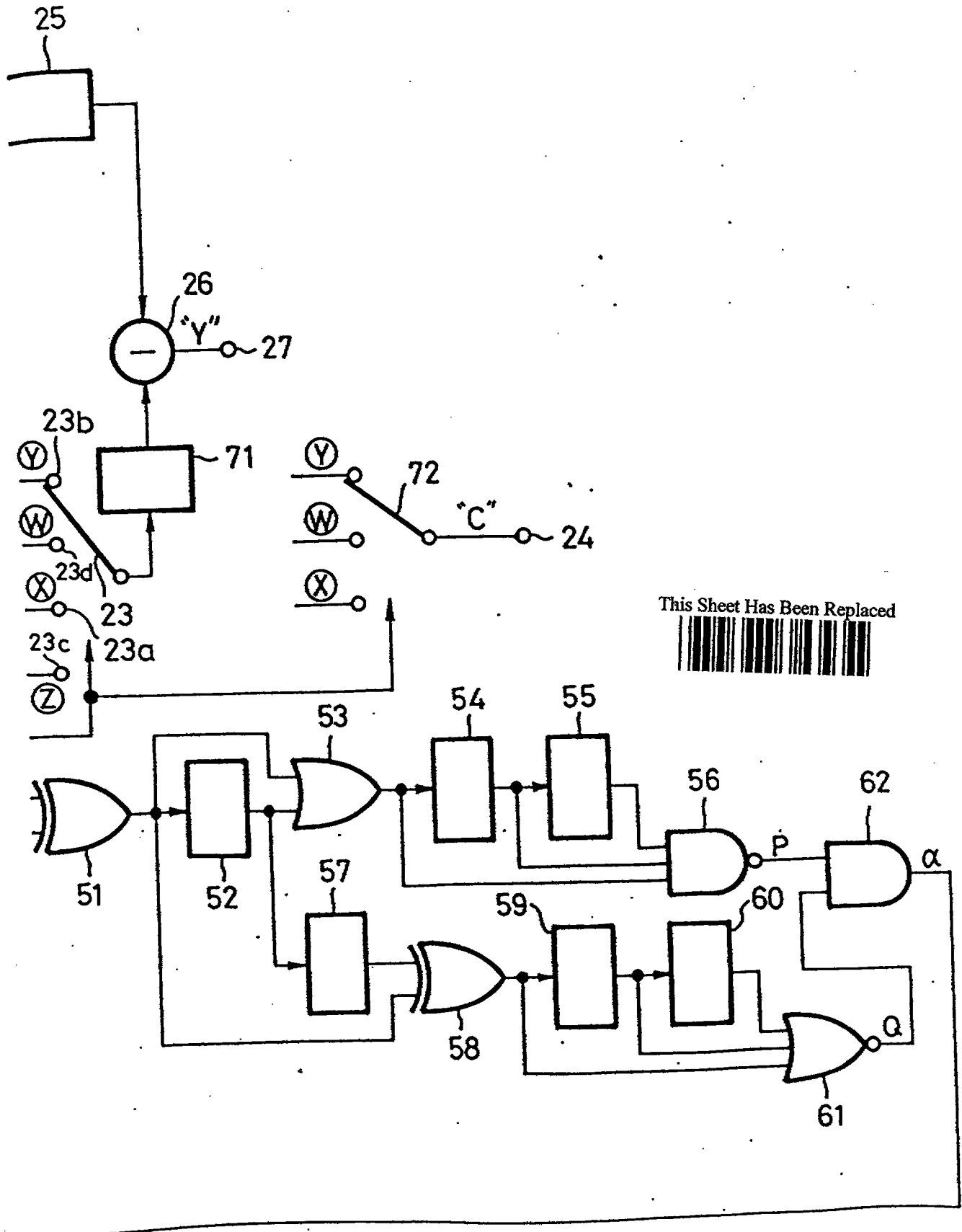


FIG. 16B





F/G. 17

a	—	∞	A	∞	V	∞	—
b	—	—	—	∞	A	∞	—
c	—	—	—	—	∞	A	∞
a+b	—	∞	—	—	—	∞	—
b+c	—	—	—	∞	—	—	—
x	0	1	1	0	0	1	0
y	0	0	0	1	0	0	1
x	—	∞	∞	∞	A	∞	—
y	—	—	—	∞	∞	V	∞
"c"	—	—	—	∞	A	∞	—

FIG. 18

a

b

c

a+b — — — — — — —

b+c — — — — — — — — —

⊗ 0 0 1 1 0 0 0 0 0

⊙ 0 0 0 0 1 1 0 0 0

⊗ — —

⊙ — —

"C"





FIG. 21



REPLACEMENT SHEETS, SG	
☒ Description :	paged 1... to paged 30
☒ Claim(s) :	paged 31... to paged 41
☒ Drawing(s) :	paged 42 to paged 43
☒ Abstract :	paged 44 to paged 5...
DATE OF RECEIPT 22/2/97	

ADAPTIVE COMB FILTERS AND METHODS OF
SEPARATING VIDEO SIGNAL COMPONENTS

REPLACEMENT SHEET
22 FEB 1997

5 This invention relates to adaptive comb filters and methods of separating video signal components.

Band-pass filters have been commonly utilized for separating luminance and chrominance signals of, for example, an NTSC composite colour video signal. However, such band-pass filters when used alone to effect signal separation are unable to distinguish between
10 chrominance signals and luminance signals falling within the chrominance frequency band, typically $3.58 \text{ MHz} \pm 500 \text{ kHz}$. This effect, commonly referred to as "cross colour", results in deterioration of the quality of an image.

Comb filters provide improved luminance and chrominance
15 signal separation by utilizing the general vertical correlation of video signals and, in the case of an NTSC or PAL composite colour video signal, the phase inversion of the chrominance sub-carrier signal which occurs with every new or every other new horizontal line of the video signal, respectively. A typical comb filter arrangement
20 for separating luminance signals (indicated as "Y") and chrominance signals (indicated as "C") from an NTSC composite colour video signal (indicated as "Y + C") is applied in Figure 1. The composite colour video signal ("Y + C") is applied to an input terminal 101 of the comb filter connected to an input terminal of a delay line (1H) 102 which
25 is operative to provide the composite colour video signal "Y + C" delayed by one horizontal line interval at an output terminal thereof. The "Y + C" signal received at the terminal 101 is also applied to a first input of a subtracting circuit 103 having a second input connected to the output terminal of the delay line 102. Since the
30 luminance signal components of successive horizontal lines are in-phase signals which generally possess vertical correlation, the subtracting circuit 103 serves to attenuate luminance signal components of the delayed composite colour video signal "Y + C" as it is provided at an output terminal thereof.

35 The output terminal of the subtracting circuit 103 is connected to an input of a band-pass filter (BPF) 104 having a pass band corresponding with the frequency range of the chrominance signal

"C" and, accordingly, provides a separated chrominance signal component "C" at an output terminal thereof which is connected to an output terminal 105 of the comb filter. The output terminal of the band-pass filter 104 is also connected to a first input of a further subtracting circuit 107. A delay line 106 has an input connected to the input terminal 101 to receive the composite colour video signal "Y + C" and is operative to delay the "Y + C" signal by an interval corresponding to the delays introduced by the delay line 102 and the band-pass filter 104. The delay line 106 provides the delayed composite colour video signal "Y + C" to a second input of the subtracting circuit 107. Accordingly, the subtracting circuit 107 serves to attenuate chrominance signal components present in the composite colour video signal "Y + C" and thereby provides a separated luminance signal component "Y" to an output terminal 108 of the comb filter.

However, vertical correlation of the chrominance signal is not always present. For example, where a change to a complementary colour occurs in the chrominance signal in a given scanning line, the phase of the chrominance signal becomes inverted, so that vertical correlation thereof with the chrominance signal of the preceding scanning line no longer exists. Not only does this result in the deterioration of the output chrominance signal, but the luminance signal "Y" at the output terminal 108 retains portions of the chrominance signal which otherwise would have been suppressed by the subtracting circuit 107, resulting in so-called "dot interference" in the reproduced video image.

On the other hand, where vertical correlation between luminance signals of successive line intervals is lacking, the chrominance signal from the subtracting circuit 103 possesses a residual component of the luminance signal therein. Accordingly, when this occurs such components may appear at the output of the BPF 104, resulting in a cross colour effect similar to that encountered in the use of a band-pass type signal separation technique of the kind described above.

To overcome the effects resulting from vertical non-correlation, it has been proposed in our Japanese Patent Application No. 88/301762 that vertical correlation of luminance signals in the

22 FEB 1997

pass-band of the chrominance signal be detected to carry out adaptive comb filtering techniques to avoid such unwanted effects. Figure 2 illustrates the overall circuit arrangement of the adaptive comb filter disclosed therein.

5 With reference to Figure 2, an NTSC composite colour video signal "Y + C" is supplied to an input terminal 1 thereof. A first delay line (1H) 2 has an input connected to the terminal 1 and is operative to delay the composite colour video signal "Y + C" by one horizontal line interval which it then provides at an output terminal
10 thereof. The output terminal of the delay line 2 is connected to an input of a second delay line (1H) 3 which is operative to delay the composite colour video signal "Y + C" from the output terminal of the delay line 2 by an additional horizontal line interval which it then provides at an output terminal thereof. A first subtracting circuit
15 4 has two inputs each connected with a respective one of the input terminal 1 and the output terminal of the delay line 2 and serves to provide a signal at an output terminal thereof representing the difference between the input composite colour video signal and a prior version thereof delayed by one horizontal line interval. A second
20 subtracting circuit 5 has two inputs each coupled with a respective one of the output terminals of the delay lines 2 and 3 and serves to provide a signal at an output terminal thereof representing the difference between the composite colour video signals respectively delayed by one and two horizontal line intervals with respect to the
25 signal at the input terminal 1.

Each of a plurality of BPFs 6, 7, 8, 9 and 10 has a pass band substantially corresponding to the frequency band of the chrominance signal component of the composite colour video "Y + C". Each of the BPFs 6 to 10 has a respective input correspondingly connected to a
30 respective one of the input terminal 1, the output terminal of the subtracting circuit 4, the output terminal of the delay line 2, the output terminal of the subtracting circuit 5 and the output terminal of the delay line 3. A first adding circuit 11 has a first input connected to an output terminal of the BPF 6 and a second input
35 connected to an output terminal of the BPF 8. A second adding circuit 12 has a first input connected to the output terminal of the band pass filter 8 and a second input connected to an output terminal of the

22 FEB 1997

band pass filter 10.

Each of a pair of absolute value generating circuits (ABSs) 13 and 16 is operative to produce absolute value signals at a respective output terminal thereof representing absolute values of signals supplied thereto. The output terminal of the first adding circuit 11 is connected to the input of the ABS 13, while the output terminal of the adding circuit 12 is connected to the input of the ABS 16. Each of a pair of coding circuits 17 and 20 is operative to convert signals received at an input thereof to a respective binary code (X) and (Y) by comparison of the input signals with a predetermined voltage level. The output terminal of the ABS 13 is connected to the input of the coding circuit 17, while the output terminal of the ABS 16 is connected to the input of the coding circuit 20. Each of the coding circuits 17 and 20 has an output terminal connected to a respective input of a logic signal processor 21.

The BPFs 7 and 9 produce respective output signals (X) and (Y) which are produced in the same fashion as the output chrominance signal at the output terminal 105 of the comb filter of Figure 1. The output terminal of the BPF 8 is connected to an input of a further BPF 22 having a pass band similar to that of BPFs used alone without comb filtering to separate chrominance signals, as first described above. The BPF 22 produces an output signal Z corresponding to the output chrominance signal produced by the band-pass filtering technique. A switching circuit 24 has a first input terminal 23a connected to the output terminal of the BPF 7 to received the signal (X) therefrom, a second input terminal 23b connected to the output terminal of the BPF 9 to received the signal (Y) therefrom and a third input terminal 23c connected to an output terminal of the BPF 22 to receive the signal (Z) therefrom. The switching circuit 23 is operative to connect an output terminal thereof to one of the input terminals 23a to 23c under the control of the logic signal processor 21.

In addition, the output terminals of the BPFs 7 and 9 are each connected to an input of a respective ABS 14 and 15 which, like circuits 13 and 16, are each operative to produce output signals at an output terminal thereof representing an absolute value of signals input thereto. The output terminals of the ABSs 14 and 15 are each connected to an input of a respective one of third and fourth coding

circuits 18 and 19 each of which is operative to convert the absolute value signal received thereby to a respective binary code $\textcircled{X}'$ and $\textcircled{Y}'$ by comparing the absolute value signal with a predetermined voltage level. An output terminal of the coding circuit 18 is connected to an input of the logic signal processor 21 to supply the signal $\textcircled{X}'$ thereto, while the coding circuit 19 has an output terminal connected to an input of the logic signal processor 21 to supply the logic signal $\textcircled{Y}'$ thereto.

The logic signal processor 21 logically compares the logic signals $\textcircled{X}$, $\textcircled{X}'$, $\textcircled{Y}$ and $\textcircled{Y}'$ in accordance with Table I below such that the switching circuit 23 selects a respective one of the signals $\textcircled{X}$, $\textcircled{Y}$ and $\textcircled{Z}$ in accordance with the signal states indicated in Table I, wherein a dash represents an indefinite state.

TABLE I

$\textcircled{X}$	$\textcircled{Y}$	$\textcircled{X}'$	$\textcircled{Y}'$	selection of input signals by switching circuit 23
0	0	-	-	$\textcircled{Y}$
0	1	-	-	$\textcircled{X}$
1	0	-	-	$\textcircled{Y}$
1	1	-	0	$\textcircled{Y}$
1	1	0	1	$\textcircled{X}$
1	1	1	1	$\textcircled{Z}$

The output terminal of the switching circuit 23 is connected to an output terminal of the comb filter circuit to provide a chrominance signal component "C" of the input composite colour video signal "Y + C" thereto. To produce a luminance signal "Y" from the input composite colour video signal "Y + C", a third delay line 25 is provided having an input connected to the output terminal of the delay line 2 and is operative to delay the composite colour video signal at the output terminal of the delay line 2 by an interval corresponding to the signal delay produced by each of the BPFs 7 and 9. An output terminal of the delay line 25 is connected to a first input of a third subtracting circuit 26 having a second input connected to the output

22 FEB 1997

terminal 24. The subtracting circuit 26 is operative to produce the luminance signal "Y" by subtracting the output chrominance signal "C" supplied at the output terminal 24 from the delayed composite colour video signal "Y + C", and supplies the thus produced luminance signal "Y" to a luminance signal output terminal 27 of the comb filter circuit.

With reference to Figure 3, the phases of exemplary output signals provided by the BPFs 6, 8 and 10 are illustrated therein by waveforms a, b and c, respectively, wherein the waveforms of coincident signals are vertically aligned. Phases of other signals as well as logic levels of coded signals appearing at respective terminals of the Figure 2 circuit are also illustrated in Figure 3. In the example of Figure 3, a phase inversion of the chrominance signal representing a change to a complementary colour is represented by a vertical dashed line in each of the waveforms a, b and c. In the example of Figure 3, the luminance signal is constant.

When the signals represented by the waveforms a, b and c are supplied as inputs to the adding circuits 11 and 12, they combine subtractively to cancel one another so long as their phases are inverted. However, upon the occurrence of a colour change as described above, the phases of these signals on successive lines coincide so that they combine additively as shown for the waveforms "a + b" and "b + c" in Figure 3, thereupon resulting in a logic "one" output by the respective coding circuits 17 and 20 represented by the signals $\textcircled{X}$ and $\textcircled{Y}$ in Figure 3. Conversely, the outputs $\textcircled{X}$ and $\textcircled{Y}$ provided respectively by the BPFs 7 and 9 from the outputs of the signal subtracting circuits 4 and 5 are eliminated upon the occurrence of a colour change.

When this takes place, the circuit of Figure 1 eliminates the chrominance signal components of the horizontal line interval in which the colour change occurs, resulting in dot interference. To avoid, this the logic signal processor 21 is operative to control the switch 23 so that it selects the signal $\textcircled{X}$ at such times that $(\textcircled{X}, \textcircled{Y}) = (0, 1)$ indicating that a colour change has occurred between the signals applied to the input terminals of the adding circuit 12, so that a drop-out of the chrominance signal "C" is avoided. It will also be seen from Table I that, upon the occurrence of a colour change between

22 FEB 1997

the signals supplied to the adding circuit 11, resulting in the production of logic signals $(\textcircled{X}, \textcircled{Y}) = (0, 1)$, the signal $\textcircled{Y}$ is selected instead of the signal $\textcircled{X}$, such that chrominance signal drop-out likewise is avoided at such time. Accordingly, a reduction in the level of the chrominance signal "C" due to chrominance signal inversion is thus avoided so that the chrominance signal "C" produced at the output terminal 24 corresponds to the chrominance signal component in the composite colour video signal "Y + C" provided at the output of the delay line 25. Accordingly, a luminance signal "Y" will be provided at the output terminal 27 which is free of dot interference under these circumstances.

Referring now to Figure 4, the phases of further exemplary output signals from BPFs 6, 8 and 10 are illustrated therein by waveforms a, b and c respectively, where a chrominance signal first appears on a given horizontal line of the composite colour video signal "Y + C", while the level of other signals together with logic levels of coded signals appearing at respective terminals of the Figure 2 circuit are also illustrated in Figure 4. Where a composite colour video signal having these characteristics is applied to the comb filter shown in Figure 1, a deterioration in the level of the chrominance signal supplied by the subtracting circuit 103 occurs as shown in the case of the signals $\textcircled{X}$ and $\textcircled{Y}$ of Figure 4. The result is a deterioration in the vertical resolution of the image produced with the use of the signals output by the Figure 1 filter circuit. However, in the case of the adaptive comb filter of Figure 2, the partially attenuated signals $\textcircled{X}$ and $\textcircled{Y}$ are rejected thereby such that vertical resolution is improved under these conditions.

With reference to Figure 5, phases of still further exemplary output signals from the BPFs 6, 8 and 10 are represented by waveforms a, b and c, respectively, thereof to illustrate luminance signal components within the frequency band of the chrominance signal, such that the phases thereof coincide on successive horizontal scan lines. The corresponding amplitudes and states of signals appearing elsewhere in the circuit of Figure 2 simultaneously therewith likewise are illustrated in Figure 5. With reference again to Table I, it will be seen that the presence of such luminance signals at the output of the BPF 8 simultaneously with the presence thereof either at the output of

the BPF 6 or 10 results in a logic signal combination $(X, Y) = (1, 1)$, whereupon the signal Y supplied by the BPF 9 is selected by the switching circuit 23 whenever the signal $Y' = 0$ (to reject the residual luminance signal component concurrently present in the signal X), whereas the signal X is selected by the switching circuit 23 whenever the signal combination $(X', Y') = (0, 1)$ (to reject the residual luminance signal component then present in the signal Y). Accordingly, under the circumstances illustrated in Figure 5, a chrominance signal "C" free of such luminance signal components is produced at the output terminal 24.

Referring now to Figure 6, outputs from the BPFs 6, 8 and 10 are symbolized therein by the waveforms a, b and c, respectively, which represent the existence of a chrominance signal on only one particular scanning line, while the level of the luminance signal is simultaneously constant. The corresponding amplitudes and states of signals appearing elsewhere in the circuit of Figure 2 simultaneously therewith likewise are illustrated in Figure 6. It will be seen from Figure 6 that, whether the signal X or the signal Y is selected when this solitary chrominance signal appears at the output of the BPF 8, a deterioration in the level thereof is unavoidable. Accordingly, under these circumstances, the chrominance signal Z provided at the output terminal of the BPF 22 is selected by the switching circuit 23, as will be seen with reference to Table I under the conditions that X, Y, X' and Y' all equal a logic "one" level indicating that neither the luminance signal nor the chrominance signal possesses vertical correlation. Under these circumstances, the signal Z is provided at the output terminal 24 by the switching circuit 23 and is free of the signal deterioration then present in the signals X and Y .

In summary, the adding circuits 11 and 12 and the signal subtracting circuits 4 and 5 serve respectively to produce signals indicating vertical correlation of the luminance signal and of the chrominance signal, so that signals which strongly correlate may be selected to produce the output chrominance signal "C". On the other hand, when the signals do not correlate satisfactorily, the output from the conventional BPF circuit is instead selected to produce luminance and chrominance signals of high quality.

With reference now to Figure 7, wherein elements

corresponding to those illustrated in Figure 2 bear the same reference numerals, a third adding circuit 28 has a first input connected to the output terminal of the BPF 9 to receive the signal Y therefrom and a second input connected to the output terminal of the BPF 7 to receive the signal X therefrom. The adding circuit 28 is operative to provide an output signal W in accordance with the following relationship:

$$\textcircled{W} = (\textcircled{X} + \textcircled{Y})/2 = [(b-a) + (b-c)]/2 \quad \dots (1)$$

Accordingly, the adding circuit 28 produces a signal $\textcircled{W}$ based on the correlation of three horizontal scan lines and corresponding to the output of a so-called 2H comb filter. An output terminal of the adding circuit 28 is connected to a fourth input 23d of the switching circuit 23.

In the adaptive comb filter of Figure 7, a modified logic signal processor 100 is operative to control the operation of the switching circuit in accordance with the logic signals $\textcircled{X}$, $\textcircled{Y}$, $\textcircled{X}'$ and $\textcircled{Y}'$ input thereto in accordance with Table II below, wherein the logic signal states are expressed in the same manner as in Table I:

TABLE II

$\textcircled{X}$	$\textcircled{Y}$	$\textcircled{X}'$	$\textcircled{Y}'$	selection of input signals by switching circuit 23
0	0	-	-	$\textcircled{W}$
0	1	-	-	$\textcircled{X}$
1	0	-	-	$\textcircled{Y}$
1	1	-	0	$\textcircled{Y}$
1	1	0	1	$\textcircled{X}$
1	1	1	1	$\textcircled{Z}$

With reference to Table II, it will be seen that, when signal correlation is present between the signals appearing on the three successive horizontal scanning lines represented by the waveforms a, b and c, the logic signals $\textcircled{X}$ and $\textcircled{Y}$ assume a low or "0" logic level such that the signal $\textcircled{W}$ at the output of the adding circuit 28 is selected by the switching circuit 23. Under the remaining conditions listed in Table II, the selection of the input signals by the switching circuit 23 corresponds to that of the Figure 2 adaptive comb

22 FEB 1997

filter circuit as expressed in Table I.

Referring now to Figure 8, exemplary chrominance signal waveforms a, b and c appearing at the output terminals of BPFs 6, 8 and 10, respectively, are illustrated therein where a complementary colour change resulting in a phase inversion occurs at successively later intervals differing by one-half of the colour sub-carrier wavelength from line to line at the output terminals of the BPFs 6, 8 and 10. The corresponding amplitudes and phases of signals appearing elsewhere in the circuit of Figure 7 therewith simultaneously likewise are illustrated in Figure 8. Under these circumstances, the signal $\textcircled{W}$ is selected except at such times that the signals of two successive lines correspond in phase, whereupon either the signal $\textcircled{U}$ or the signal $\textcircled{V}$ is selected to avoid the concurrent deterioration in the level of the signal W. Accordingly, in the adaptive comb filter of Figure 7, when the signals a, b and c from successive horizontal scan lines exhibit good correlation, the adaptive comb filter acts as a 2H comb filter which provides superior separation of the chrominance signal. When the chrominance signal derived in this manner is subtractively combined with the composite colour video signal "Y + C" in the subtracting circuit 26, dot interference can be reduced by 6 dB, thus providing improved image quality.

In the adaptive comb filter circuits of Figures 2 and 7, the coding circuits 17 to 20 compare the input absolute value signals with a predetermined voltage level. In the event that the levels of the input signals change, the coding circuits 17 to 20 may be ineffective to encode properly the input signals if the level thereof does not reach the predetermined voltage level. If the predetermined voltage level is reduced in an attempt to enable proper encoding of weaker signals, the probability that noise present in the input signal will interfere with proper operation of the adaptive filter circuit is increased.

According to the present invention there is provided an adaptive comb filter for separating at least one component of a video signal, the filter comprising:

a filter input terminal for receiving said video signal;
first and second signal delay means for delaying signals supplied to respective input terminals thereof and providing respective delayed

input signals at respective output terminals thereof, the input terminal of the first signal delay means being coupled to said filter input terminal and the output terminal thereof being coupled to the input terminal of the second delay means;

- 5 first and second signal subtracting means for each producing output difference signals at a respective output terminal thereof representing a difference between signals supplied to a respective plurality of input terminals thereof, first and second input terminals of the first signal subtracting means each being coupled to a
10 respective one of the input and output terminals of said first signal delay means and first and second input terminals of the second signal subtracting means each being coupled to a respective one of the input and output terminals of said second signal delay means;

- first signal adding means for producing a summation signal at an
15 output terminal thereof representing a summation of signals received at first and second input terminals thereof, said first and second input terminals of said first signal adding means being respectively coupled with said output terminals of said first and second signal subtracting means;

- 20 first switching means for selectively providing one of a plurality of input signals received at respective input terminals thereof to an output terminal thereof, said first switching means having first and second input terminals respectively coupled to said output terminals of said first and second signal subtracting means, a third input
25 terminal coupled to the output terminal of said first signal adding means and a fourth input terminal coupled to the output terminal of said first signal delay means, the output terminal of said switching means being operative to provide a first component of said video signal;

- 30 second and third signal adding means for each producing a summation signal at a respective output terminal thereof representing a summation of signals received at respective first and second input terminals thereof, said first input terminal of each of said second and third signal adding means being coupled to the output terminal of
35 said first signal delay means, said second input terminal of said second signal adding means being coupled to said filter input terminal, and said second input terminal of said third signal adding

means being coupled to the output terminal of said second signal delay means;

first and second absolute value means for each producing an output signal at a respective output terminal thereof representing an absolute value of a signal received at a respective input terminal thereof, the input terminals of said first and second absolute value means being respectively coupled with the output terminals of said second and third signal adding means, respectively; and
controlling means for controlling the selective provision of input signals by said first switching means to its output terminal based upon a relative value of the output signals produced at the output terminals of said first and second absolute value means.

According to the present invention there is also provided an adaptive comb filter for separating at least one component of a video signal, the filter comprising:

a filter input terminal for receiving said video signal;
first delay means for delaying said video signal by a first interval to produce a first delayed video signal;
second delay means for delaying said video signal by a second interval to produce a second delayed video signal;
first difference means for producing a first difference signal proportional to a difference between said video signal and said first delayed video signal;
second difference means for producing a second difference signal proportional to a difference between said first delayed video signal and said second delayed video signal;
first summing means for producing a first summation signal proportional to a summation of said first difference signal and said second difference signal;
first signal selecting means for controllably selecting one of said first difference signal, said second difference signal and said first summation signal as said at least one component of said video signal;
second summing means for producing a second summation signal proportional to a summation of said video signal and said first delayed video signal;
third summing means for producing a third summation signal proportional to a summation of said first delayed video signal and

said second delayed video signal; and
control means for controlling the selection of one of said first
difference signal, said second difference signal and said first
summation signal by said first signal selecting means based upon
5 relative magnitudes of said second summation signal and said third
summation signal.

According to the present invention there is also provided a
method of separating a chrominance signal from a composite colour
video signal, the method comprising the steps of :
10 delaying said composite colour video signal by a first interval to
produce a first delayed video signal;
delaying said composite colour video signal by a second interval to
produce a second delayed video signal;
producing a first difference signal proportional to a difference
15 between said composite colour video signal and said first delayed
video signal;
producing a second difference signal proportional to a difference
between said first delayed video signal and said second delayed video
signal;
20 producing a first summation signal proportional to a summation of said
first difference signal and said second difference signal;
producing a second summation signal proportional to a summation of
said composite colour video signal and said first delayed video
signal;
25 producing a third summation signal proportional to a summation of said
first delayed video signal and said second delayed video signal; and
selecting one of said first difference signal, said second difference
signal and said first summation signal as said chrominance signal
based upon a comparison of relative magnitudes of said second
30 summation signal and said third summation signal.

In accordance with an aspect of the present invention, an
adaptive comb filter for separating at least one component of a video
signal comprises a filter input terminal for receiving said video
signal; first and second signal delay means for delaying signals
35 supplied to respective input terminals thereof and providing the
delayed input signals at respective output terminals thereof, the
input terminal of the first signal delay means being connected to said

filter input terminal and the output terminal of the first signal delay means being connected to the input terminal of the second signal delay means; first and second signal subtracting means for producing output difference signals at respective output terminals thereof
5 representing differences between signals supplied to respective first and second input terminals thereof, the first and second input terminals of the first signal subtracting means being connected to the input and output terminals, respectively, of the first signal delay means, the first and second input terminals of the second signal
10 subtracting means being connected to the input and output terminals, respectively, of the second signal delay means; first signal adding means for producing a summation signal at an output terminal thereof representing a summation of signals received at first and second input terminals thereof, the first and second input terminals of the first
15 signal adding means being respectively coupled with the output terminals of the first and second signal subtracting means, respectively; switching means for selectively providing one of a plurality of input signals received at respective input terminals thereof to an output terminal thereof, the switching means having
20 first and second input terminals coupled with the output terminals of the first and second signal subtracting means, respectively, a third input terminal connected to the output terminal of the first signal adding means and a fourth input terminal connected to the output terminal of the first signal delay means, the output terminal of the
25 switching means being operative to provide a first component of the video signal; second and third signal adding means for producing summation signals at respective output terminals thereof representing summations of signals received at first and second input terminals of the respective signal adding means, the first input terminals of the
30 second and third signal adding means being connected to the output terminal of the first signal delay means, the second input terminal of the second signal adding means being connected to the filter input terminal, and the second input terminal of the third signal adding means being connected to the output terminal of the second signal
35 delay means; first and second absolute value means for producing output signals at respective output terminals thereof representing absolute values of signals received at respective input terminals

thereof; the input terminals of the first and second absolute value means being connected to the output terminals of the second and third signal adding means, respectively; and controlling means for controlling the selective provision of input signals by the switching means to its output terminal based upon a relative value of the output signals produced at the output terminals of the first and second absolute value means.

In accordance with another aspect of this invention, an adaptive comb filter for separating at least one component of a video signal comprises a filter input terminal for receiving said video signal; first delay means for delaying the video signal by a first interval to produce a first delayed video signal; second delay means for delaying the video signal by a second interval to produce a second delayed video signal; first difference means for producing a first difference signal proportional to a difference between the video signal and first delayed video signal; second difference means for producing a second difference signal proportional to a difference between the first and second delayed video signals; first summing means for producing a first summation signal proportional to a summation of the first and second difference signals; first signal selecting means for controllably selecting one of the first difference signal, the second difference signal and the first summation signal as the at least one component of the video signal; second summing means for producing a second summation signal proportional to a summation of the video signal and the first delayed video signal; third summing means for producing a third summation signal proportional to a summation of the first delayed video signal and the second delayed video signal; and control means for controlling the selection of one of the first difference signal, the second difference signal and the first summation signal by the first signal selecting means based upon relative magnitudes of said second and third summation signals.

In accordance with a further aspect of this invention, a method of separating a chrominance signal from a composite colour video signal comprises the steps of delaying the composite colour video signal by a first interval to produce a first delayed video signal; delaying the composite colour video signal by a second interval to produce a second delayed video signal; producing a first

22 FEB 1997

difference signal proportional to a difference between the composite colour video signal and the first delayed video signal; producing a second difference signal proportional to a summation of the first and second difference signals; producing a first summation signal proportional to a summation of the first and second difference signals; producing a second summation signal proportional to a summation of the composite colour video signal and the first delayed video signal; producing a third summation signal proportional to a summation of the first and second delayed video signals; and selecting one of the first difference signal, the second difference signal and the first summation signal as the chrominance signal based upon a comparison of relative magnitudes of the second and third summation signals.

The invention will now be described by way of example with reference to the accompanying drawings, throughout which like parts are referred to by like references, and in which:

Figures 1, 2 and 7 are circuit block diagrams of examples of previously proposed comb filters for separating luminance and chrominance signal components of an NTSC composite colour video signal;

Figures 3 to 6 and 8 are waveform diagrams referred to in connection with Figures 2 and 7;

Figures 9 to 11 are respective circuit block diagrams of first to third embodiments of adaptive comb filter in accordance with the present invention for separating the luminance and chrominance signal components of an NTSC composite colour video signal;

Figure 12, comprising Figures 12A and 12B, is a circuit block diagram of a fourth embodiment of adaptive comb filter in accordance with the present invention for separating luminance and chrominance signal components of an NTSC composite colour video signal;

Figures 13 and 14 are waveform diagrams referred to in connection with Figure 12;

Figure 15, comprising Figures 15A and 15B, is a circuit block diagram of a fifth embodiment of adaptive comb filter in accordance with the present invention for separating luminance and chrominance signal components of an NTSC composite colour video signal;

Figure 16, comprising Figures 16A and 16B, is a circuit block

diagram of a sixth embodiment of adaptive comb filter in accordance with the present invention for separating luminance and chrominance signal components of a PAL composite colour video signal;

Figures 17 to 20 are waveform diagrams referred to in connection with Figure 16; and

Figure 21 is a waveform diagram of input colour video signals illustrated in Figure 20.

In the first embodiment of adaptive comb filter, shown in Figure 9, first to fourth low-pass filters (LPFs) 31 to 34 are each operative to attenuate high frequency components of signals supplied at a respective input terminal thereof and having a filter characteristic selected as:

$$(1 + 2Z^{-1} + Z^{-2})/4 \quad \dots (2)$$

where Z represents the sampling cycle. The output terminals of the absolute value generating circuits (ABSs) 13 to 16 are each connected to the input terminal of a respective one of the LPFs 31 to 34. the LPFs 31 to 34 are each operative to produce a respective correlation signal x, X, Y and y at a respective output terminal thereof. Each of the output terminals of the LPFs 31 to 34 is connected to an input of a signal correlation comparator 35. the correlation comparator 35 is operative to compare the signals x, X, Y and y supplied thereto for controlling the selection of the input signals (W), (X), (Y) and (Z) by the switching circuit 23. To do so, the correlation comparator 35 compares the relative magnitudes of the correlation signals x, X, Y, y and determines the selection of the input to the switching circuit 23 in accordance with the relationships of the input correlation signals set forth in the left-hand column of Table III below.

TABLE III

Relationship of input correlation signals	Selection of input signals by switching circuit 23
$y > 2x$	(X)
$y < \frac{1}{2}x$	(Y)
$\frac{1}{2}y \leq x \leq 2y$ and $\frac{1}{2}Y \leq X \leq 2Y$ and $x, y, X, Y \neq 0$ and $X < 2x$ and $Y < 2y$	(Z)
Cases other than those described above	(W)

It has been observed that when $y > 2x$, non-correlation frequently exists between the signals b and c ; accordingly, as indicated in Table III above, the correlation comparator 35 is then operative to control the switching circuit 23 to select the input signal X . Furthermore, it has been observed that when $\frac{1}{2}y \leq x \leq 2y$ and $\frac{1}{2}Y \leq X \leq 2Y$, generally the chrominance signal exists only on one scanning line. In that case, when in addition the correlation signals x, y, X and $Y \neq 0$ and $X < 2x$ and $Y < 2y$ which ensures a sufficient signal level, the signal $\textcircled{Z}$ is selected by the switching circuit 23 under the control of the correlation comparator 35. In all other cases, it is found that the correlation of the input signal is sufficiently high, so that the signal $\textcircled{W}$ is selected.

Since the correlation comparator 35 compares the relative magnitudes of the correlation signals x, X, Y and y for determining whether non-correlation exists therebetween, this determination is unaffected by a change in the absolute levels of the correlation signals, thus enabling the Figure 9 embodiment reliably to separate the luminance and chrominance signal components "Y" and "C" despite such variations in signal levels.

In the adaptive comb filter of the Figure 10 embodiment, ABSs 36, 37 and 38 are each operative to produce signals at a respective output terminal thereof proportional to the absolute value of corresponding signals received at a respective input terminal thereof. The input of the ABS 36 is connected to the output terminal of the ABS 6 to received the signal "a" therefrom and provides an absolute value signal $|a|$ at its output terminal which connected to an input of a signal correlation comparator 110. The input of the ABS 37 is connected to the output of the BPF 8 to received the signal "b" therefrom and provides an absolute value signal $|b|$ at its output terminal which is connected to a further input of the correlation comparator 110. The input of the ABS 38 is connected to the output terminal of the BPF 10 to receive the signal "c" therefrom and provides an output signal $|c|$ at its output terminal which is connected to a further input of the correlation comparator 110. The correlation comparator 110 is operative to control the selection of the input signals $\textcircled{W}, \textcircled{X}, \textcircled{Y}$ and $\textcircled{Z}$ by the switching circuit 23 based upon the signals $x, y, |a|, |b|$ and $|c|$ to effect the selection of

the input signals by the switching circuit 23 in accordance with the input correlation signal relationships expressed in the left-hand column of Table IV below.

5

TABLE IV

10

Relationship of input correlation signals		Selection of input signals by switching circuit 23
$y > 2x$		ⓧ
$y < \frac{1}{2}x$		Ⓨ
$\frac{1}{2}y \leq x \leq 2y$	$ a + c < \frac{1}{2} b $	Ⓩ
	In other cases	Ⓦ

As in the case of the Figure 9 embodiment, the adaptive comb filter of Figure 10 is operative to select the signal ⓧ as the output chrominance signal component "C" whenever $y > 2x$, and is operative to select the signal Ⓨ as the output chrominance signal "C" whenever $y < \frac{1}{2}x$. It has been observed that when $\frac{1}{2}y \leq x \leq 2y$ either the signal correlation is higher than a predetermined level or else non-correlation exists. Under these conditions, it has been observed that when $|a| + |c| \leq |b|$, the chrominance signal generally exists only on one scanning line, that is, at the output of the BPF 8. In accordance with this observation, and in order to minimize the effects of signal noise, the correlation comparator 110 is operative to cause the switching circuit 23 to select the input signal Ⓩ when both $\frac{1}{2}y \leq x \leq 2y$ and $|a| + |c| \leq \frac{1}{2}|b|$. In all other cases, when the signal correlation exceeds a predetermined level, that is, when $\frac{1}{2}y \leq x \leq 2y$, the signal Ⓦ is selected by the switching 23 circuit under the control of the correlation comparator 110.

30

As in the case of the Figure 9 embodiment, the adaptive comb filter of Figure 10 is capable of provided reliable separation of luminance and chrominance signal components from the input composite colour video signal despite changes in the absolute levels of the input video signal, since the correlation comparator 110 compares the relationship of the absolute values of the signals a, b and c. In addition, since the selection of the band-pass filtered chrominance signal Ⓩ is conditioned upon the relationship $|a| + |c| \leq \frac{1}{2}|b|$,

35

the probability that the adaptive comb filter of the Figure 10 embodiment will erroneously select the band-pass filtered signal⁽²⁾ due to noise is substantially reduced. Accordingly, it is unnecessary to provide low-pass filtering circuitry for removing noise from the output signals provided by the absolute value generating circuits 36, 37 and 38 in the Figure 10 embodiment, so that the circuitry thereof can be considerably simplified.

In the embodiment of Figure 11, BPFs 6 to 10 of the Figure 10 embodiment have been eliminated and in place thereof a single BPF 39 is utilized having the same characteristics as those of the BPFs 6 to 10. An input terminal of the BPF 39 is connected to the input terminal 1 and an output terminal thereof is connected to respective inputs of the delay line 2, the subtracting circuit 4 and the adding circuit 11 to supply the band-pass filtered composite colour video signal "a" thereto. Since the luminance signal components of the signal at the output terminal of the delay line 2 have been largely eliminated by the BPF 39, a further delay line 40 operative to delay signals received at an input terminal thereof by one horizontal line period delay of the composite colour video signal "Y + C" provided by the delay line 2 in the preceding embodiments. Accordingly, the input of the delay line 40 is connected to the input terminal 1 to receive the composite colour video signal "Y + C" and an output terminal thereof is connected to the input of the delay line 25. It will be appreciated that the adaptive comb filter of Figure 11 is further simplified with respect to that of Figure 10.

In the embodiment illustrated in Figure 12, BPFs 121, 122 and 123 having the same characteristics of the BPFs, 6 to 10 of the Figure 10 embodiment are provided. An input of the BPF 121 is connected to the input terminal 1 and an output terminal thereof is connected to a respective input terminal of each of the signal subtracting circuit 4, the adding circuit 11 and the ABS 36. The input of the BPF 122 is connected to the output terminal of the delay line 2 and an output terminal thereof is connected to a respective input terminal of each of the subtracting circuits 4 and 5, the adding circuits 11 and 12 and the ABS circuit 37. The input of the BPF 123 is connected to the output terminal of the delay line 3 and an output terminal thereof is connected to respective inputs of each of the signal subtracting

circuit 5, the adding circuit 12 and the ABS 38.

The embodiment of Figures 12 also includes a horizontal correlation detecting logic circuit which is operative to detect horizontal correlation of luminance signal components present in the chrominance signal frequency band of the composite colour video signal "Y + C". The horizontal correlation detecting logic circuit includes an exclusive-OR gate 51 having a first input S1 connected to the output terminal of the adding circuit 11 and a second input S2 connected to the output terminal of the adding circuit 12. An output terminal of the exclusive-OR gate 51 is connected to an input of a one sample cycle delay circuit (D) 52. An output terminal of the delay circuit 52 is connected to an input terminal of an OR gate 53 and the input of the delay circuit 52 is connected to a second input of the OR gate 53. An output terminal of the OR gate 53 is connected to an input of a further delay circuit (D) 54 which also provides a one sampling cycle delay, and an output terminal of the delay circuit 54 is connected to an input of a still further delay circuit (D) 55 providing a further one sampling cycle delay. A NAND gate 56 having three inputs is provided such that its first input is connected to the output terminal of the OR gate 53, its second input is connected to the output terminal of the delay circuit 54 and its third input is connected to the output terminal of the delay circuit 55.

The output terminal of the delay circuit 52 is also connected to an input of another delay circuit 57 which provides a one sampling cycle delay and has an output terminal connected to an input terminal of an exclusive-OR gate 58. The second input of the exclusive-OR gate 58. The second input of the exclusive-OR gate 58 is connected to the input of the delay circuit 52. Still another delay circuit (D) 59 providing a one sampling cycle delay has an input connected to the output terminal of the exclusive-OR gate 58 and an output terminal connected to an input of another delay circuit (D) 60 providing a further one sampling cycle delay. A three input NOR gate 61 has its first input connected to the output terminal of the exclusive-OR gate 58, its second input connected to the output terminal of the delay circuit 59 and its third input connected to an output terminal of the delay circuit 60. A two input AND gate 62 has its first input connected to the output of the NAND gate 56 to receive an output

signal P therefrom and its second input connected to the output terminal of the NOR gate 61 to receive an output signal Q therefrom.

The embodiment of Figures 12 further includes a signal correlation comparator 120 having inputs each connected with a respective one of the output terminals of the LPFs 31 and 34 and of the ABSs 36, 37 and 38 to receive the signals x , y , $|a|$, $|b|$ and $|c|$, respectively, therefrom. The correlation comparator 120 has a further input connected to an output terminal of the AND gate 62 to receive an output signal α therefrom. The correlation comparator 120 is operative to control the selection of the input signals $\textcircled{W}$, $\textcircled{X}$, $\textcircled{Y}$ and $\textcircled{Z}$ by the switching circuit 23 in accordance with the correlation signal relationships expressed in the left-hand column of Table V below.

TABLE V

Relationship of input correlation signals		Selection of input signals by switching circuit 23
$y > 2x$		$\textcircled{X}$
$y < \frac{1}{2}x$		$\textcircled{Y}$
$\frac{1}{2}y \leq x \leq 2y$	$ a + c < \frac{1}{2} b $ and $\alpha = 1$	$\textcircled{Z}$
	In other cases	$\textcircled{W}$

With reference to Table V, in the embodiment of Figures 12, like that of Figure 10, the correlation comparator 120 is operative to control the switching circuit 23 to select the input signal $\textcircled{X}$ when $y > 2x$, and is operative to control the switching circuit 23 to select the signal $\textcircled{Y}$ when $y < \frac{1}{2}x$. When either the correlation is higher than the predetermined level or non-correlation exists, such that $\frac{1}{2}y \leq x \leq 2y$, the correlation comparator 120 controls the switching circuit 23 to select either the signal $\textcircled{Z}$ or the signal $\textcircled{W}$. As in the case of the Figure 10 embodiment, a condition for the selection of the signal $\textcircled{Z}$ is that $|a| + |c| < \frac{1}{2}|b|$, indicating generally that the chrominance signal exists only at the output of the BPF 122. The second condition for the selection of the signal Z is that $\alpha = 1$ indicating the absence of horizontal correlation, which is explained below in connection with the examples of Figures 13 and 14.

With reference now to Figure 13, waveforms a, b and c represent signals appearing at the output terminals of the BPFs 121, 122 and 123, respectively, to illustrate luminance signal components having a frequency of approximately 3.58 MHz. Under these conditions, the signals a + b (supplied to the input S1 of the exclusive-OR gate 51) and b + c (supplied to the input S2 of the exclusive-OR gate 51) supplied respectively by the adding circuits 11 and 12 will appear as shown in Figure 13. The signals S1 and S2 are sampled at the points indicated by the cross marks superimposed on their illustrated waveforms, so that the output S1 $\oplus$ S2 of the exclusive-OR circuit 51 will have the logic values indicated in Figure 13 beneath the waveform S2 in correspondence with each pair of sampling points. Since the output of the exclusive-OR gate 51 alternates between logic "1" and "0" levels with each successive sampling interval, the output of the OR gate 53 remains at a logic "1" level so long as the signals a, b and c continue as shown in Figure 13. Accordingly, the output P of the NAND gate 56 at the same time will remain at a logic "0" level.

Referring to Figure 14, the waveforms a, b and c represent luminance signal components having a frequency of approximately 6 MHz. The signals S1 and S2 corresponding to the outputs a + b and b + c, respectively, produced by the adding circuits 11 and 12 are illustrated in Figure 14 wherein signal sampling points are represented by open circles superimposed on the waveforms of the signals S1 and S2. The S1 and S2 signal waveforms of Figure 14 represent "0" levels at points beneath the horizontal axis thereof and represent logic "1" levels at points on or above the horizontal axis. The logic level output S1 $\oplus$ S2 produced by the exclusive-OR gate 51, accordingly, assumes the values indicated beneath the waveform S2 in correspondence with each pair of the sample signals S1 and S2. It will be seen from the illustrated values of S1 $\oplus$ S2 as shown in Figure 14 that, so long as luminance signal components having the characteristics represented by the waveforms a, b and c as shown in Figure 14 are provided at the inputs of the adding circuits 11 and 12, the output signal Q from the NOR gate 61 will remain at a logic "0" level.

It will be seen that, under the conditions illustrated in Figures 13 and 14, the output signal supplied by the AND gate 62 is

maintained at a logic "0" level and, with reference to Table V above, the correlation comparator 120 will be operative, under these conditions, to prevent the selection of the signal ②. Stated differently, the signal ② is selected only when middle and high band components of the luminance signal, such as sweep signal components and the like, are not sufficiently large to enable their detection by the horizontal correlation detecting logic circuit. Where, however, the condition $\frac{1}{2}y \leq x \leq 2y$ obtains, and either $|a| + |c| \geq \frac{1}{2}|b|$ or $\alpha = 0$, the correlation of the signals is sufficiently high so that the signal ④ is selected by the switch 23 under the control of the correlation comparator 120.

Since the adaptive comb filter of Figure 12 requires that the relative magnitudes of the signals a, b and c satisfy the condition $|a| + |c| < \frac{1}{2}|b|$ before the correlation comparator 120 thereof will control the switching circuit 23 to select the signal ② as the output chrominance signal "C", the probability that the signal ② will be selected erroneously due to noise is substantially reduced. Consequently, it unnecessary to provide means for removing noise from the signals $|a|$, $|b|$ and $|c|$, such as a LPF, so that the adaptive comb filter of Figure 12 can be considerably simplified.

The BPFs 121, 122 and 123 of the adaptive comb filter embodiment of Figures 12 have relatively wide pass bands ensuring that the frequency bands of the luminance and chrominance signal components transmitted thereby will overlap. Accordingly, in order to increase the resolution of the luminance signal "Y" at the output terminal 27 of the adaptive comb filter, the BPF 22 is provided with a relatively narrower pass band than that of BPFs 121 to 123. Presently, the chrominance signal can be limited to a bandwidth of ± 500 KHz while still providing an acceptable image. However, broad band chrominance signals are required for newly developed imaging systems. If the bandwidth of the BPFs 121, 122 and 123 are broadened accordingly, however, the resolution of the output luminance signal "Y" will deteriorate. In addition, the selection of the signal ② by the switch 23 of the adaptive comb filter of Figure 12 results in a cross colour effect which is generally inconspicuous since it occurs relatively infrequently. Nevertheless, it is desirable from an aesthetic viewpoint to provide the greatest possible degree of signal

separation.

Figures 15 shows a fifth embodiment of adaptive comb filter in which the above described shortcomings and disadvantages encountered in the operation of the adaptive comb filter of Figures 12
5 can be substantially minimized.

In the embodiment of Figure 15, a BPF 71 possessing a desirably narrow pass band for improving the resolution of the luminance signal "Y" has an input connected to the output terminal of the switching circuit 23 and an output terminal connected to the
10 second input of the subtracting circuit 26 to provide the separated chrominance signal thereto for producing the luminance signal "Y". Accordingly, the BPF 22 is unnecessary in the embodiment of Figure 15. A further switching circuit 72 is provided in parallel with the switching circuit 23 and has a first input connected to the output
15 terminal of the subtracting circuit 4 to receive the signal (X) therefrom, a second input connected to the output terminal of the subtracting circuit 5 to receive the signal (Y) therefrom and a third input connected to the output terminal of the adding circuit 28 to receive the signal (W) therefrom. The switching circuit 72 has an
20 output terminal connected to the output terminal 24 of the adaptive comb filter circuit to supply the chrominance signal "C" thereto. The switching circuit 72 is operative under the control of a correlation comparator 130 to select one of the input signals (W), (X) or (Y) to be supplied to the output terminal 24 as the output chrominance signal
25 "C". The correlation comparator 130 operates in the same fashion as the correlation comparator 120 of the embodiment of Figure 12, except that when the correlation comparator 130 controls the switching circuit 23 to select the input signal (Z) it simultaneously controls the switching circuit 72 to select the input signal (W) to be supplied as
30 the chrominance signal "C" at the output terminal 24.

It will be seen, therefore, that in the embodiment of Figure 15 the pass band of the BPF 71 may be selected to improve the resolution of the luminance signal accordingly without simultaneously narrowing the bandwidth of the chrominance signal at the output
35 terminal 24. Therefore, the pass bands of the BPFs 121, 122 and 123 may then be broadened to accommodate a chrominance signal of broader bandwidth. At the same time, the 2H comb filter output signal (W) is

selected by the switching circuit 72 as the output chrominance signal is lieu of the signal ② desirably to suppress the cross colour effect present in the signal ④.

Referring to Figure 16, it is noted initially that the phases of the chrominance signal components of the PAL composite colour video signal differ by 90° from each horizontal line to the next, so that a delay of two horizontal line periods thereof will provide a chrominance signal which is phase inverted with respect to a newly received chrominance signal, provided that the signals exhibit vertical correlation. In the embodiment of Figures 16, a first pair of signal delay circuits 2a and 2b each provides a signal delay substantially equal to one horizontal line interval. An input of the delay circuit 2a is connected to the input terminal 1 and an output terminal thereof is connected to an input of the delay circuit 2b is connected to the input of the delay line 25 as well as to the input of the BPF 122.

A second pair of delay circuits 3a and 3b is provided each serving to delay signals provided at a respective input thereof by one horizontal line interval. An input of the delay circuit 3a is connected to the output terminal of the delay circuit 2b and an output terminal of the delay circuit 3a is connected to an input of the delay circuit 3b. An output terminal of the delay circuit 3b is connected to the input terminal of the BPF 123. Accordingly, the first pair of delay circuits 2a and 2b together serve to delay a PAL composite colour video signal received at the input terminal 1 by two horizontal line intervals. Furthermore, the second pair of delay circuits 3a and 3b is operative to delay the PAL composite colour video signal at the output terminal of the delay circuit 2b by a further two horizontal line intervals, so the signal at the output terminal of the delay circuit 3b is delayed with respect to the input signal by four horizontal line intervals.

A signal correlation comparator 140 of the adaptive comb filter of Figure 16 serves to compare the values x , y , $|a|$, $|b|$ and $|c|$ input thereto to determine the selection of an input signal by each of the switching circuits 23 and 72 in accordance with the relationships set forth in Table VI below:

TABLE VI

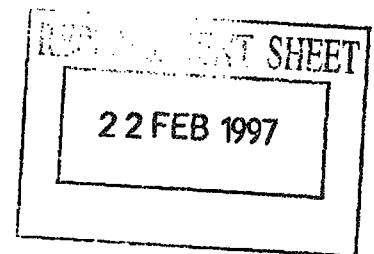
Relationship of input correlation signals	Selection of input signals by switching circuits 23 and 72
5 $y > 4x$	$\textcircled{X}$
$y < \frac{1}{4}x$	$\textcircled{Y}$
$\frac{1}{4}y \leq x \leq 4y$	Switch 23 selects signal $\textcircled{Z}$ Switch 72 selects signal $\textcircled{W}$
10 In other cases	$\textcircled{W}$

With reference to Table VI, it will be seen that the relative ratios employed by the correlation comparator 140 are selected as $\frac{1}{4}$ and 4, instead of $\frac{1}{2}$ and 2, in order more effectively to eliminate cross colour effects in the chrominance signal component of the PAL composite colour video signal.

The polarity of the colour sub-carrier of the PAL composite colour video signal differs from that of the NTSC signal. Since the signal correlation comparator 140 compares absolute values of the signals a, b and c, however, modifications need not be made to the correlation comparator 130 of the embodiment of Figure 15 to adapt it for use as the correlation comparator 140 of the Figure 16 embodiment specifically to accommodate such polarity differences.

With reference now to Figure 17, the amplitudes and phases of exemplary output signals from the BPFs 121, 122 and 123, are represented by the waveforms a, b and c, respectively, in the case where a chrominance signal first appears on a given horizontal line of the PAL composite colour video signal supplied at the input terminal 1, while the level of the luminance component thereof is constant.

With the exception of the logic signals $\textcircled{X}$ and $\textcircled{Y}$ (described below), the remaining waveforms illustrated in Figure 17 represent the phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. It will be seen from Figure 17 that when such chrominance signal components are applied to only one input of either of the subtracting circuits 4 and 5, their respective output signals $\textcircled{X}$ or $\textcircled{Y}$ are attenuated with respect to the input signal. Consequently, the selection of either of the signals $\textcircled{X}$



or $\textcircled{Y}$ without the use of an adaptive comb filtering technique in accordance with the operation of the embodiment of Figure 16 results in a loss of vertical resolution of the image produced with the use of either of the signals $\textcircled{X}$ or $\textcircled{Y}$ alone. However, the adaptive comb filter of Figure 16 advantageously selects the unattenuated one of signals $\textcircled{X}$ or $\textcircled{Y}$ under these circumstance such that vertical resolution is thereby improved. The enhanced separation of the chrominance signal component under these condition likewise results in a reduction of dot interference in the luminance signal component "Y" supplied at the output terminal 27.

The logic levels x and y of Figure 18 represent the signals $\textcircled{X}$ and $\textcircled{Y}$ which would be produced by an adaptive comb filter of the type illustrated Figure 2, but modified to separate components of PAL composite colour video signals, to provide a comparison of the operation of the embodiment of Figure 16 therewith. It will be seen that the Figure 16 embodiment provides the same degree of signal separation under these conditions as that of Figure 2, while providing the ability to operate reliably despite variations in input signal levels.

Referring to Figure 18, the phases of further exemplary output signals from the BPFs 121, 122 and 123 are represented by the waveforms a, b and c therein illustrated. In the example of Figure 18, a phase inversion of the chrominance signal components representing a change to a complementary colour is represented by a vertical dashed line in each of the waveforms a, b and c. The logic levels $\textcircled{X}$ and $\textcircled{Y}$ of Figure 18 represent signals which would be produced in the same fashion therefor as in the case of Figure 17 and the remaining waveforms represent the phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. In the example of Figure 18, the luminance signal component is constant.

When signals having the phases represented by the waveforms a, b and c of Figure 18 are supplied as inputs to the adding circuits 11 and 12, they combine subtractively to cancel one another so long as their phases are inverted. Since the phases of the chrominance signal components of the PAL composite colour video signal differ by 90° from each horizontal line to the next, chrominance signal components

exhibiting good vertical correlation will possess a 180° phase difference when separated by two horizontal line intervals. However, upon the occurrence of a colour change as described above, the phases of these signals when separated by two horizontal line intervals coincide, so that when such signals are applied simultaneously to the inputs of the subtracting circuits 4 and 5, their respective output signals, $\textcircled{X}$ and $\textcircled{Y}$, exhibit signal drop-out. When this occurs, it is detected by the correlation comparator 140 by a comparison of the magnitudes of the signals x and y so that, in accordance with the relationships therebetween expressed in the left-hand column of Table VI, the signal $\textcircled{X}$ or $\textcircled{Y}$ which does not exhibit drop-out is thereupon selected, so that the chrominance signal "C" output by the switch 72 under the control of the correlation comparator 140 likewise is then free of signal drop-out. Since the switching circuit 23 selects the same chrominance signal component under these circumstances, dot interference effects which would otherwise appear in the luminance signal "Y" as a consequence of such chrominance signal drop-outs are avoided.

With reference to Figure 19, the amplitudes and phases of exemplary output signals from the BPFs 121, 122 and 123, are represented by the waveforms a, b and c, respectively, thereof which depict luminance signal components within the frequency band of the chrominance signal, such that the phases thereof coincide on successive horizontal scan lines. The logic levels $\textcircled{X}$ and $\textcircled{Y}$ of Figure 19 represent signals which would be produced in the same fashion therefor as in the case of Figure 17 and the remaining waveforms represent phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. In the example of Figure 19, the luminance signal components appear on four successive scan lines, such that for four horizontal line periods during which the luminance signal components appear at the output terminal of the BPF 122 represented by the signal b in Figure 19, the conditions for the selection of the signal W obtain, so that a chrominance signal "C" as shown in Figure 19 is provided at the output terminal 24. Accordingly, under the circumstances illustrated in Figure 19, the adaptive comb filter of Figure 16 is able to derive a chrominance signal "C" in which cross colour effects produced by

22 FEB 1997

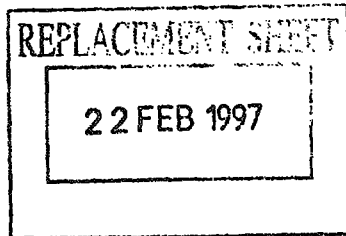
middle and high band components of the luminance signal are minimized, while possessing the ability to operate reliably despite changes in input signal levels.

With reference now to Figure 20, the waveforms a, b and c therein represent the existence of a chrominance signal on only one particular scanning line, while the level of the luminance signal is simultaneously constant. The logic levels (X) and (Y) of Figure 20 represent signals which would be produced in the same fashion therefor as in the case of Figure 17 and the remaining waveforms represent phases and amplitudes of the correspondingly labelled signals coincident with the signals a, b and c represented therein. Figure 21 illustrates an exemplary waveform of an input signal giving rise to the chrominance signal component illustrated in Figure 20. It will be seen from Figure 20 that, whether the signal (X) or (Y) is selected when this solitary chrominance signal appears at the output of the BPF 122, a deterioration in the level thereof is unavoidable. Accordingly, the use of either the signal (X) or the signal (Y) for producing the chrominance signal "Y" will necessarily result in dot interference.

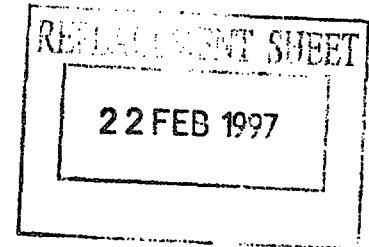
With reference to Table VI, it will be seen that, when the chrominance signal appears at the output of the BPF filter 122 as the signal b illustrated in Figure 20, the conditions for the selection of the signal (Z) by the switching circuit 23 are fulfilled, so that a luminance signal "Y" is produced at the output terminal 27 in which dot interference is substantially reduced.

Since embodiments of adaptive comb filter are operative to compare the relative magnitudes of input signal values, they are enabled reliably to determine the optimum mode of filter operation despite variations in input signal levels, while at the same time providing separated signal components of high quality in which cross effects are minimized,

It will be appreciated that the embodiments of adaptive comb filter may be implemented in whole or in part using either analogue or digital circuitry, and that all or part of the signal processing functions thereof may be carried out either by hardwired circuits or with the use of a microprocessor, microcomputer or the like.

CLAIMS

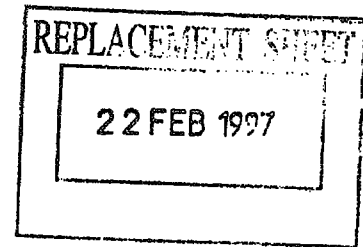
1. An adaptive comb filter for separating at least one component of a video signal, the filter comprising:
- 5 a filter input terminal (1) for receiving said video signal; first and second signal delay means (2, 3) for delaying signals supplied to respective input terminals thereof and providing respective delayed input signals at respective output terminals thereof, the input terminal of the first signal delay means (2) being
- 10 coupled to said filter input terminal (1) and the output terminal thereof being coupled to the input terminal of the second delay means (3);
- first and second signal subtracting means (4, 5) for each producing output difference signals at a respective output terminal thereof
- 15 representing a difference between signals supplied to a respective plurality of input terminals thereof, first and second input terminals of the first signal subtracting means (4) each being coupled to a respective one of the input and output terminals of said first signal delay means (2) and first and second input terminals of the second
- 20 signal subtracting means (5) each being coupled to a respective one of the input and output terminals of said second signal delay means (3);
- first signal adding means (28) for producing a summation signal at an output terminal thereof representing a summation of signals received at first and second input terminals thereof, said first and second
- 25 input terminals of said first signal adding means (28) being respectively coupled with said output terminals of said first and second signal subtracting means (4, 5);
- first switching means (23) for selectively providing one of a plurality of input signals received at respective input terminals
- 30 thereof to an output terminal thereof, said first switching means (23) having first and second input terminals respectively coupled to said output terminals of said first and second signal subtracting means (4, 5), a third input terminal coupled to the output terminal of said first signal adding means (28) and a fourth input terminal coupled to
- 35 the output terminal of said first signal delay means (2), the output terminal (24) of said switching means (23) being operative to provide a first component of said video signal;



second and third signal adding means (11, 12) for each producing a summation signal at a respective output terminal thereof representing a summation of signals received at respective first and second input terminals thereof, said first input terminal of each of said second and third signal adding means (11, 12) being coupled to the output terminal of said first signal delay means (2), said second input terminal of said second signal adding means (11) being coupled to said filter input terminal (1), and said second input terminal of said third signal adding means (12) being coupled to the output terminal of said second signal delay means (3);

first and second absolute value means (13, 16) for each producing an output signal at a respective output terminal thereof representing an absolute value of a signal received at a respective input terminal thereof, the input terminals of said first and second absolute value means (13, 16) being respectively coupled with the output terminals of said second and third signal adding means (11, 12), respectively; and controlling means (35) for controlling the selective provision of input signals by said first switching means (23) to its output terminal based upon a relative value of the output signals produced at the output terminals of said first and second absolute value means (13, 16).

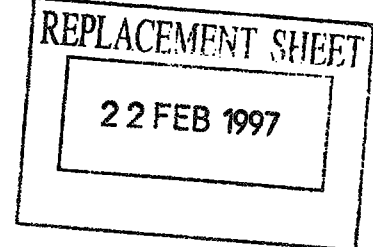
2. A filter according to claim 1 further comprising first, second and third band-pass filtering means (6, 8, 10) for providing output signals at respective output terminals thereof representing components of input signals received at the input terminals of the respective band-pass filtering means (6, 8, 10), such that components of said input signals outside respective pass bands of said first, second and third band-pass filtering means (6, 8, 10) are attenuated at the respective output terminals thereof, the input terminal of the first band-pass filtering means (6) being coupled to said filter input terminal (1) and the output terminal thereof being coupled to the second input terminal of said second signal adding means (11), the input terminal of said second band-pass filtering means (8) being coupled to the output terminal of said first signal delay means (2) and the output terminal thereof being coupled to the first input terminal of each of said second and third signal adding means (11,



12), and the input terminal of the third band-pass filtering means (10) being coupled to the output terminal of the second signal delay means (3) and the output terminal of said third band-pass filtering means (10) being coupled to the second input terminal of the third
5 signal adding means (12).

3. A filter according to claim 1 further comprising first and second low-pass filtering means (31, 34) for providing output signals at respective output terminals thereof representing components of
10 input signals received at respective input terminals thereof such that high frequency components of said input signals are attenuated at the respective output terminals of said low-pass filtering means (31, 34), the input terminals of said first and second low-pass filtering means (31, 34) being respectively coupled to the output terminals of said
15 first and second absolute value means (13, 16) respectively, and the output terminals of said first and second low-pass filtering means (31, 34) being coupled to said controlling means (35) to provide thereto low-pass filtered versions of the output signals produced at the output terminals of said first and second absolute value means
20 (13, 16).

4. A filter according to claim 1 further comprising third signal delay means (25) for delaying signals supplied to an input terminal thereof and providing the delayed input signals at an output terminal
25 thereof, the input terminal of said third signal delay means (25) being coupled to the filter input terminal (1), and third signal subtracting means (26) for producing output difference signals at an output terminal thereof representing a difference between signals supplied to first and second input terminals thereof, said first input
30 terminal of said third signal subtracting means (26) being coupled to the output terminal of said third signal delay means (25) and said second input terminal of said third signal subtracting means (26) being coupled to the output terminal of said first switching means (23), said third signal subtracting means (26) being operative to
35 provide a second component of said video signal at the output terminal thereof (27).

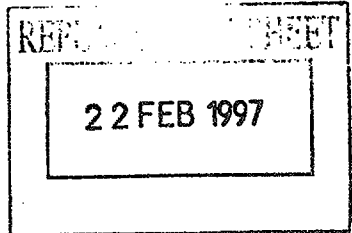


5. A filter according to claim 4 further comprising band-pass filtering means (39) for providing output signals at an output terminal thereof representing components of input signals received at an input terminal of said band-pass filtering means (39) such that
5 components of said input signals outside a pass band of said band-pass filtering means (39) are attenuated at the output terminal thereof, the input terminal of said band-pass filtering means (39) being coupled to the filter input terminal (1) and the output terminal of said band-pass filtering means (39) being coupled to the input
10 terminal of said first signal delay means (2).

6. A filter according to claim 1 wherein said first switching means (23) is operative to provide a chrominance signal component at the output terminal thereof as said first component of said video
15 signal, and further comprising chrominance band-pass filtering means (22) for providing output signals at an output terminal thereof representing components of input signals received at an input terminal of said chrominance band-pass filtering means (22) such that components of said input signals outside of a predetermined pass band
20 for a chrominance signal component of said video signal are attenuated at said output terminal of said chrominance band-pass filtering means (22), the input terminal of said chrominance band-pass filtering means (22) being coupled to the output terminal of said first signal delay means (2), and the fourth input terminal of said first switching means
25 (23) being coupled to the output terminal of said chrominance band-pass filtering means (22) such that the output terminal of said first signal delay means (2) is coupled to said fourth input terminal of said first switching means (23) through said chrominance band-pass filtering means (22).

30

7. A filter according to claim 6 wherein said controlling means (35) includes correlation detecting means for detecting horizontal correlation of luminance signal components in the summation signals produced at the output terminals of said second and third signal
35 adding means (11, 12) and wherein said controlling means (35) is operative to control said first switching means (23) to inhibit selection thereby of input signals from said chrominance band-pass



filtering means (22) upon detection of said horizontal correlation of luminance signal components in said summation signals.

8. A filter according to claim 1 wherein said first switching means (23) is operative to provide a chrominance signal component at the output terminal thereof as said first component of said video signal, said adaptive comb filter further comprising luminance separation means for separating a luminance component of said video signal, said luminance separation means including:

10 third signal delay means (25) for delaying said video signal by an interval substantially equal to a signal delay between said video signal and said chrominance signal component provided at the output terminal of said first switching means (23) to provide a delayed video signal at an output terminal of said third signal delay means (25);

15 and

signal subtraction means (26) for attenuating chrominance signal components in said delayed video signal by subtractive combination therewith of said chrominance signal component provided at the output terminal of said first switching means (23) to provide a separated

20 luminance signal component of said video signal at an output terminal (27) of said signal subtraction means (26).

9. A filter according to claim 8 further comprising second switching means (72) for selectively providing one of a plurality of

25 input signals received at respective input terminals thereof to an output terminal (24) of said second switching means (72), said second switching means (72) having first and second input terminals coupled to the output terminals of said first and second signal subtracting means (4, 5), respectively, and a third input terminal coupled to the

30 output terminal of said first signal adding means (28), said second switching means (72) being operative to provide a chrominance signal component of said video signal at the output terminal (24) thereof; and

wherein said controlling means (130) is operative to control said

35 second switching means (72) to select an input signal received at the third input terminal thereof as said chrominance signal component of said video signal when said controlling means (130) controls said



22 FEB 1997

first switching means (23) to select an input signal received at the fourth input terminal thereof.

10. A filter according to claim 1 wherein said controlling means
5 (130) is operative to control the selective provision of input signals by said first switching means (23) to its output terminal based upon a comparison of the magnitudes of the output signals produced at the output terminals of said first and second absolute value means (13, 16).

10

11. An adaptive comb filter for separating at least one component of a video signal, the filter comprising:

a filter input terminal (1) for receiving said video signal;

15 first delay means (2) for delaying said video signal by a first interval to produce a first delayed video signal;

second delay means (3) for delaying said video signal by a second interval to produce a second delayed video signal;

20 first difference means (4) for producing a first difference signal proportional to a difference between said video signal and said first delayed video signal;

second difference means (5) for producing a second difference signal proportional to a difference between said first delayed video signal and said second delayed video signal;

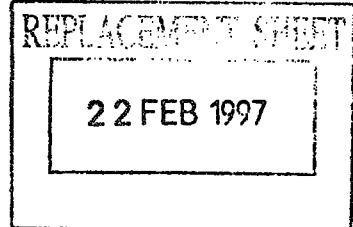
25 first summing means (28) for producing a first summation signal proportional to a summation of said first difference signal and said second difference signal;

30 first signal selecting means (23) for controllably selecting one of said first difference signal, said second difference signal and said first summation signal as said at least one component of said video signal;

second summing means (11) for producing a second summation signal proportional to a summation of said video signal and said first delayed video signal;

35 third summing means (12) for producing a third summation signal proportional to a summation of said first delayed video signal and said second delayed video signal; and

control means (35) for controlling the selection of one of said first



difference signal, said second difference signal and said first summation signal by said first signal selecting means (23) based upon relative magnitudes of said second summation signal and said third summation signal.

5

12. A filter according to claim 11 wherein said control means (35) further comprises absolute value means (13, 16) for producing first and second absolute value signals proportional to the absolute values of the magnitudes of said second summation signal and said
10 third summation signal, respectively, and wherein said control means (35) is operative to compare the magnitudes of said first and second absolute value signals for controlling the selection of said at least one component of said video signal.

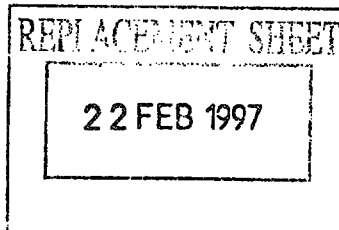
15 13. A filter according to claim 12 further comprising low-pass filtering means (31, 34) for attenuating high frequency components of said first and second absolute value signals.

14. A filter according to claim 11 wherein said control means
20 (35) is operative to control the selection of said at least one component of said video signal based upon relative magnitudes of said first difference signal and said second difference signal.

15. A filter according to claim 11 wherein said control means
25 (35) is operative to control the selection of said at least one component of said video signal based upon relative magnitudes of said video signal, said first delayed video signal and said second delayed video signal.

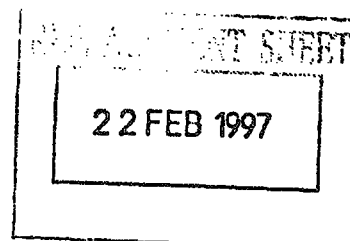
30 16. A filter according to claim 11 wherein said first signal selecting means (23) is operative to provide a chrominance signal as said at least one component of said video signal.

17. A filter according to claim 16 wherein said first delay means
35 (21) is operative to delay said video signal by an interval substantially equal to one horizontal line interval of said video signal, and said second delay means (3) is operative to delay said



video signal by an interval substantially equal to two horizontal line intervals of said video signal.

18. A filter according to claim 16 wherein said first delay means
5 (2) is operative to delay said video signal by an interval substantially equal to two horizontal line intervals of said video signal, and said second delay means (3) is operative to delay said video signal by an interval substantially equal to four horizontal line intervals of said video signal.
- 10 19. A filter according to claim 16 further comprising first band-pass filtering means (8) for filtering said first delayed video signal to attenuate components thereof outside a first chrominance signal pass band to produce a band-pass filtered signal, and wherein said
15 first signal selecting means (23) is operative to select one of said band-pass filtered signal, said first difference signal, said second difference signal and said first summation signal as said chrominance signal.
- 20 20. A filter according to claim 19 further comprising luminance separation means for separating a luminance component of said video signal, said luminance separation means including:
second band-pass filtering means (39) for filtering said chrominance
25 signal pass band to produce a band-pass filtered chrominance signal, said second chrominance signal pass band being relatively narrower than said first chrominance signal pass band of said first band-pass filtering means (6);
third delay means (25) for delaying said video signal by a third
30 interval substantially equal to a signal delay between said video signal and said band-pass filtered chrominance signal to produce a delayed video signal; and
subtractive combining means (26) for producing said luminance
35 video signal with the use of said band-pass filtered chrominance signal.



21. A filter according to claim 16 further comprising luminance separation means for separating a luminance component of said video signal, said luminance separation means including:
third delay means (25) for delaying said video signal by a delay interval substantially equal to a signal delay between said video signal and said chrominance signal to produce a third delayed video signal; and
subtractive combining means (26) for producing said luminance component by attenuating chrominance signal components of said third delayed video signal with the use of the chrominance signal provided by said first signal selecting means (23).
22. A filter according to claim 21 wherein said third delay means (25) is operative further to delay said first delayed video signal to provide said third delayed video signal.
23. A filter according to claim 21 further comprising first band-pass filtering means (22) for filtering said video signal received at said filter input terminal (1) to attenuate components thereof outside a chrominance signal pass band to produce a band-pass filtered signal, said first band-pass filtering means (22) being coupled to said first delay means (2), said second delay means (3) and said first difference means (4) to provide said band-pass filtered signal thereto as said video signal, and wherein said third delay means (25) is coupled to said filter input terminal to receive said video signal therefrom.
24. A filter according to claim 19 wherein said control means (35) includes correlation detecting means for detecting horizontal correlation of luminance signal components in said second summation signal and said third summation signal, and wherein said control means (35) is operative to control said first signal selecting means to inhibit selection of said band-pass filtered signal upon detection of said horizontal correlation of luminance signal components.
25. A filter according to claim 16 further comprising first, second and third band-pass filtering means (121, 122, 123) for attenuating components of said video signal, said first delayed video

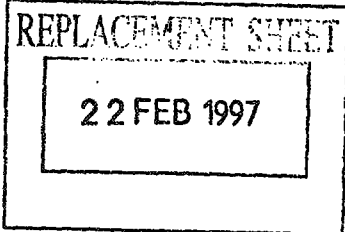


22 FEB 1997

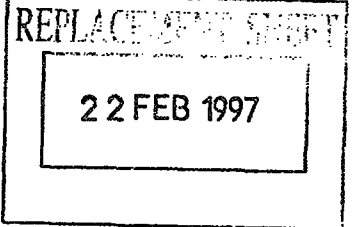
signal and said second delayed video signal, respectively, which are outside a chrominance signal pass band, the first band-pass filtering means (121) having an input terminal coupled to said filter input terminal (1) and an output terminal coupled with said difference means (4) and said second summing means to provide the band-pass filtered video signal thereto, the second band-pass filtering means (122) having an input terminal couple to said first delay means (21) and an output terminal coupled to said first difference means (4), said second difference means (5), said second summing means (11) and said third summing means (12) to provide the band-pass filtered first delayed video signal thereto, and the third band-pass filtering means (123) having an input terminal coupled to said second delay means (3) and an output terminal coupled to said second difference means (5) and said third summing means (121) to provide the band-pass filtered second delayed video signal thereto.

26. A filter according to claim 21 wherein said first signal selecting means (23) is operative to select one of said first delayed video signal, said first difference signal, said second difference signal and said first summation signal as said chrominance signal, said adaptive comb filter further comprising second signal selecting means (72) for controllably selecting one of said first difference signal, said second difference signal and said first summation signal as an output chrominance signal to be supplied by said adaptive comb filter; and wherein said control means (130) is operative to control said second signal selecting means (72) to select said first summation signal as said output chrominance signal when said control means (130) controls said first signal selecting means (23) to select said first delayed video signal.

27. A filter according to claim 26 further comprising band-pass filtering means (71) for attenuating components of said chrominance signal provided by said first signal selecting means (23) outside of a chrominance signal pass band and coupled to said subtractive combining means (26) to supply the band-pass filtered chrominance signal thereto.



28. A method of separating a chrominance signal from a composite colour video signal, the method comprising the steps of :
- delaying said composite colour video signal by a first interval to produce a first delayed video signal;
- 5 delaying said composite colour video signal by a second interval to produce a second delayed video signal;
- producing a first difference signal proportional to a difference between said composite colour video signal and said first delayed video signal;
- 10 producing a second difference signal proportional to a difference between said first delayed video signal and said second delayed video signal;
- producing a first summation signal proportional to a summation of said first difference signal and said second difference signal;
- 15 producing a second summation signal proportional to a summation of said composite colour video signal and said first delayed video signal;
- producing a third summation signal proportional to a summation of said first delayed video signal and said second delayed video signal; and
- 20 selecting one of said first difference signal, said second difference signal and said first summation signal as said chrominance signal based upon a comparison of relative magnitudes of said second summation signal and said third summation signal.

ABSTRACTADAPTIVE COMB FILTERS AND METHODS OF
SEPARATING VIDEO SIGNAL COMPONENTS

5

An adaptive comb filter for separating at least one component of a video signal includes a filter input terminal (1) for receiving the video signal, a first delay device (2) for delaying the video signal by a first interval to produce a first delayed video signal, a
10 second delay device (3) for delaying the video signal by a second interval to produce a second delayed video signal, a first signal subtractor (4) for producing a first difference signal proportional to a difference between the video signal and the first delayed video signal, a second subtractor (5) for producing a second difference
15 signal proportional to a difference between the first delayed video signal and the second delayed video signal, a first signal summing device (28) for producing a first summation signal proportional to a summation of the first difference signal and the second difference signal, a signal selecting device (23) for controllably selecting one
20 of the first difference signal, the second difference signal and the first summation signal as the separated component of the video signal, a second summing device (11) for producing a second summation signal proportional to a summation of the video signal and the first delayed video signal, a third summing device (12) for producing a third
25 summation signal proportional to a summation of the first delayed video signal and the second delayed video signal, and a controller (35) for controlling the selection of the separated component of the video signal by the signal selecting device based upon relative magnitudes of the second summation signal and the third summation
30 signal. In addition a method of separating a chrominance signal from a composite colour video signal is provided.

Figure 9

22 FEB 1997

1/23

FIG. 1

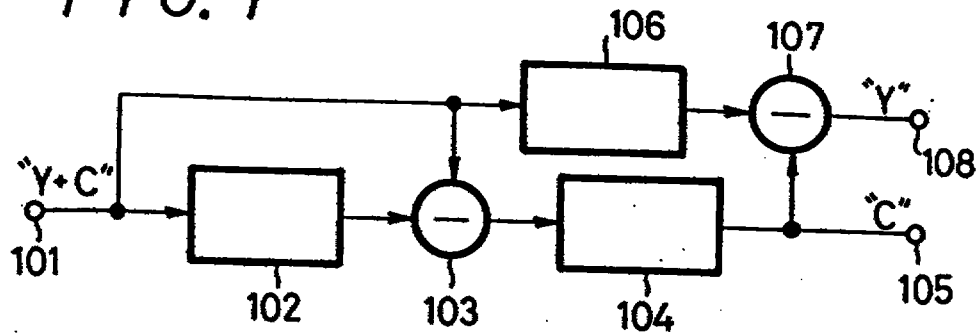
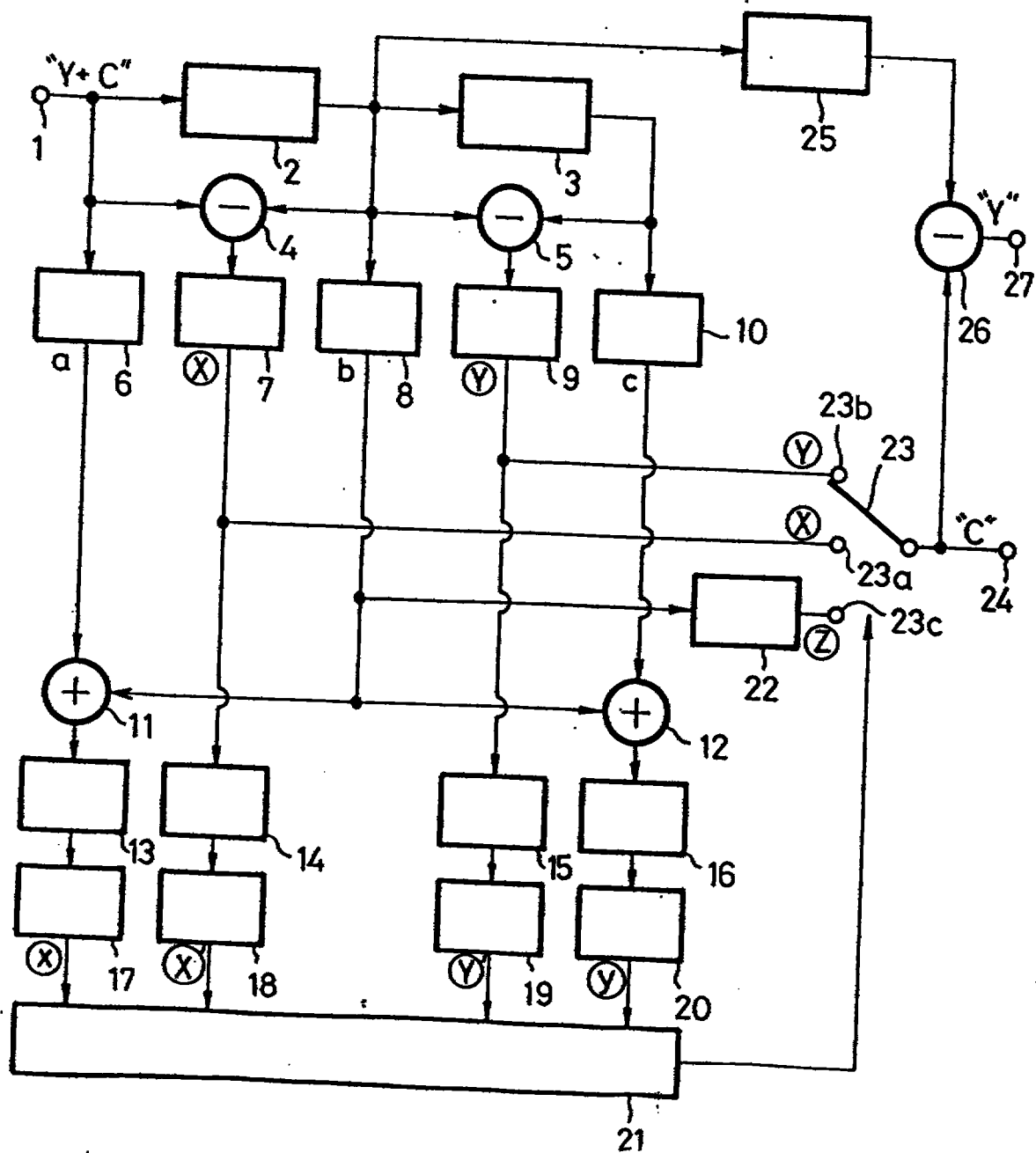
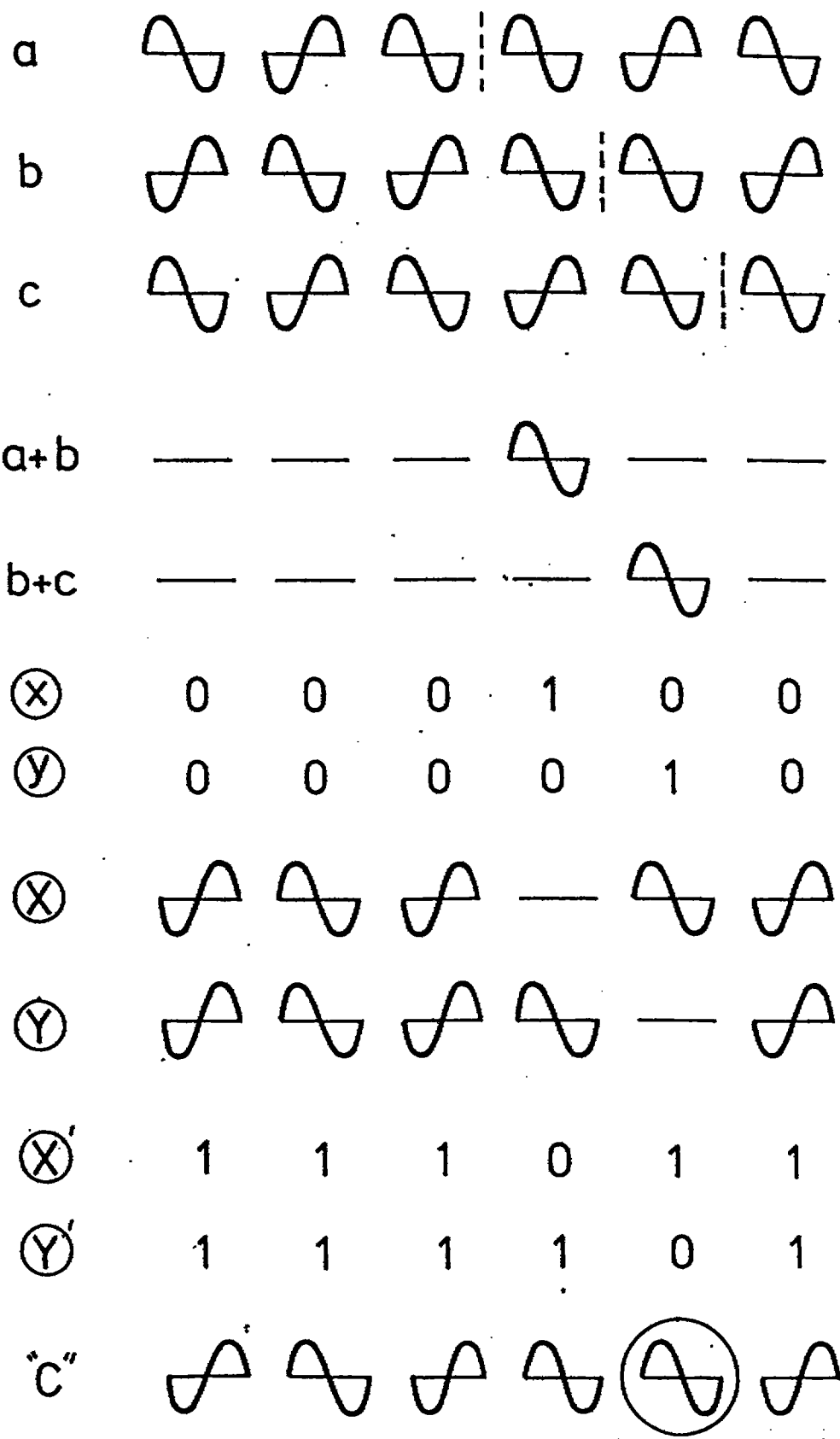


FIG. 2

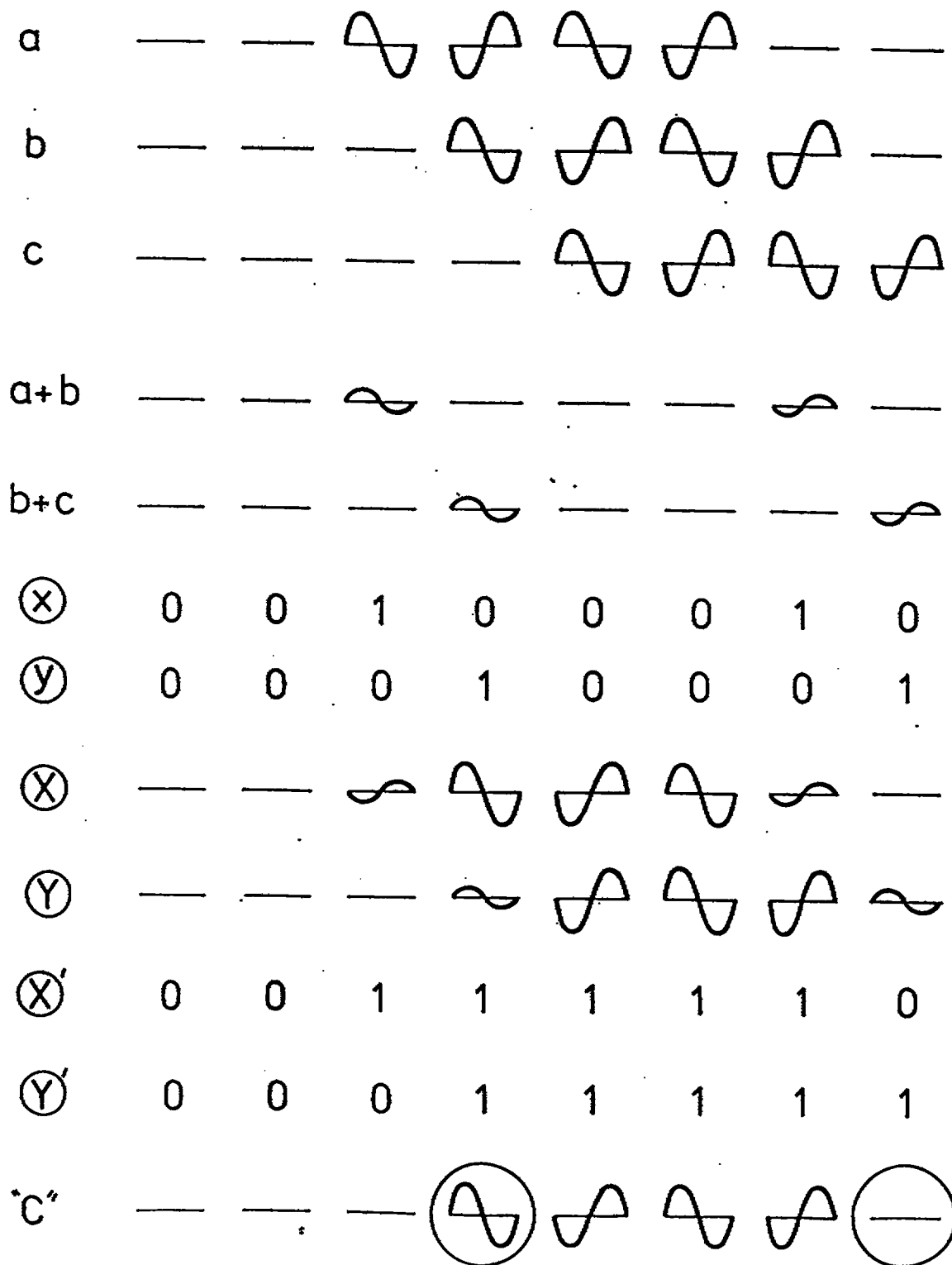


2/23
FIG. 3



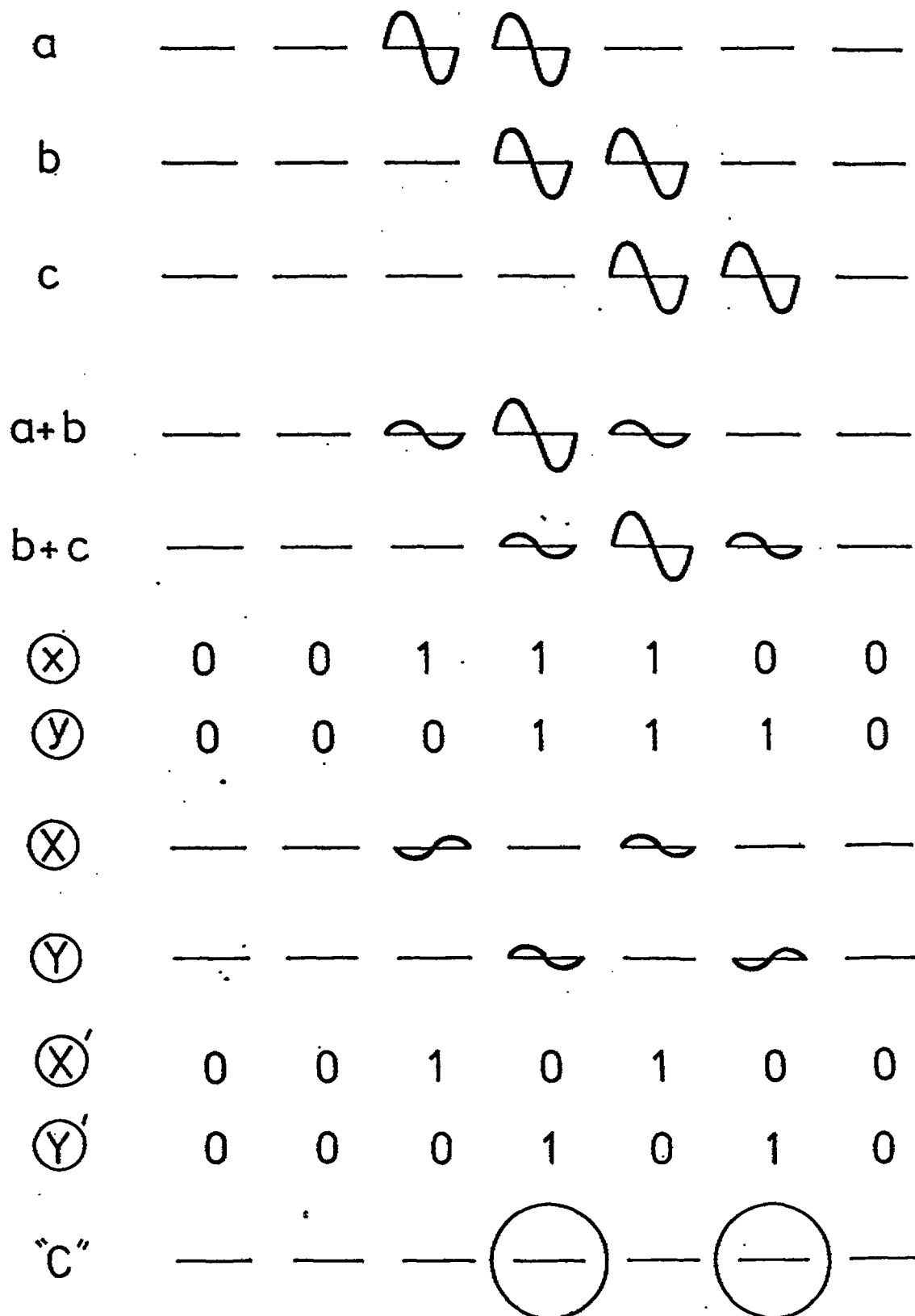
3/23

FIG. 4



22 FEB 1997

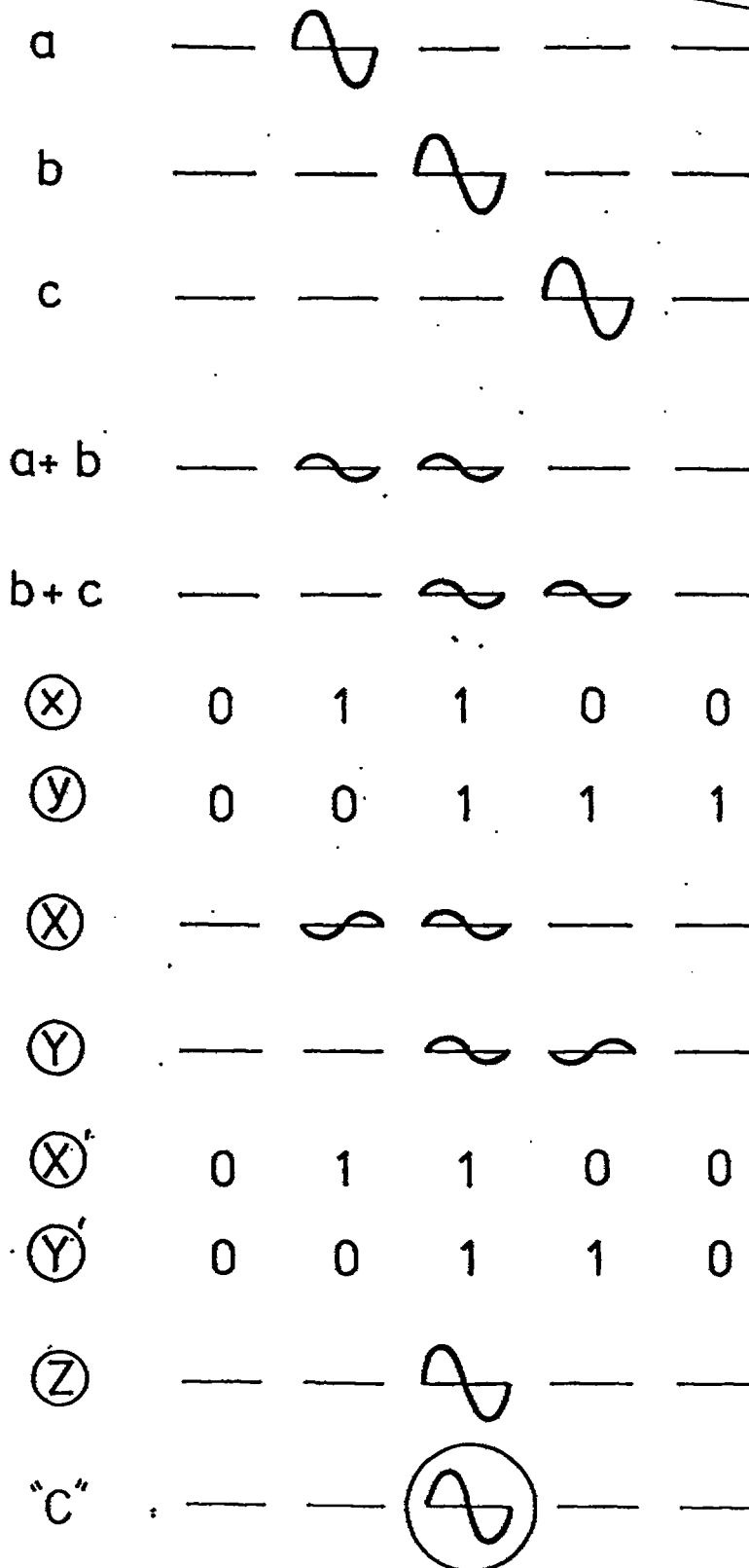
4/23
FIG. 5



REPLACEMENT SHEET
22 FEB 1997

5/23

FIG. 6



6/23

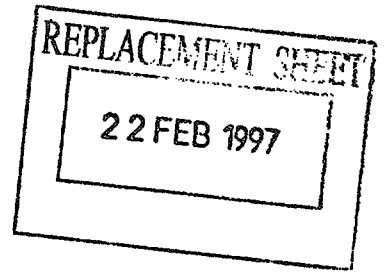
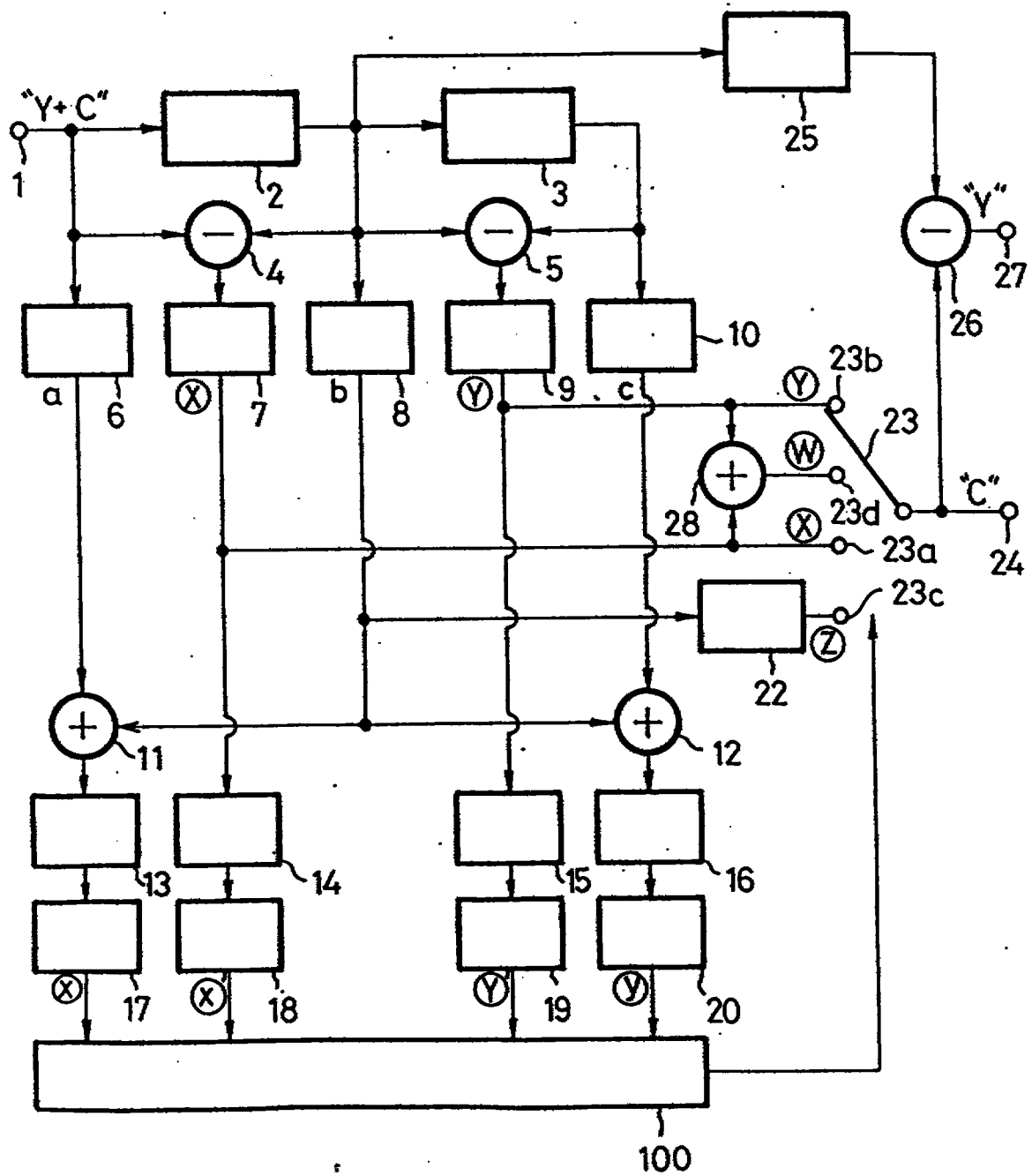
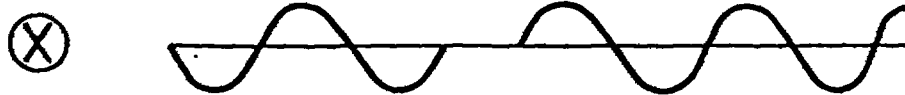
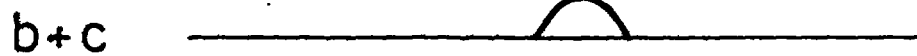


FIG. 7



22 FEB 1997

FIG. 8



8/23

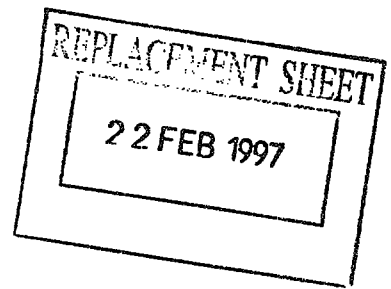
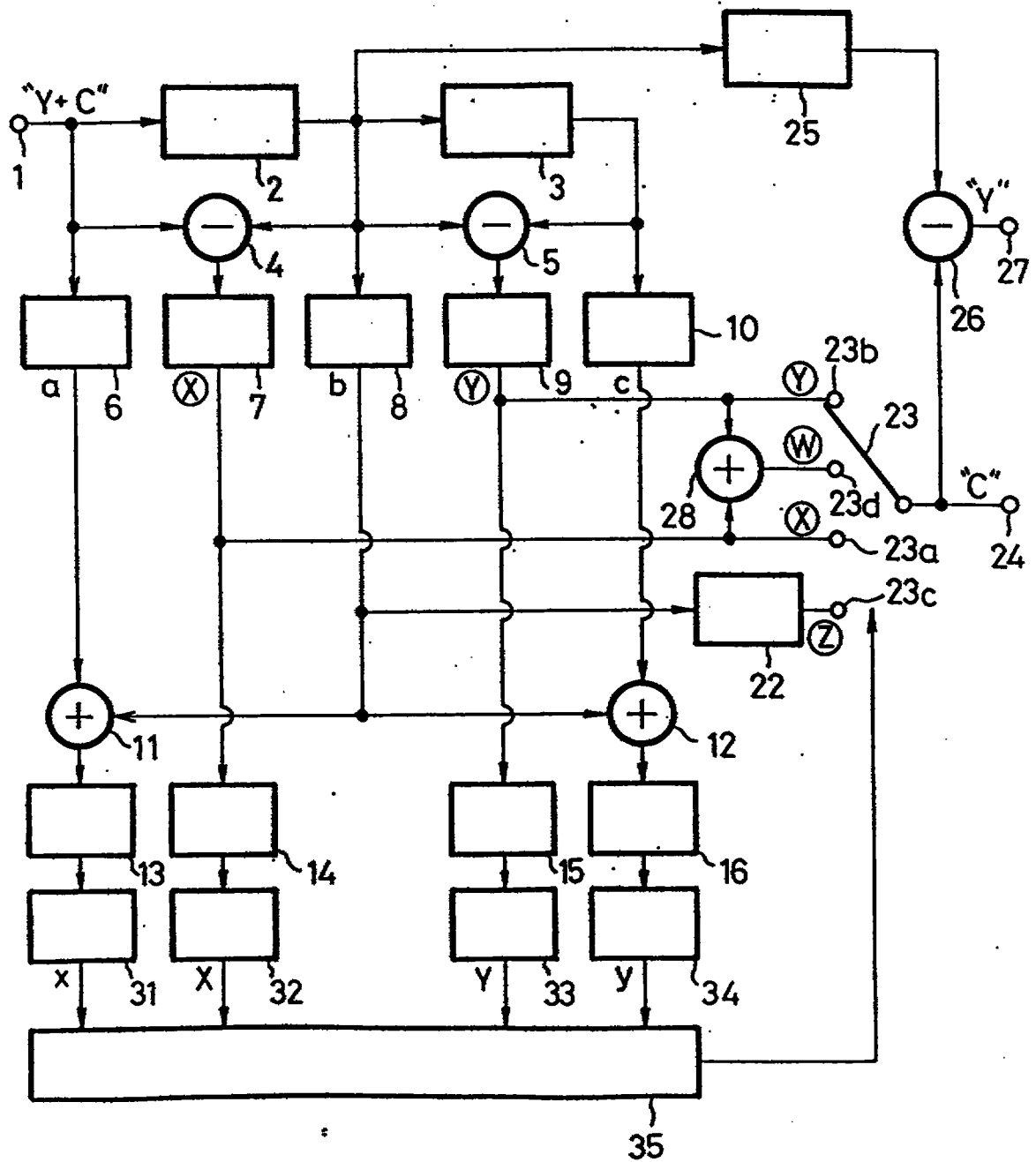


FIG. 9



671

10/23

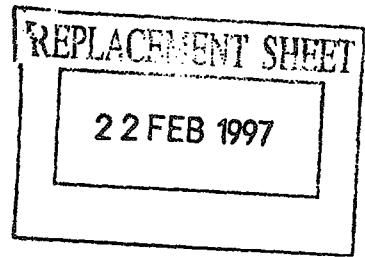
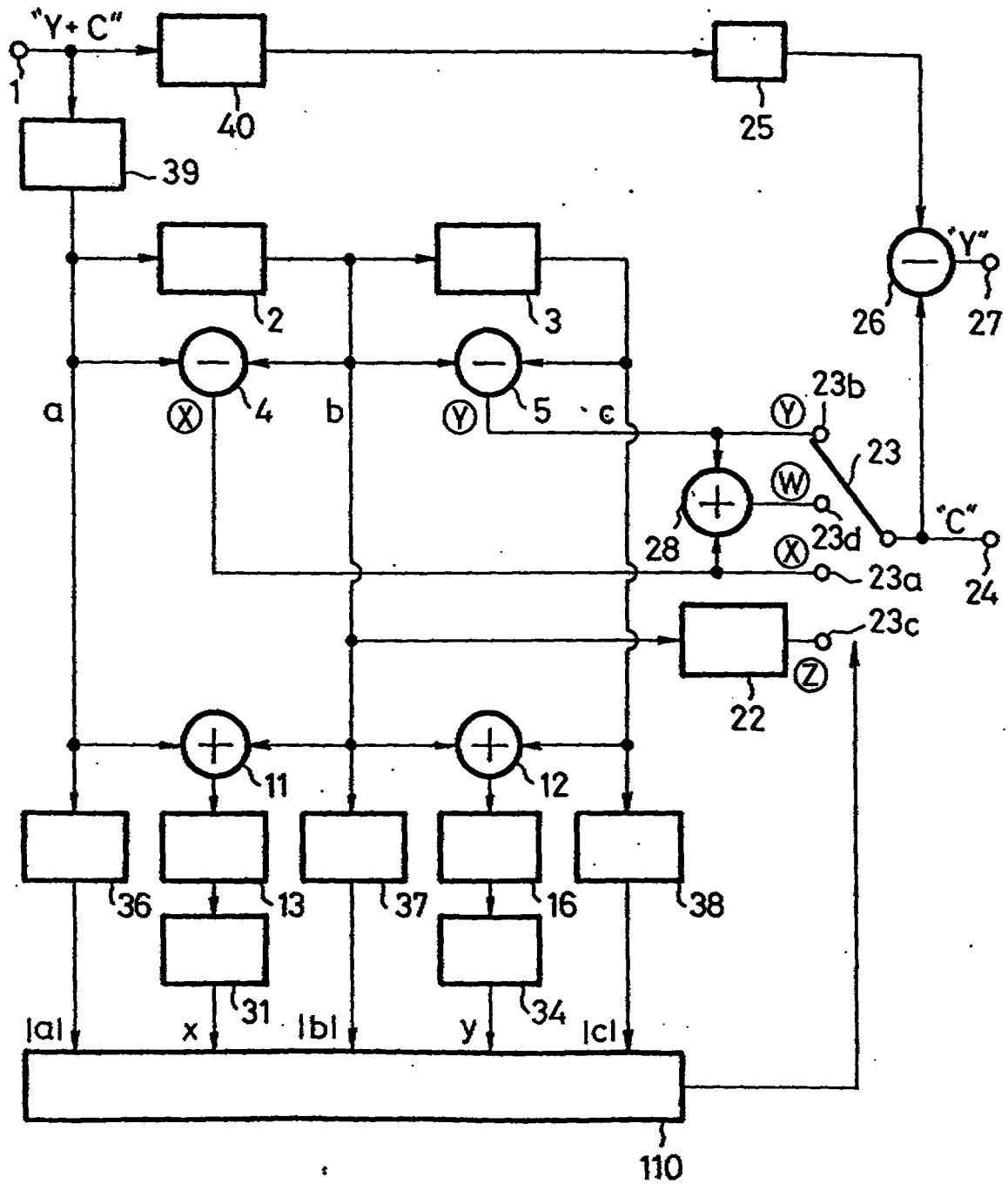
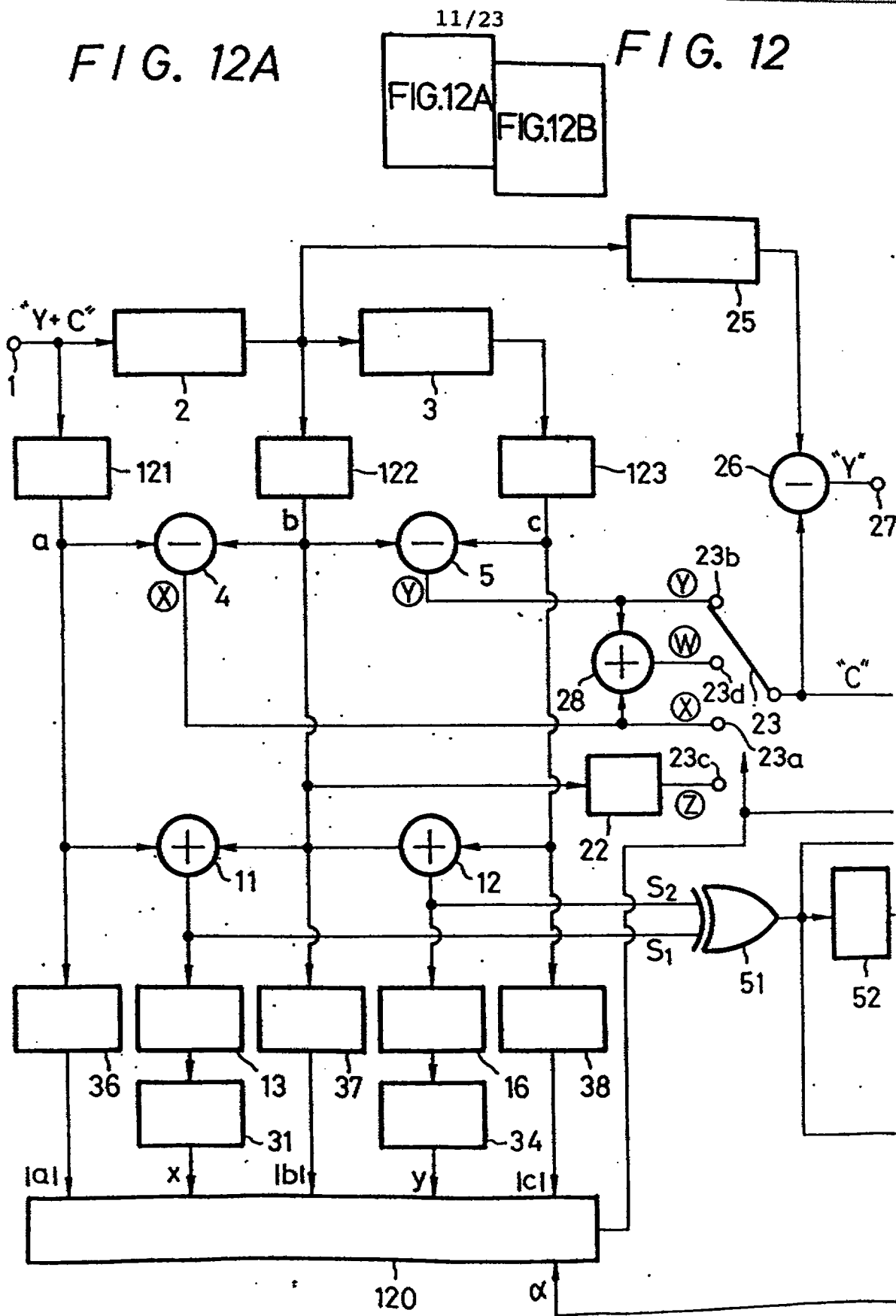


FIG. 11





22 FEB 1997

FIG. 12A

FIG. 12

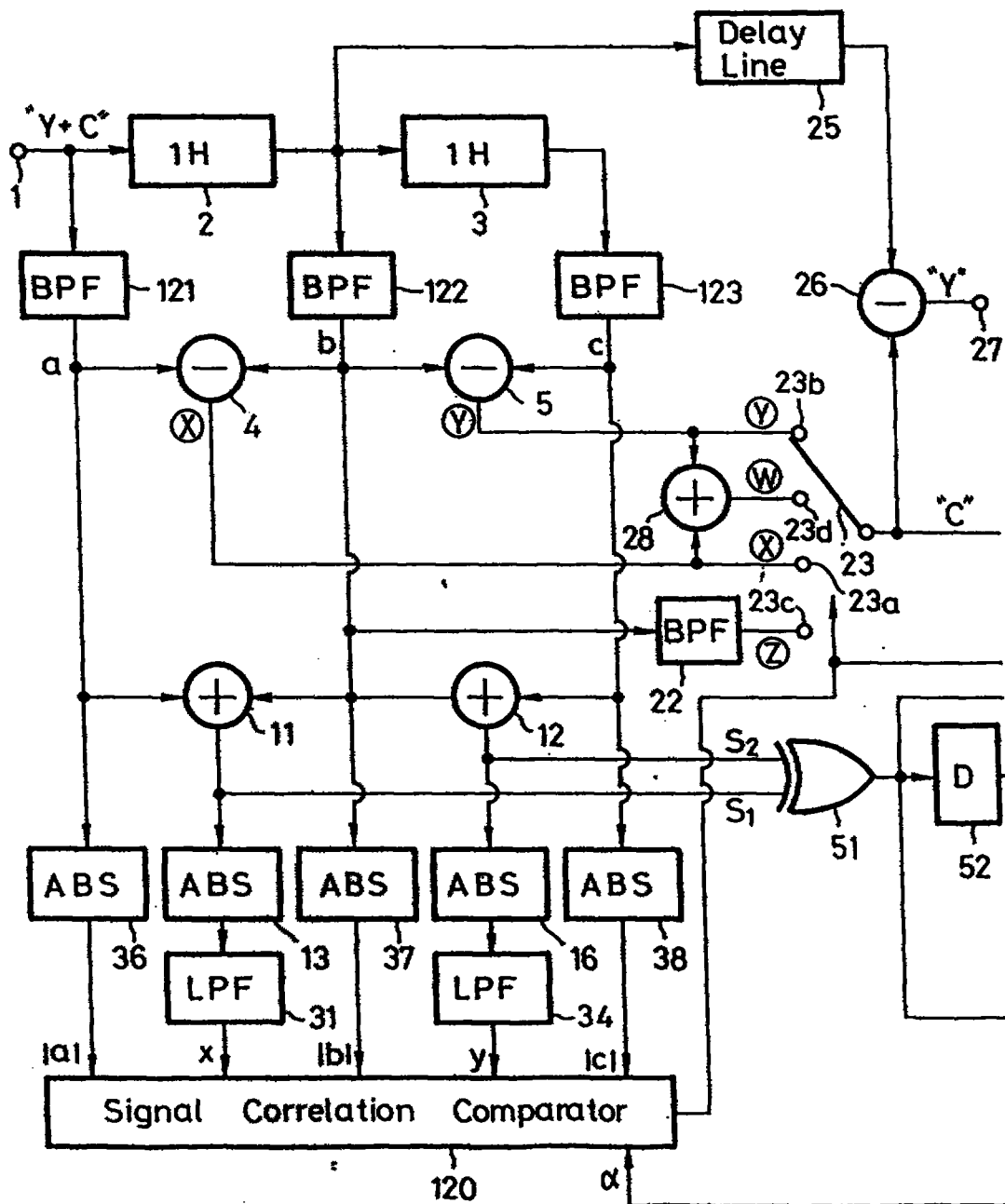
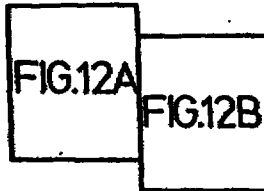


FIG. 12B

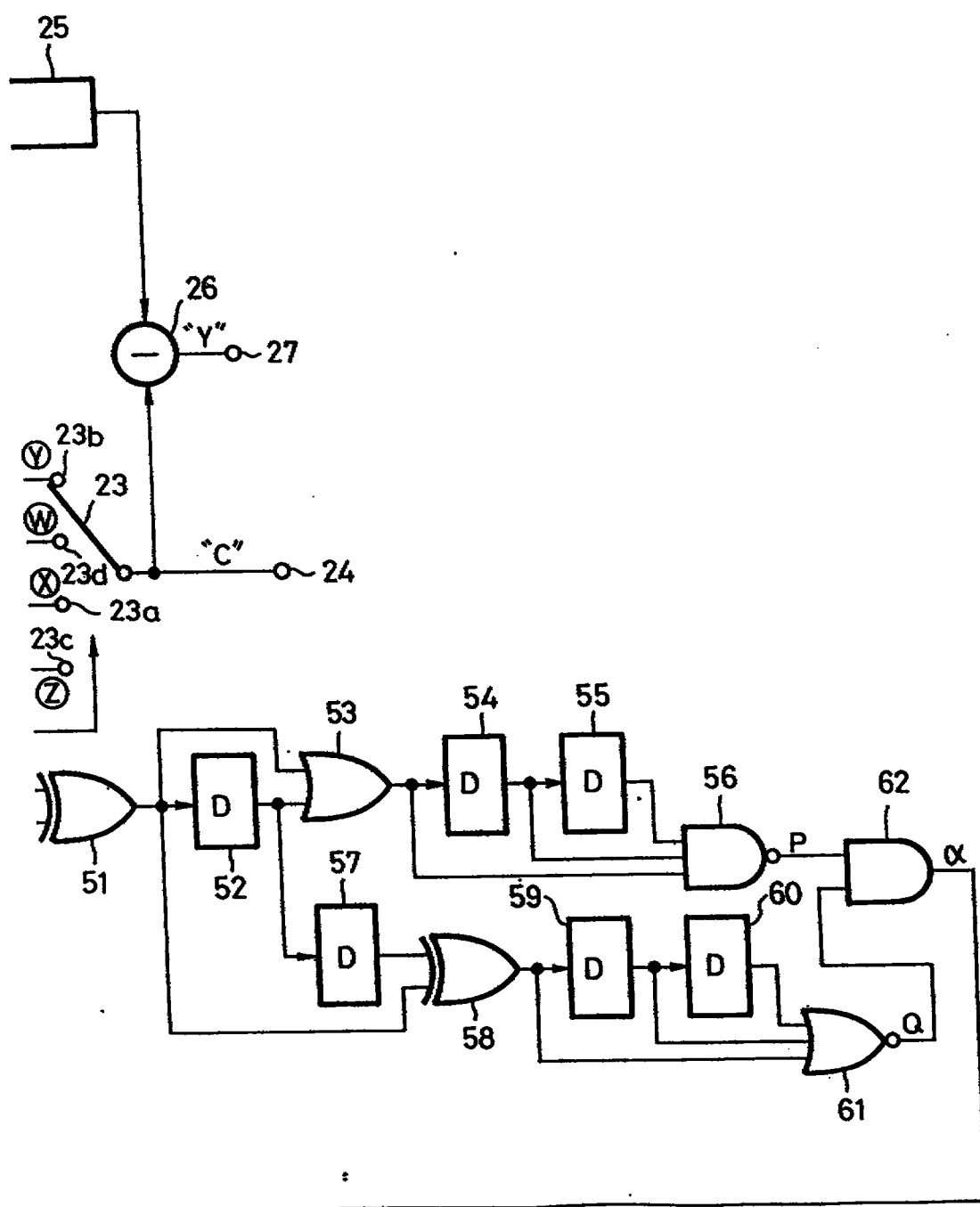
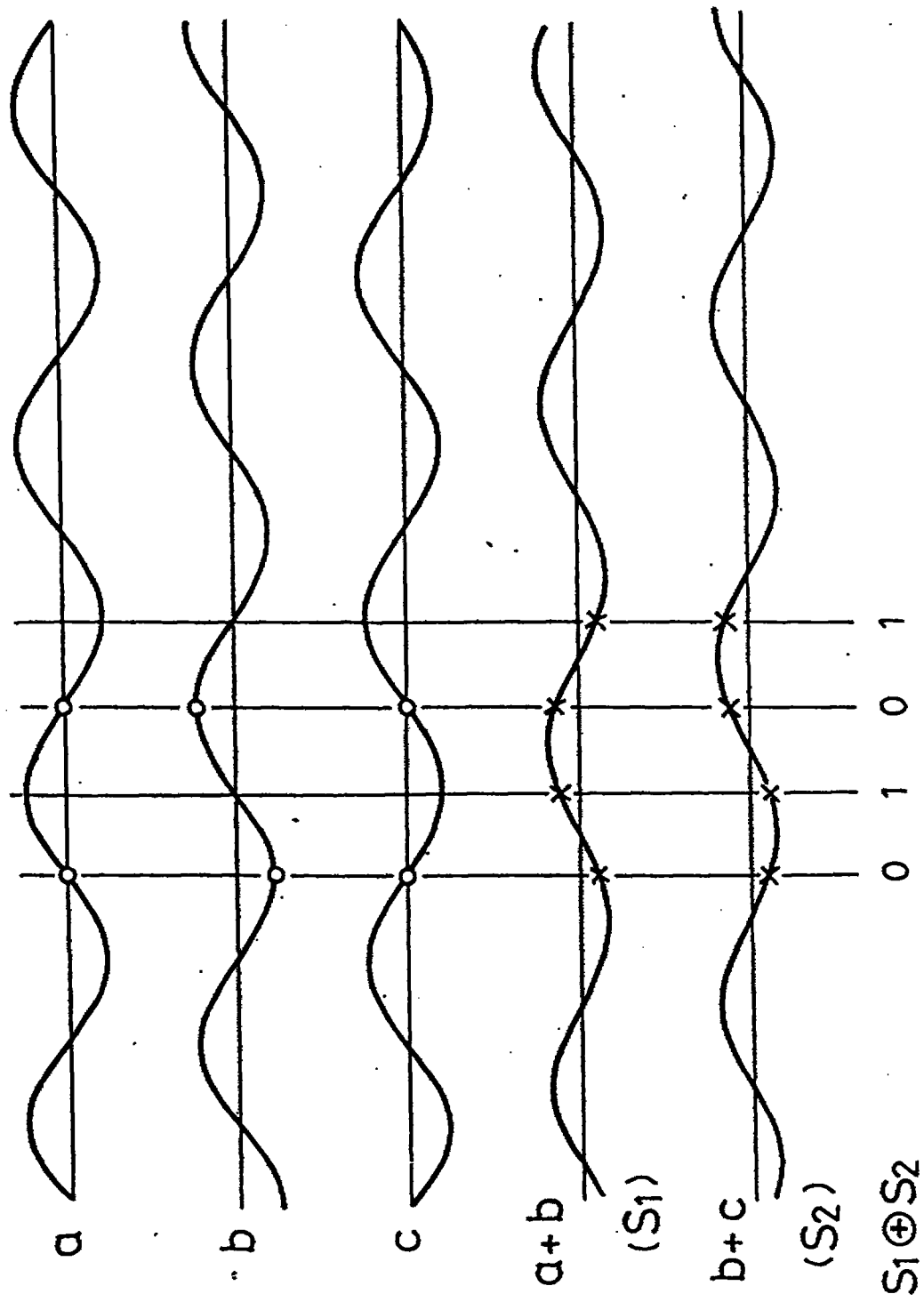


FIG. 13



15/23

FIG. 14

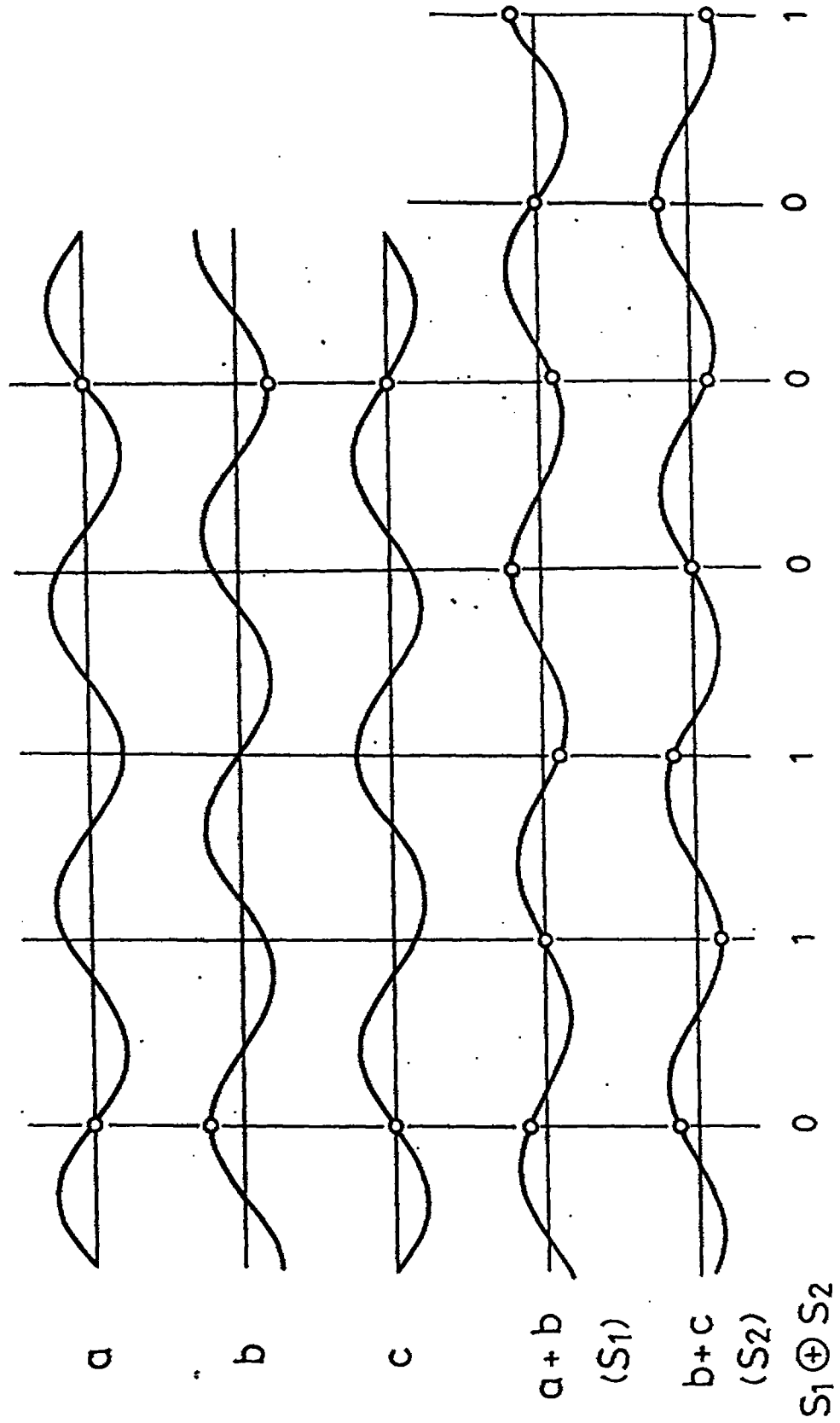


FIG. 15A

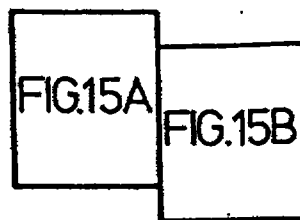


FIG. 15

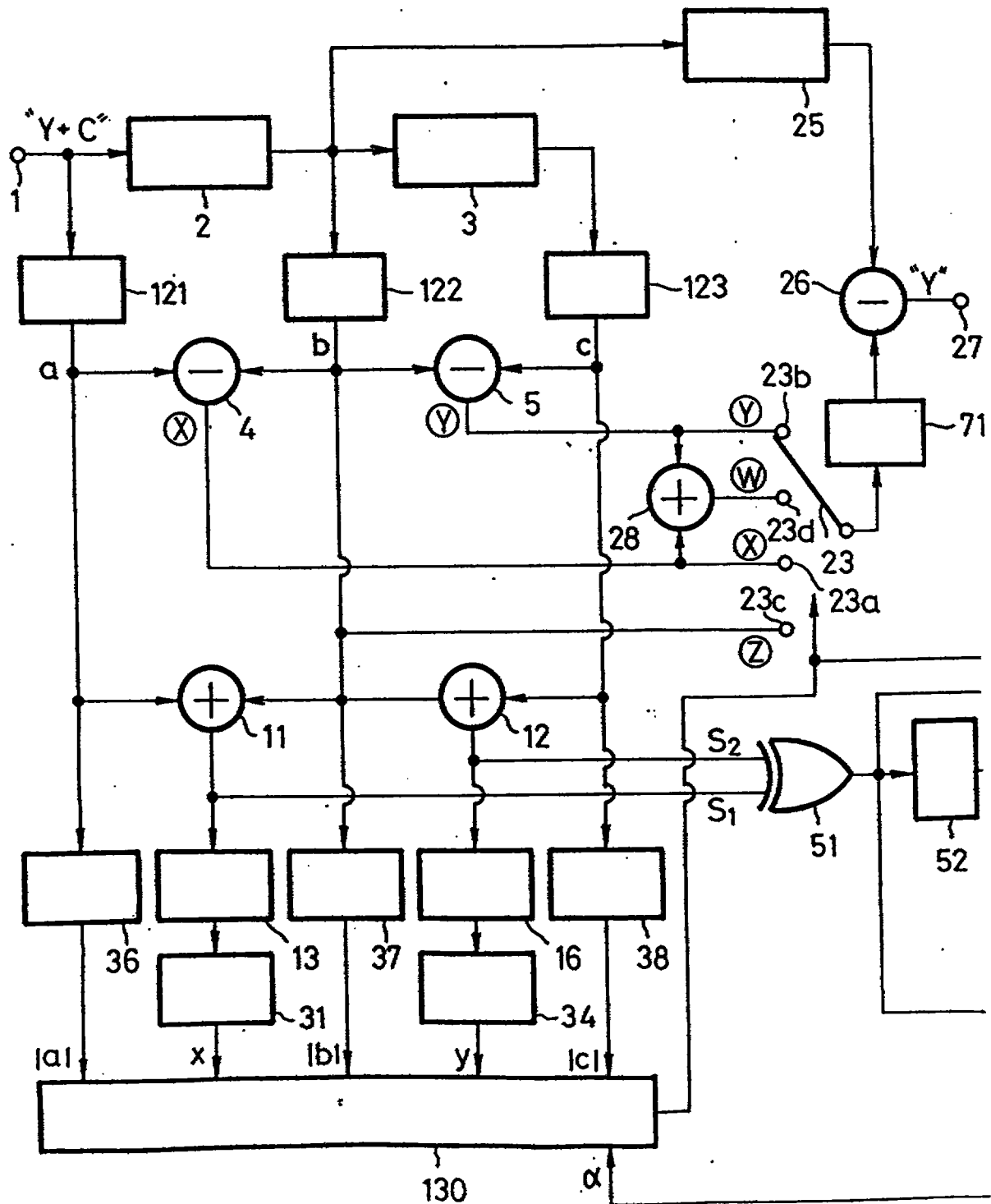
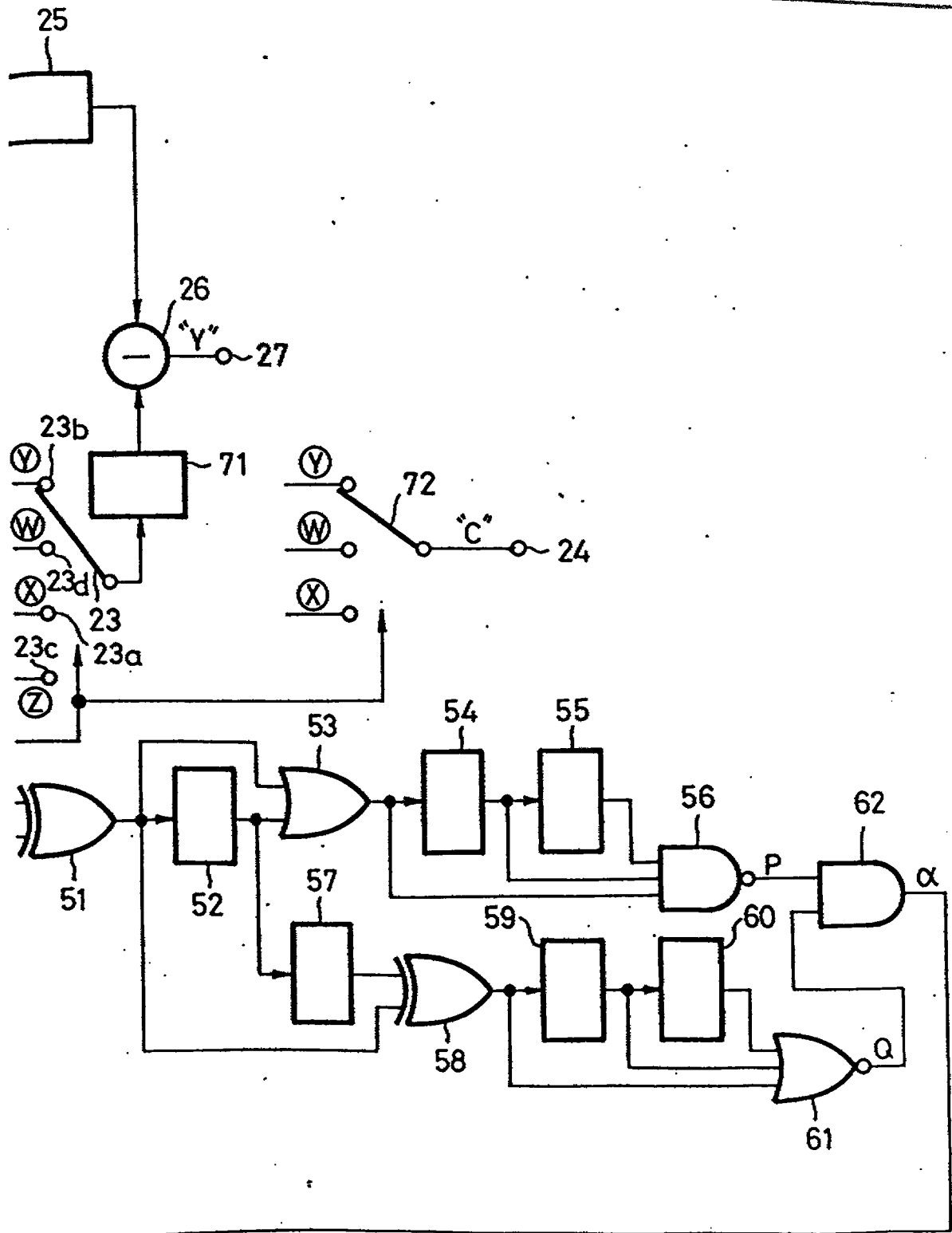


FIG. 15B



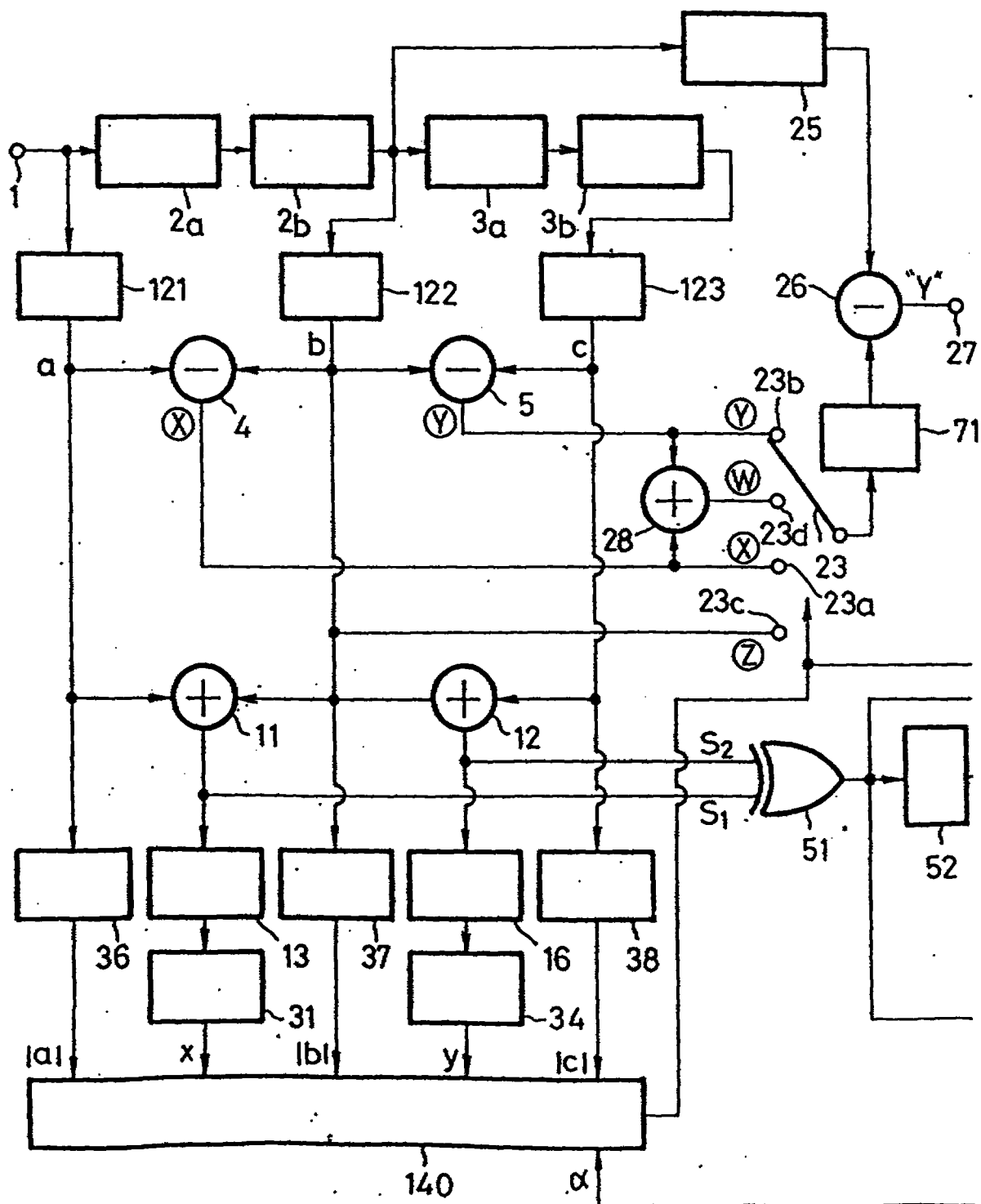
18/23

FIG. 16A

FIG. 16A

FIG. 16B

FIG. 16



REPLACEMENT SHEET

22 FEB 1997

FIG. 16B

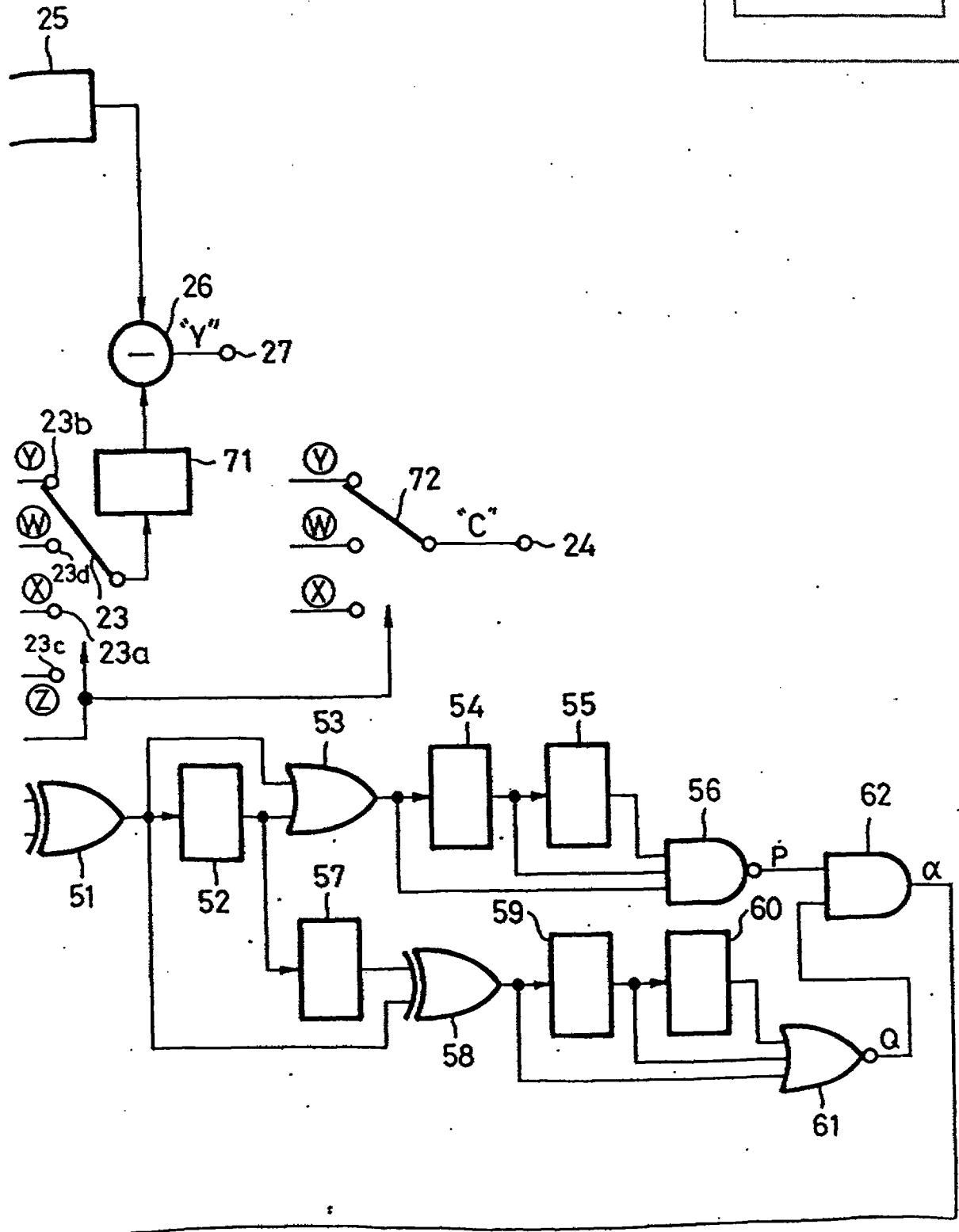


FIG. 17

a	—	∞	∞	∞	—
b	—	—	∞	∞	—
c	—	—	—	∞	—
a+b	—	∞	—	—	—
b+c	—	—	—	—	—
⊗	0	1	0	0	0
⊙	0	0	1	0	0
⊗	—	∞	∞	∞	—
⊙	—	—	∞	∞	—
"c"	—	—	∞	∞	—

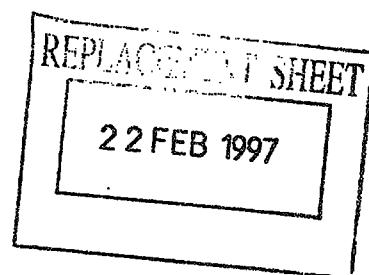
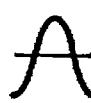
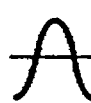
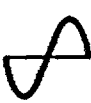
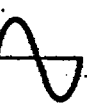
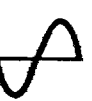


FIG. 18

a   |      

b     |    

c       |   

a+b — —   — — — — —

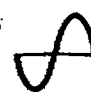
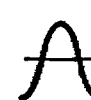
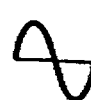
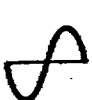
b+c — — — —   — — — — —

⊗ 0 0 1 1 0 0 0 0 0

⊙ 0 0 0 0 1 1 0 0 0

⊗   — —     

⊙     — —   

"c"         

— — — — —

— — — — —

— Δ Δ Δ — — — — — 3

$$a+b \quad \infty \quad \infty \quad \infty \quad \infty \quad \infty \quad \infty$$

— — — — —
b+c

$$\begin{pmatrix} 0 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 0 & 0 \\ \otimes & 0 & 1 & 1 & 1 & 1 & 1 & 1 & 0 & 0 \end{pmatrix}$$

⑦	0	0	0	1	1	1	1	1	0
---	---	---	---	---	---	---	---	---	---

1
1
1
8
8
1
1
8
8
1
⊗

|
 §
 §
 |
 |
 §
 §
 |
 |
 |
 ⊕

— — — 8 8 8 8 — — — 0

1000

REPLACEMENT SHEET
22 FEB 1997

FIG. 20

23/23

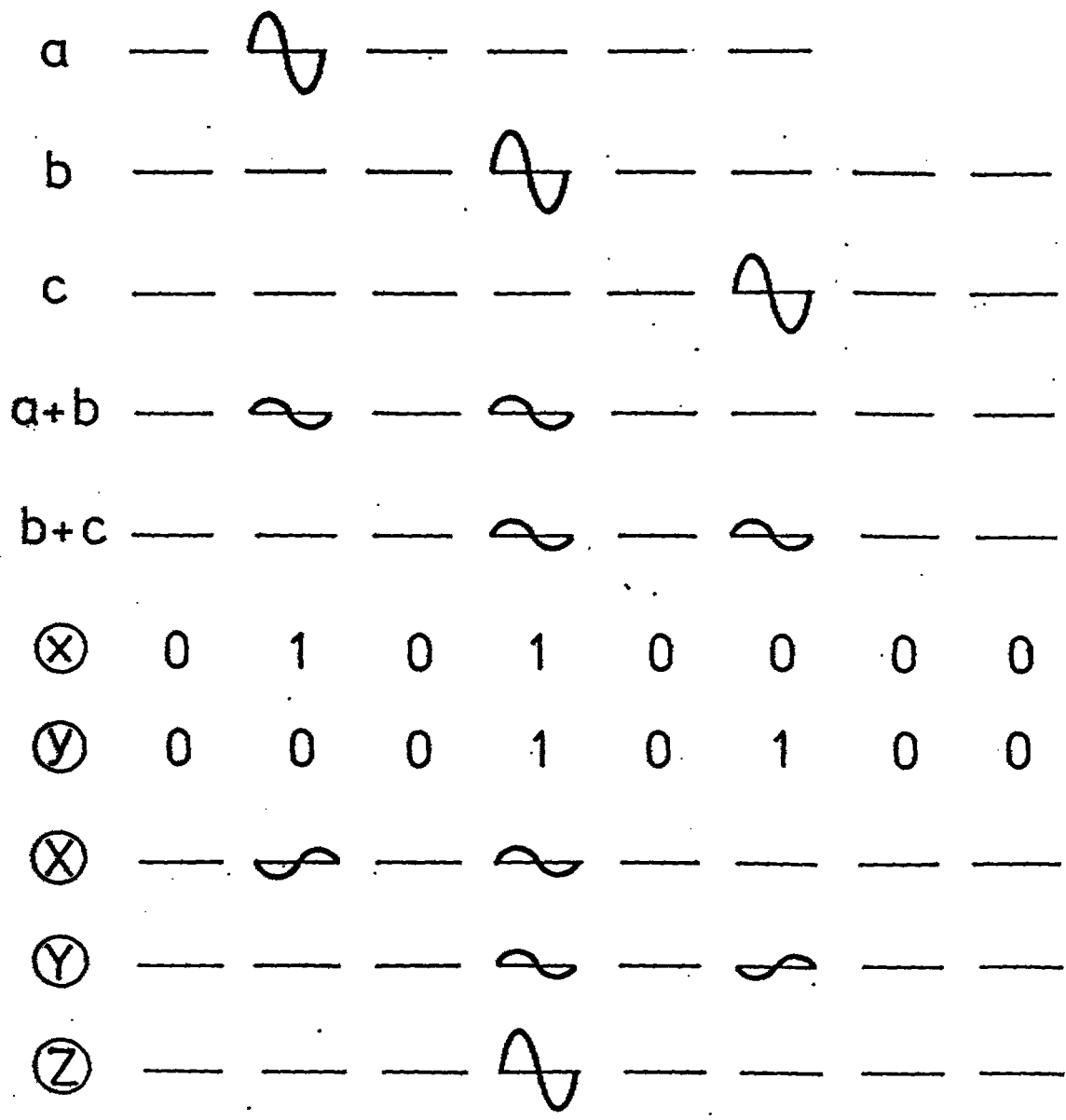


FIG. 21

