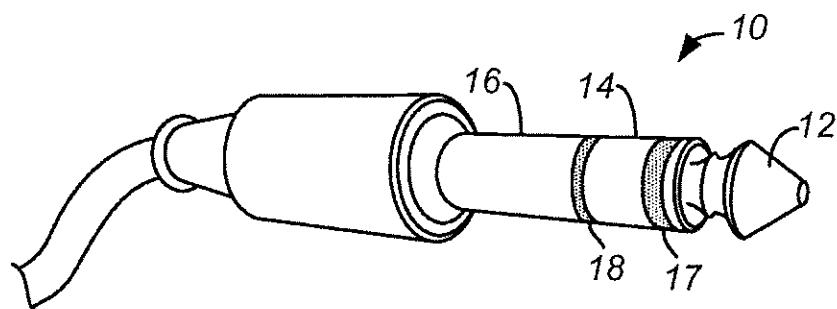
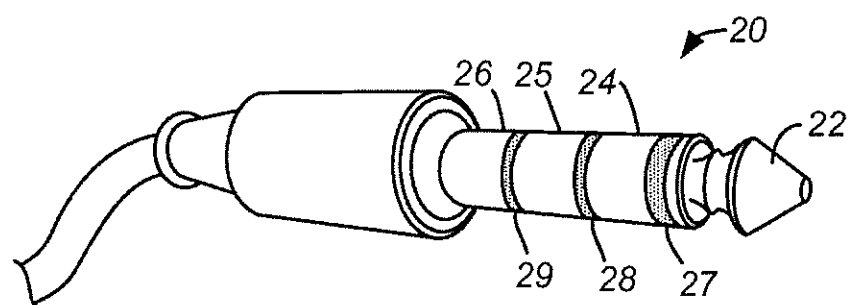
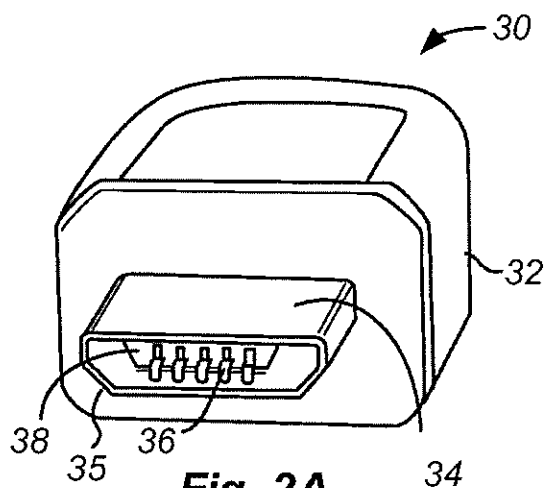
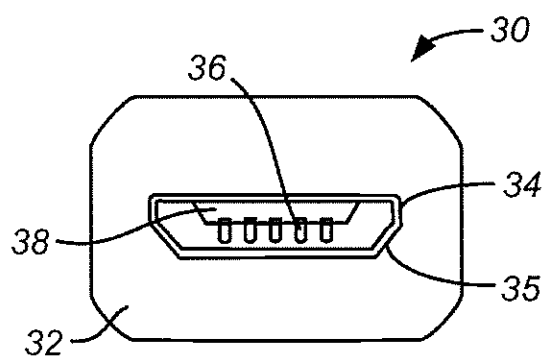
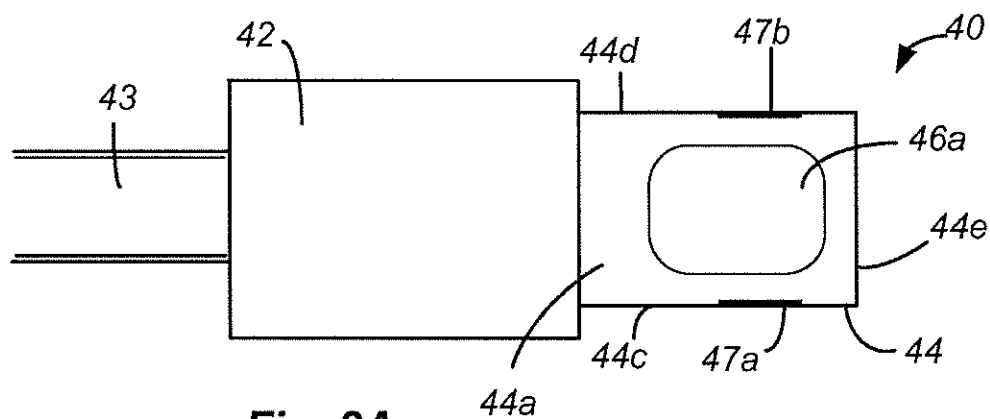


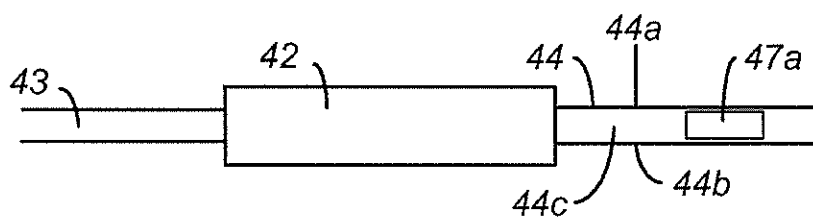
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**Fig. 1A****Fig. 1B****Fig. 2A****Fig. 2B**

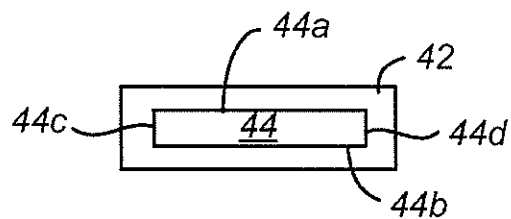
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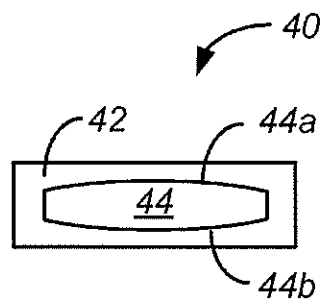
**Fig. 3A**



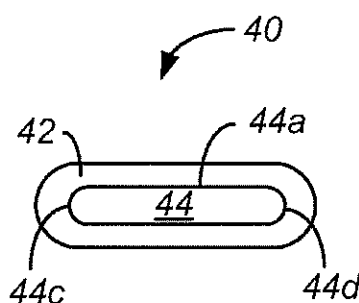
**Fig. 3B**



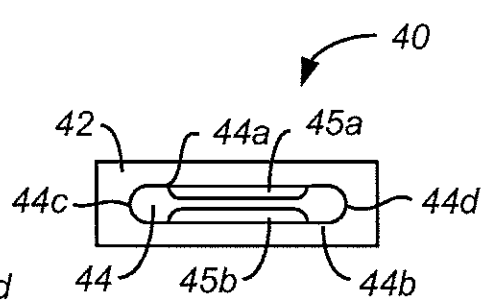
**Fig. 3C**



**Fig. 4A**

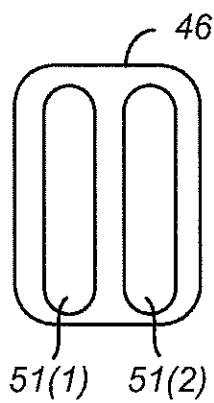
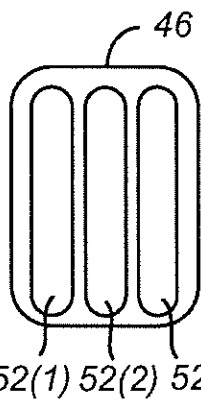
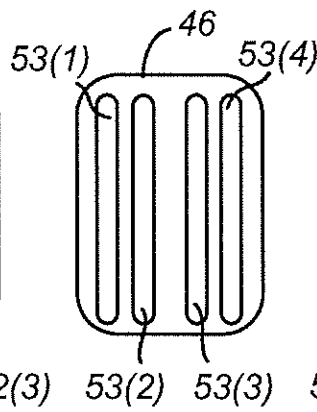
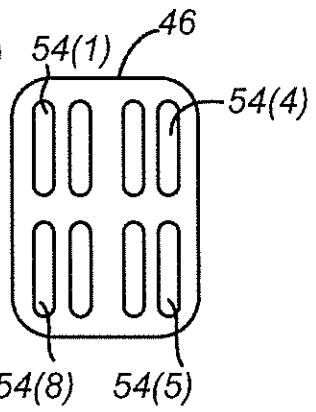
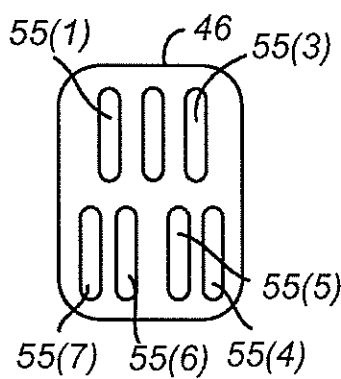
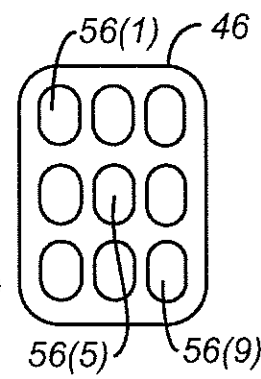
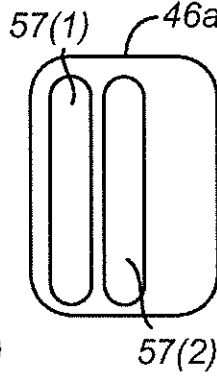
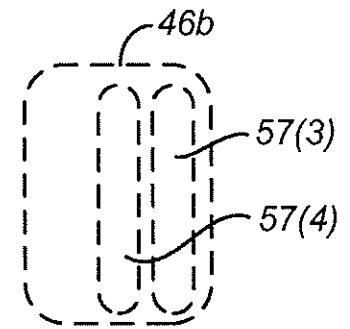
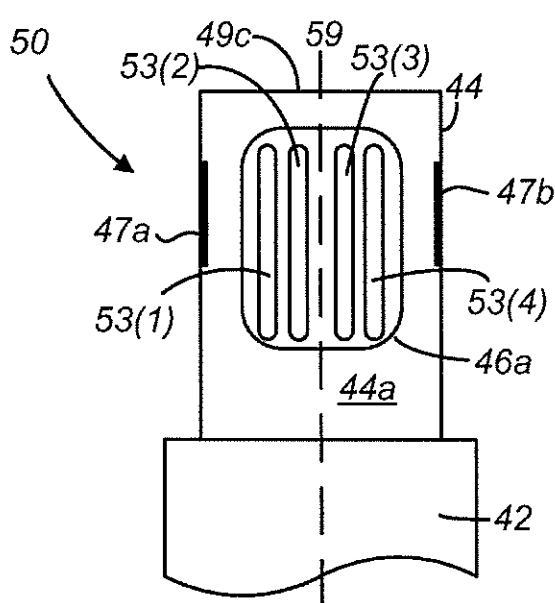
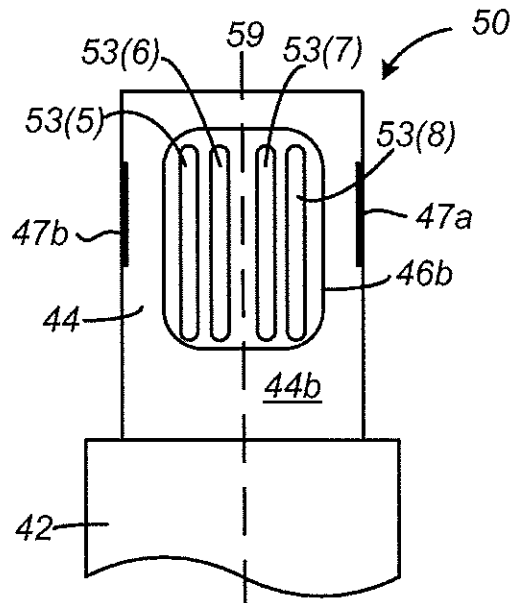


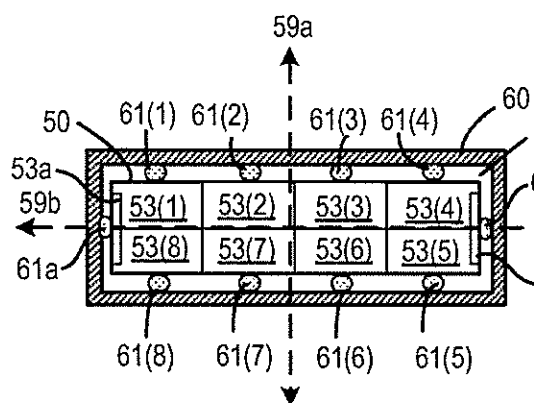
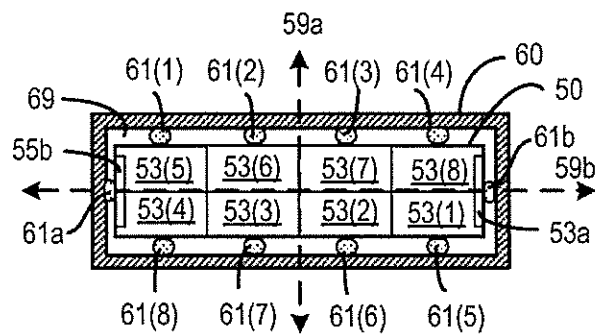
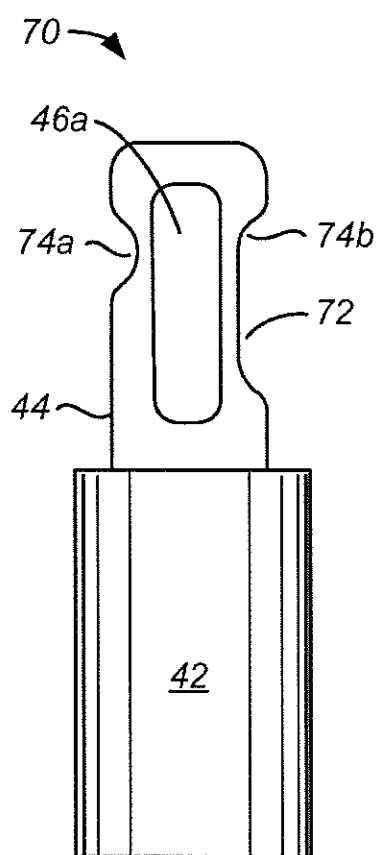
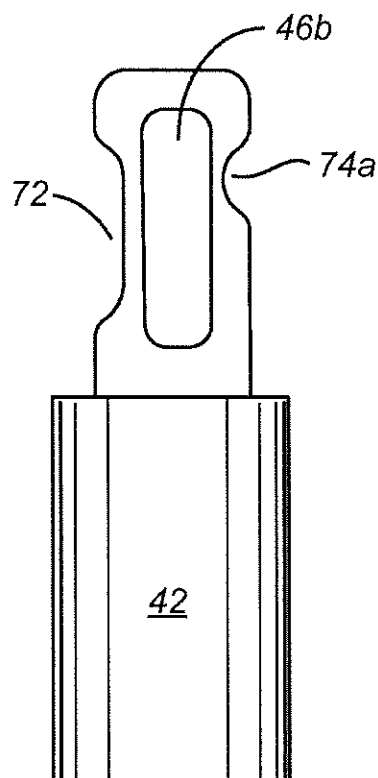
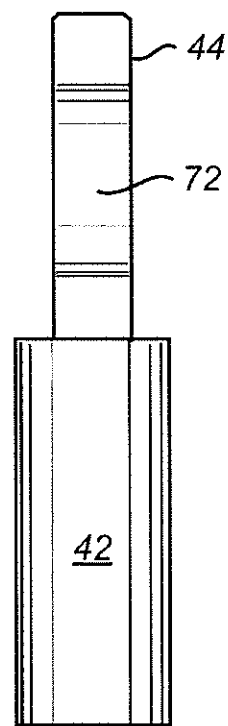
**Fig. 4B**



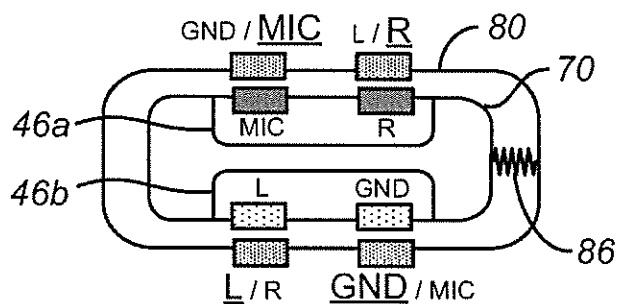
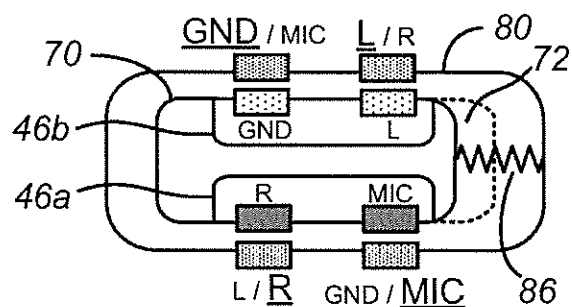
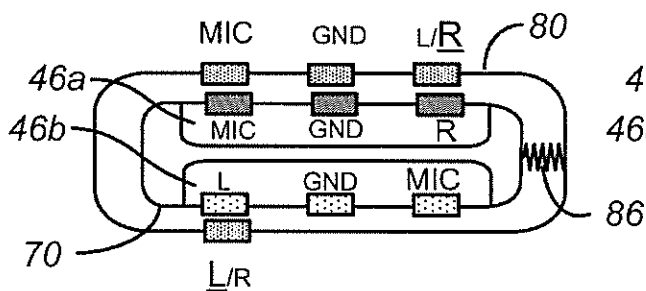
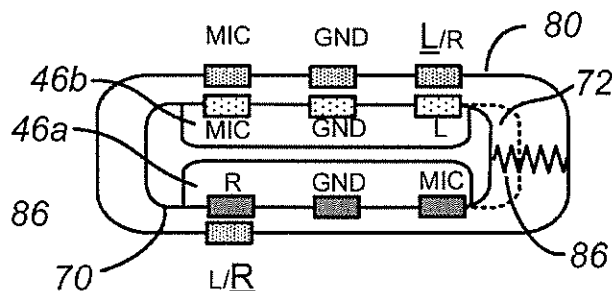
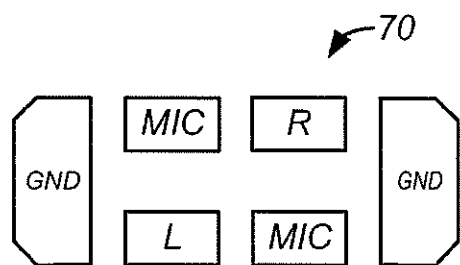
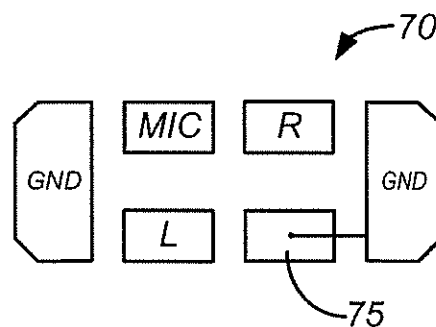
**Fig. 4C**

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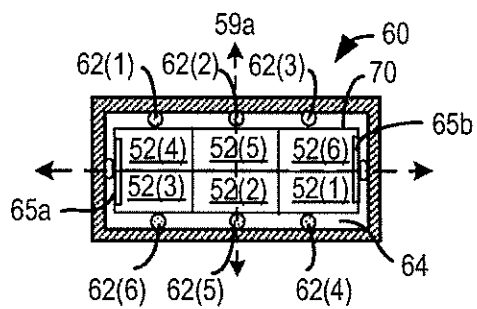
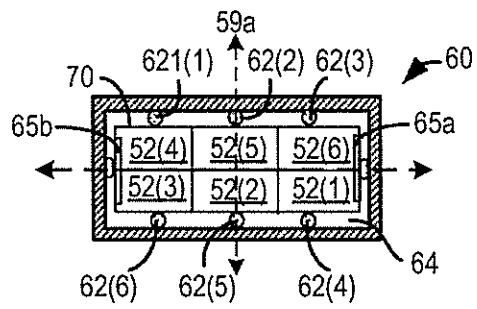
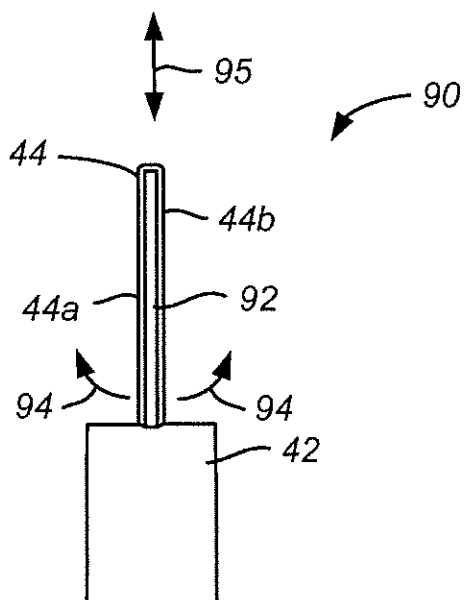
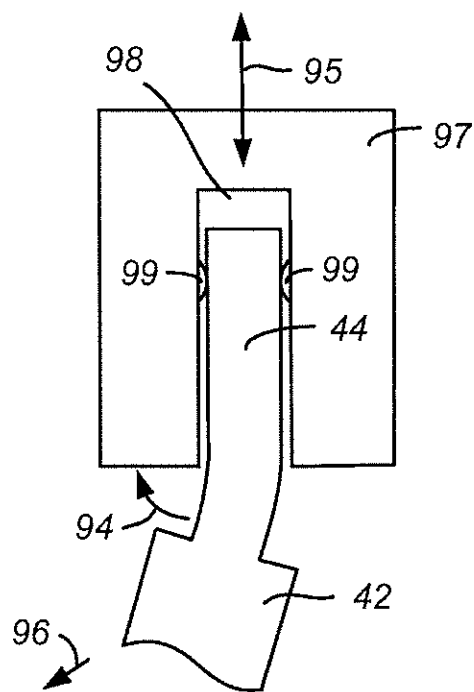
**Fig. 5A****Fig. 5B****Fig. 5C****Fig. 5D****Fig. 5E****Fig. 5F****Fig. 5G****Fig. 5H****Fig. 6A****Fig. 6B**

**Fig. 7A****Fig. 7B****Fig. 8A****Fig. 8B****Fig. 8C**

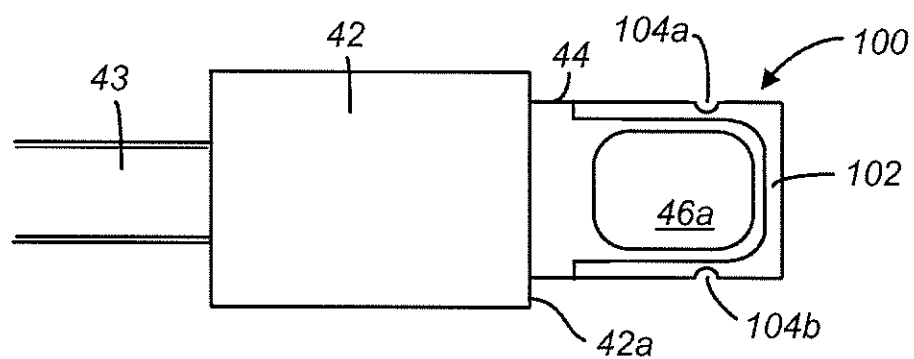
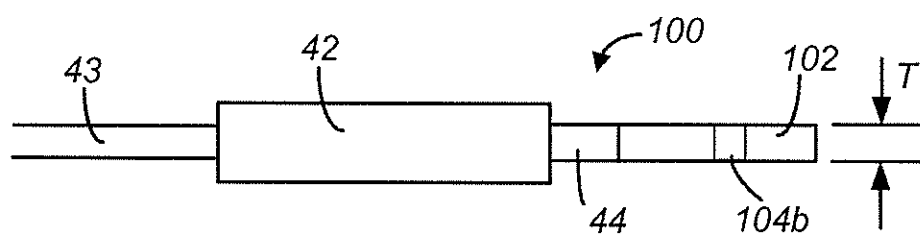
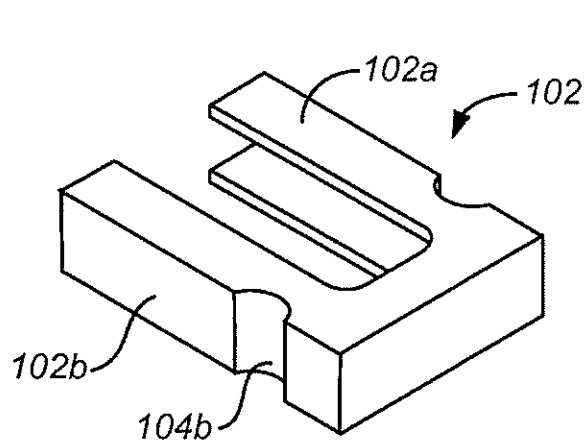
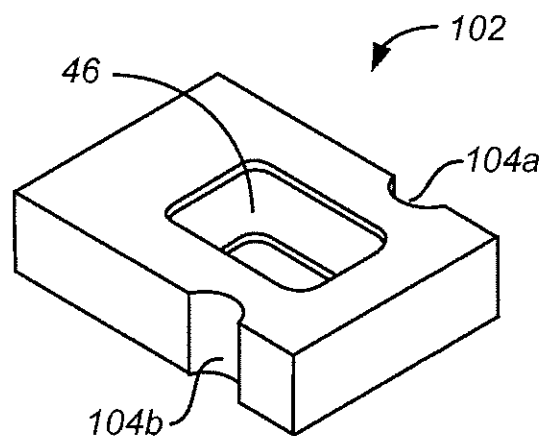
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**Fig. 9A****Fig. 9B****Fig. 9C****Fig. 9D****Fig. 9E****Fig. 9F**

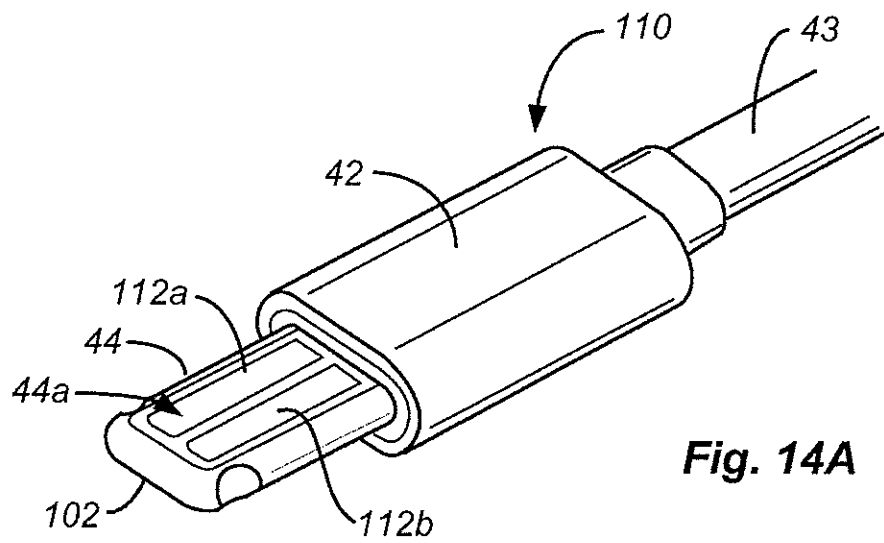
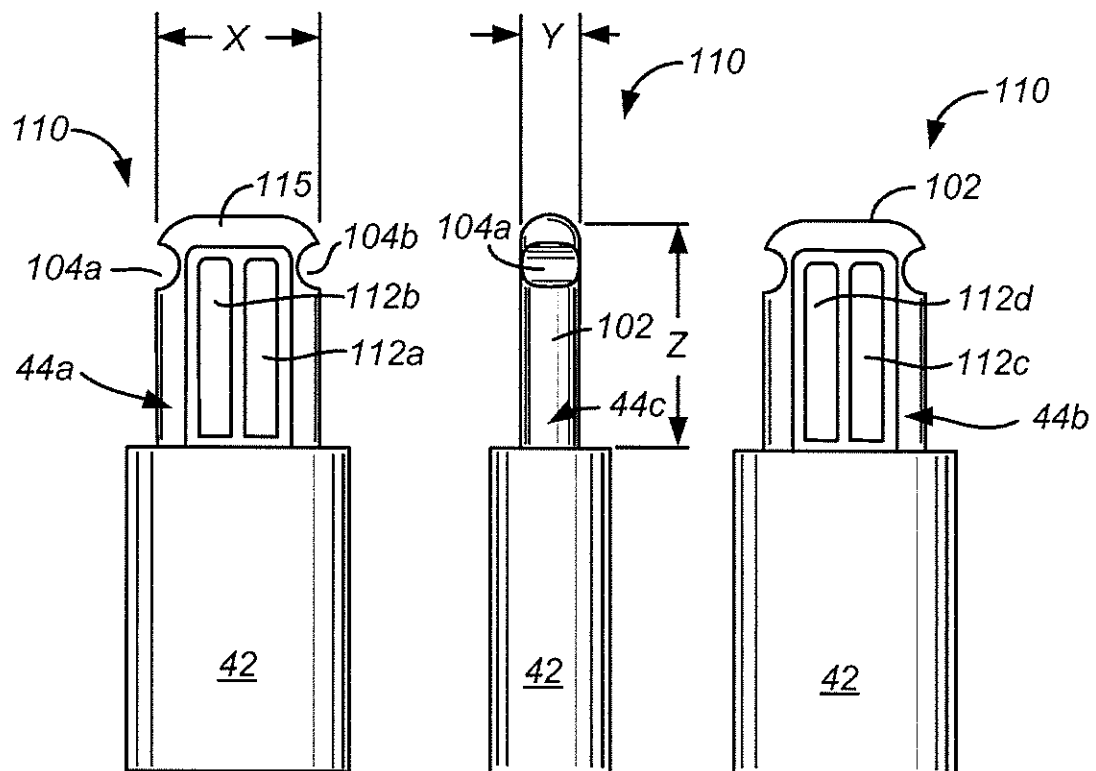
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**Fig. 10A****Fig. 10B****Fig. 11A****Fig. 11B**

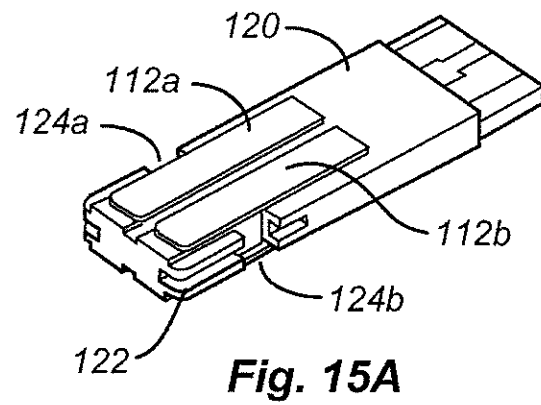
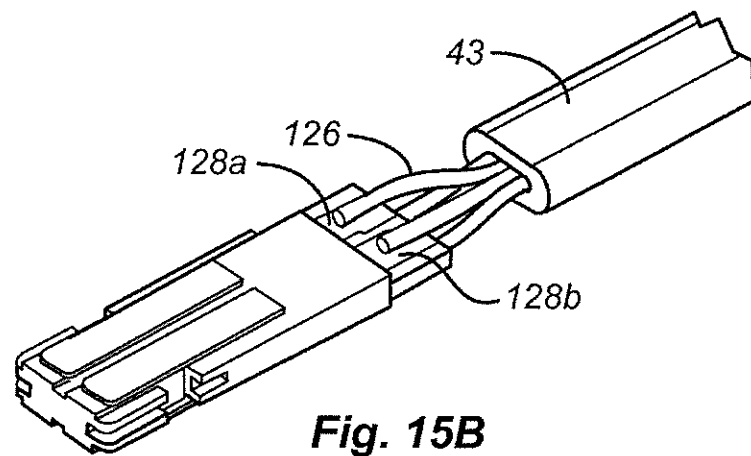
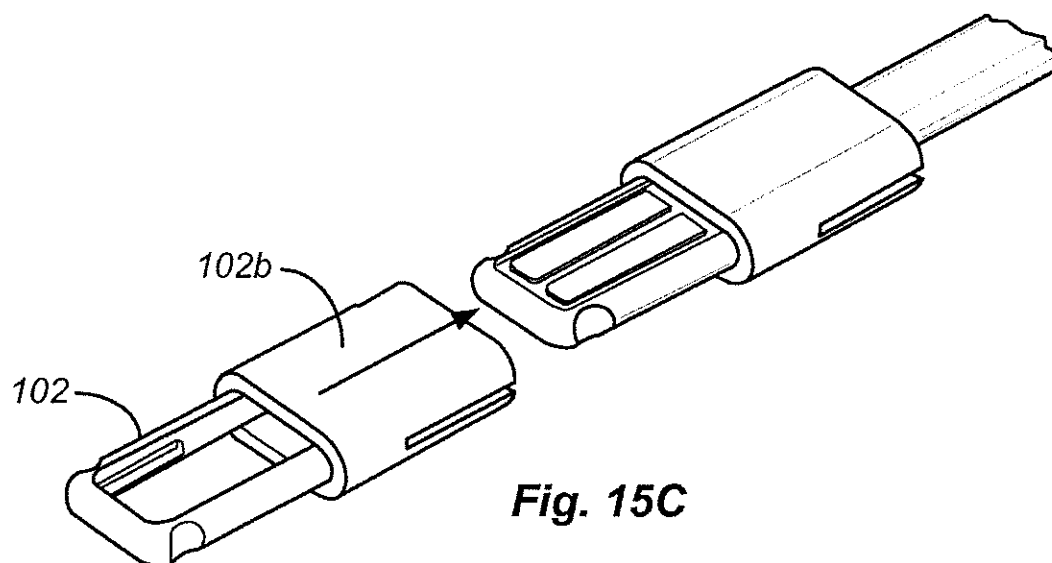
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**Fig. 12A****Fig. 12B****Fig. 13A****Fig. 13B**

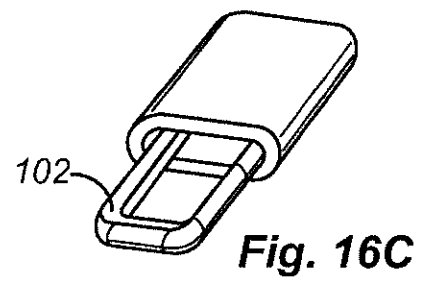
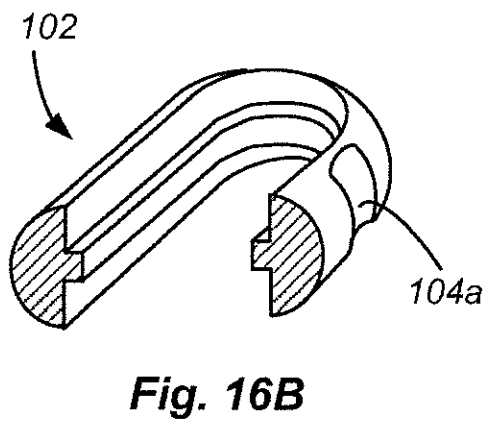
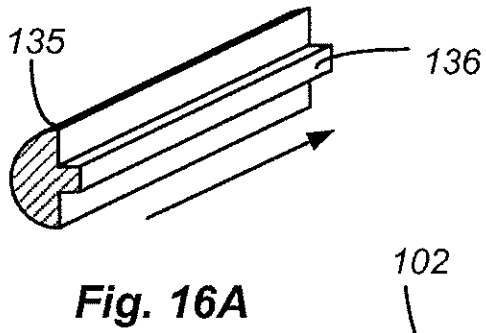
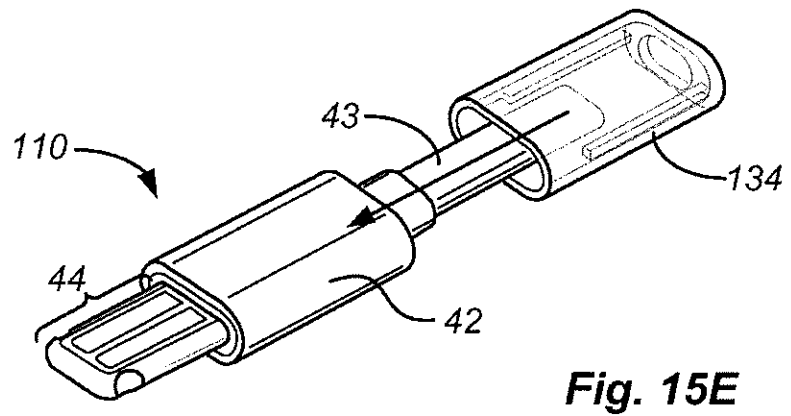
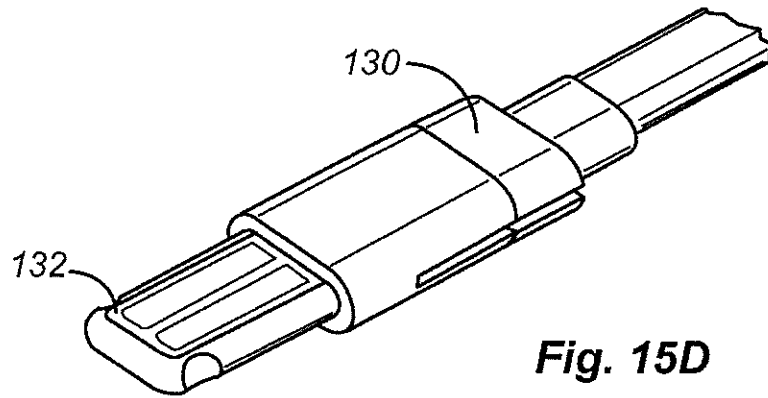
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**Fig. 14A****Fig. 14B****Fig. 14C****Fig. 14D**

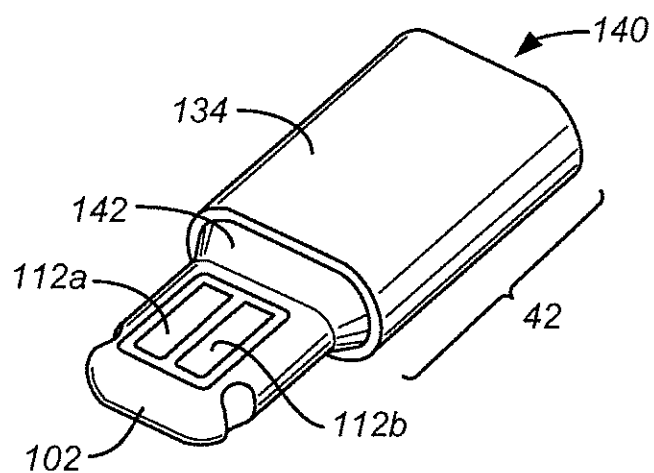
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**Fig. 15A****Fig. 15B****Fig. 15C**

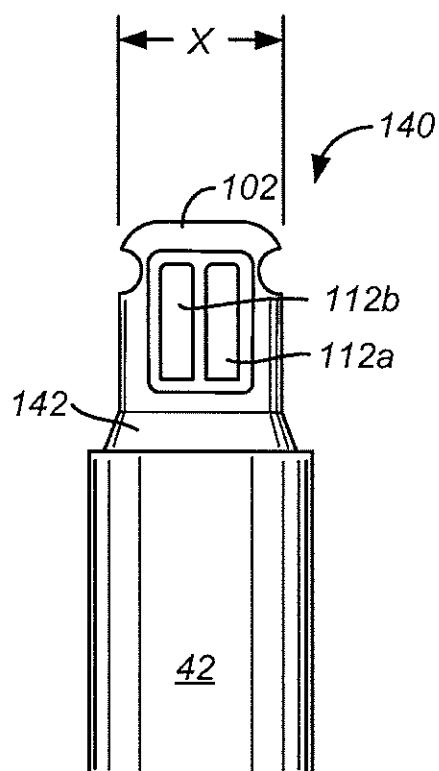
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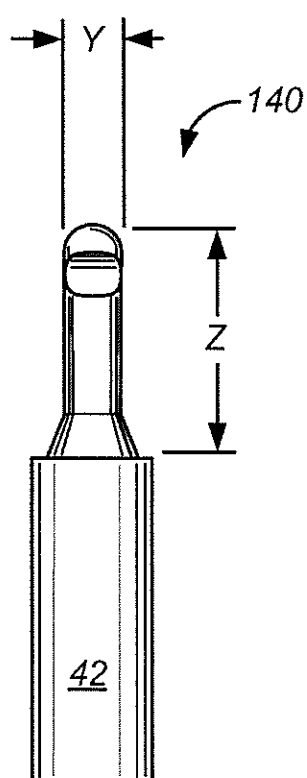
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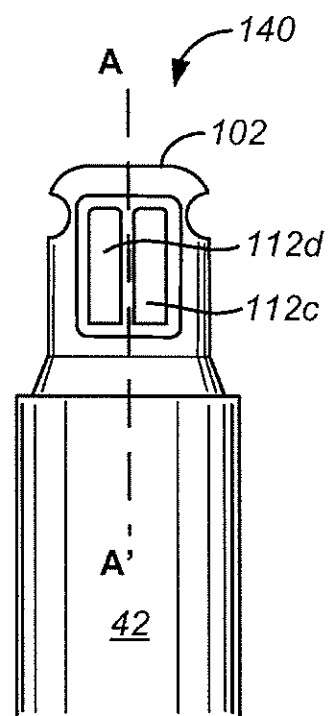
**Fig. 17A**



**Fig. 17B**

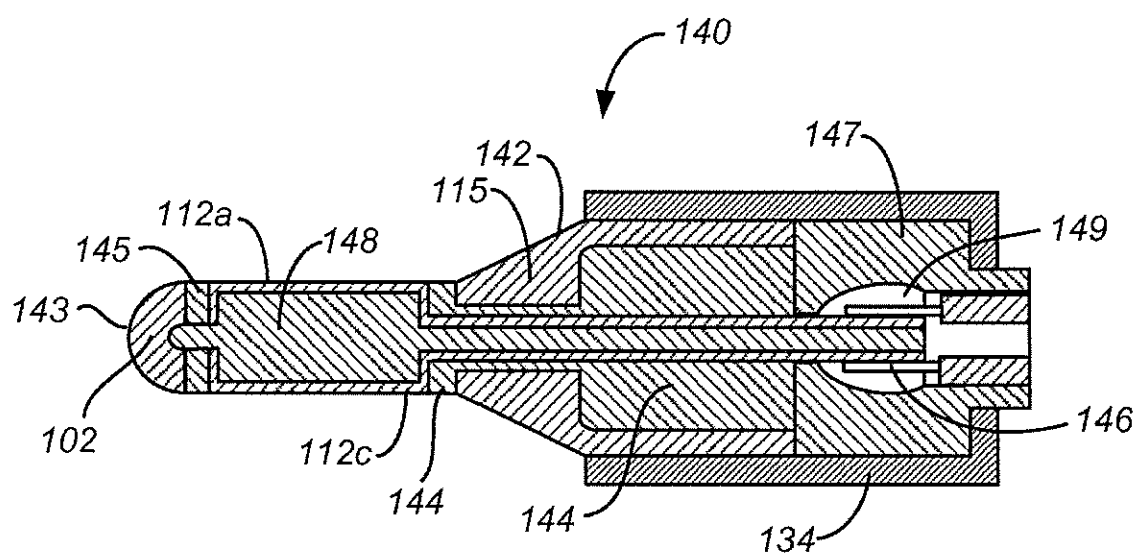


**Fig. 17C**

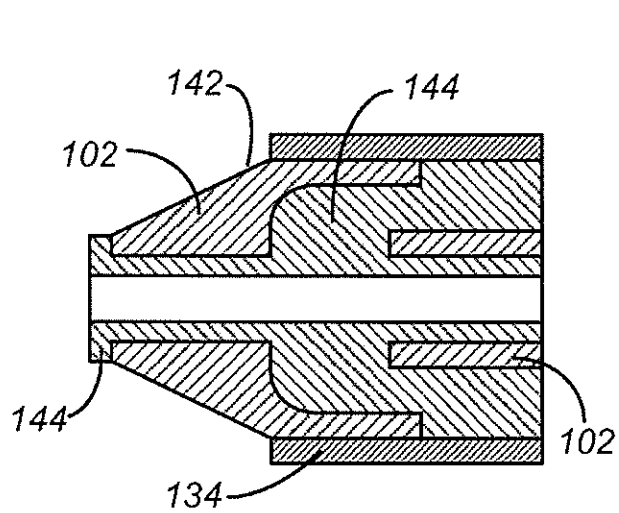


**Fig. 17D**

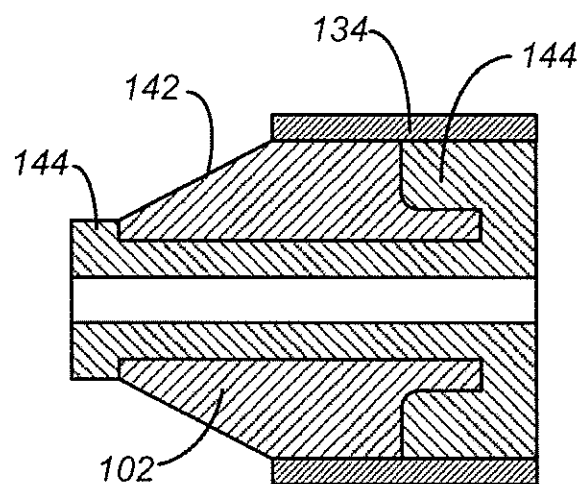
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**Fig. 18**

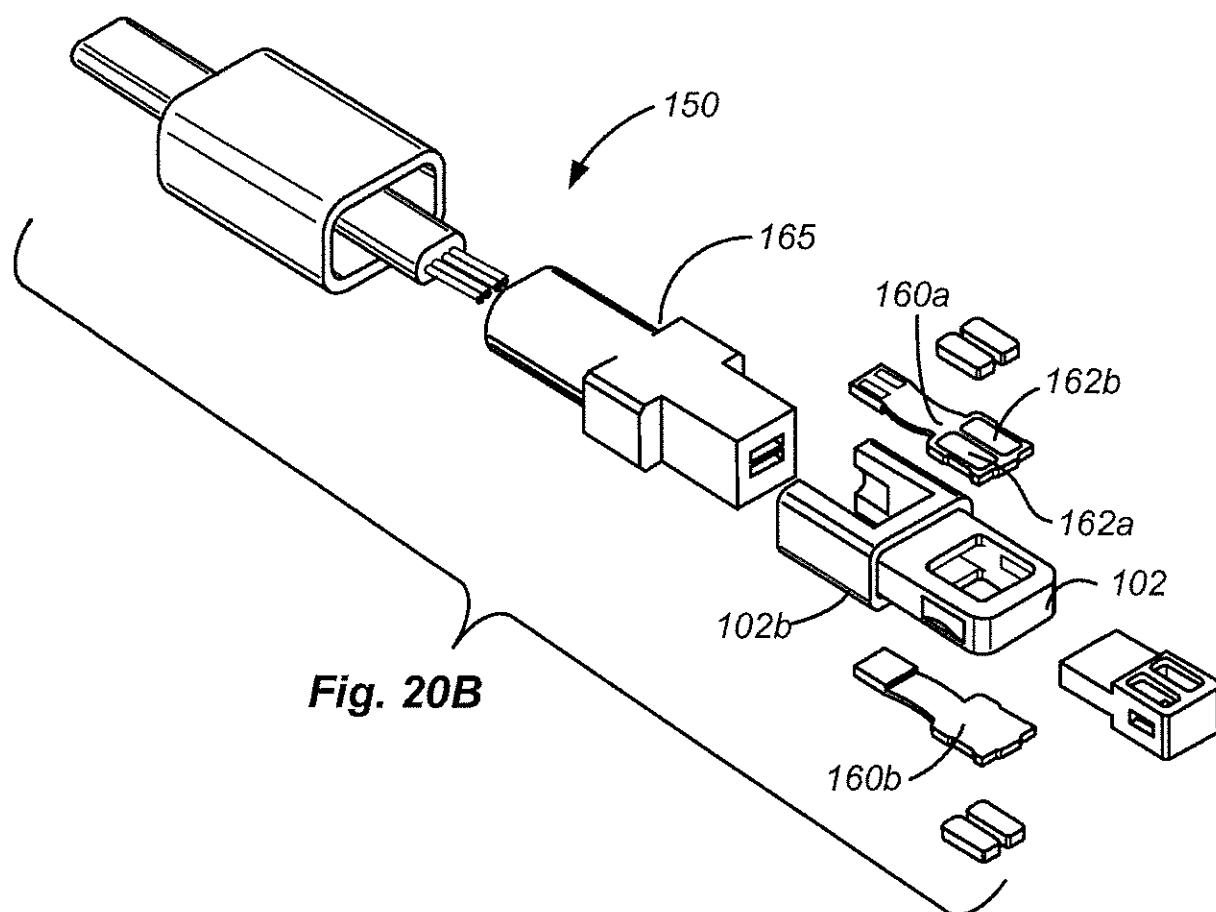
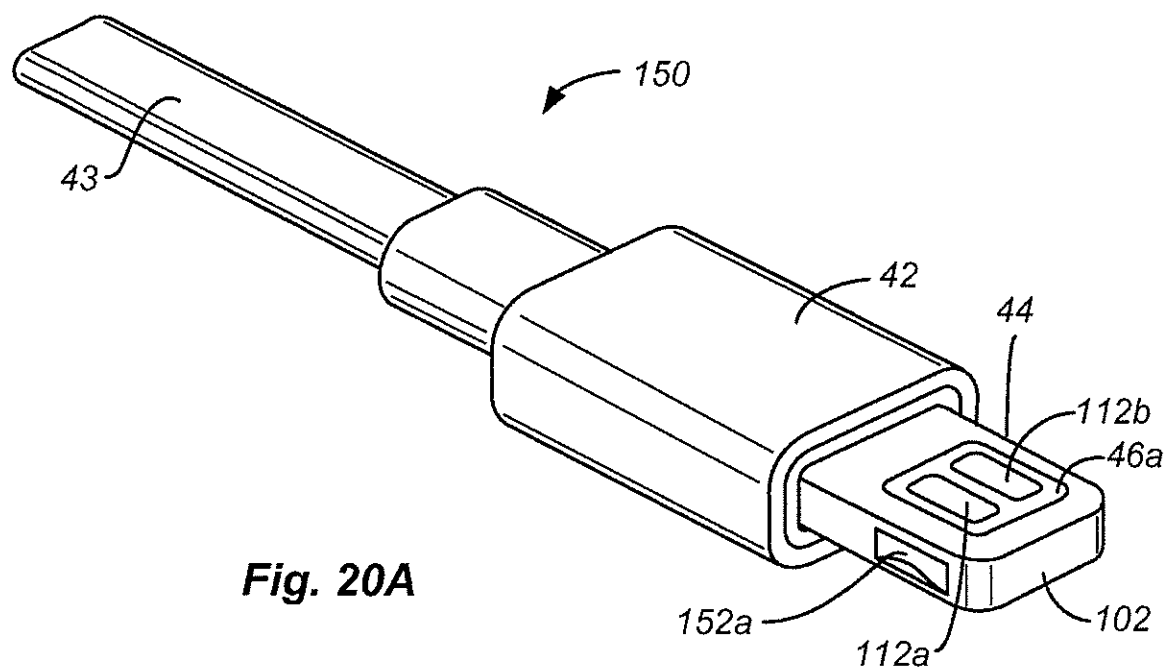


**Fig. 19A**

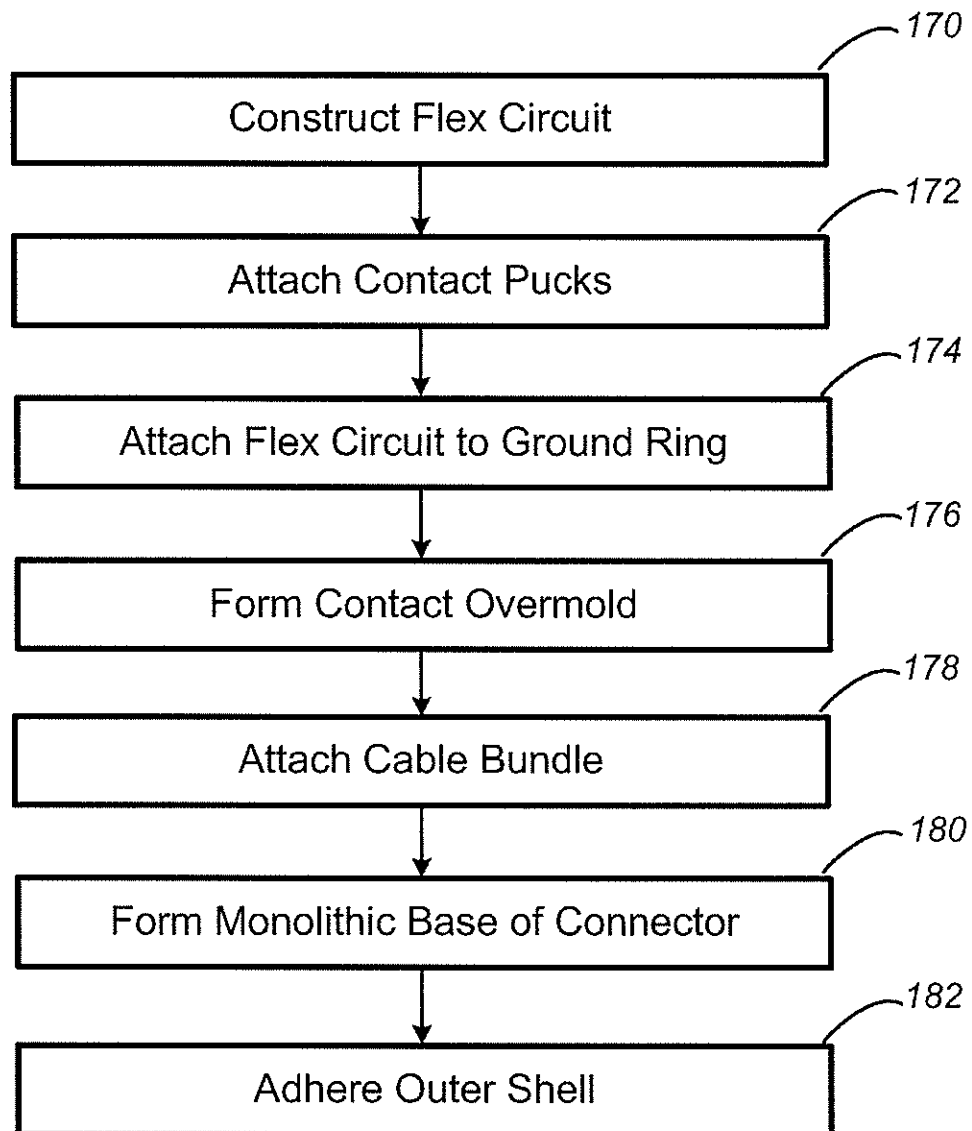


**Fig. 19B**

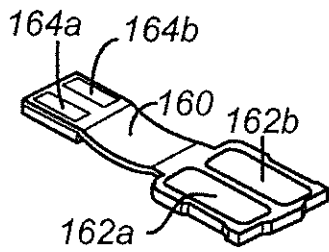
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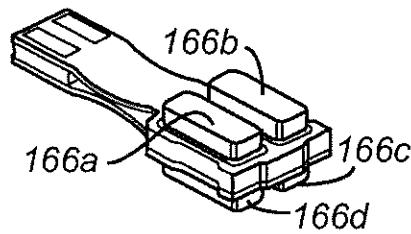
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**Fig. 21**

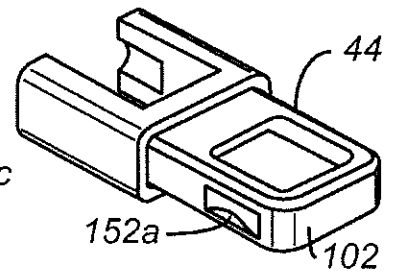
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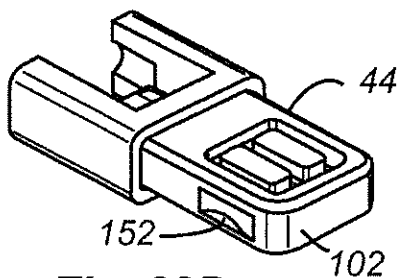
**Fig. 22A**



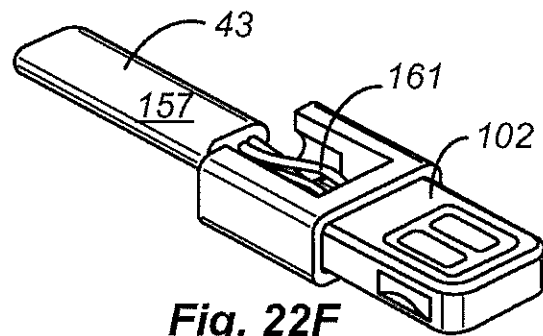
**Fig. 22B**



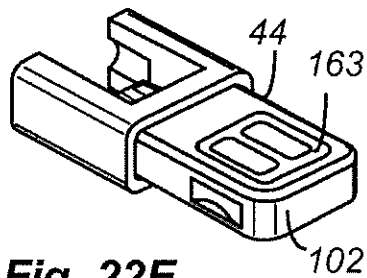
**Fig. 22C**



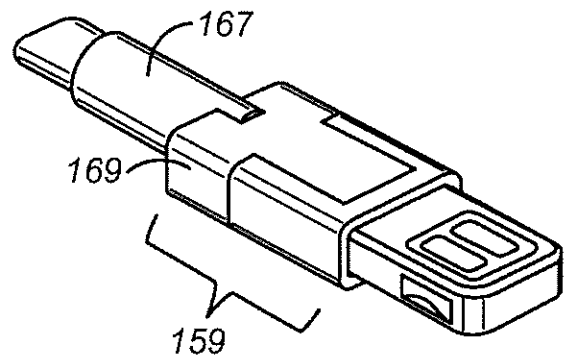
**Fig. 22D**



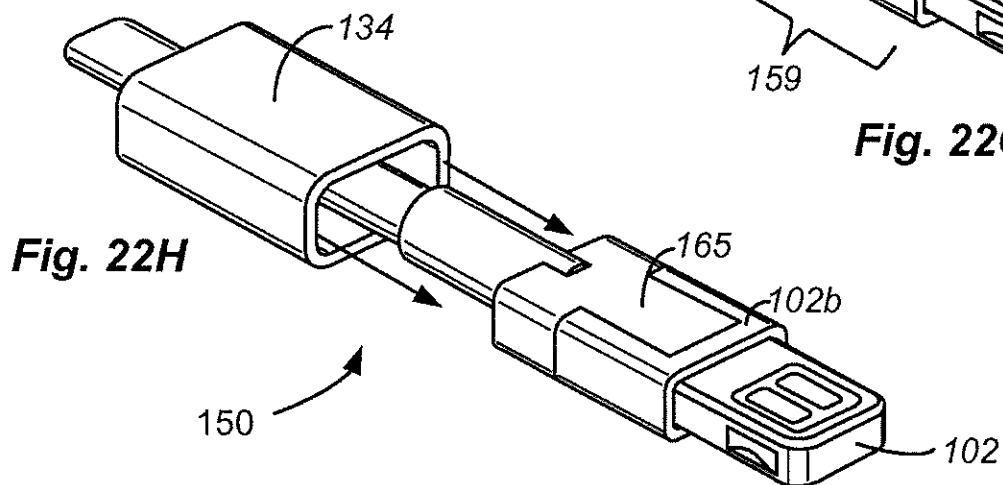
**Fig. 22F**



**Fig. 22E**

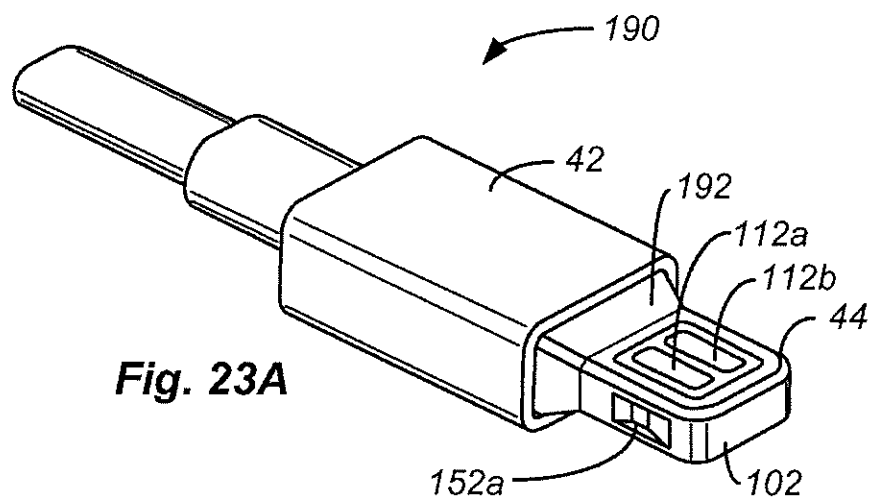


**Fig. 22G**

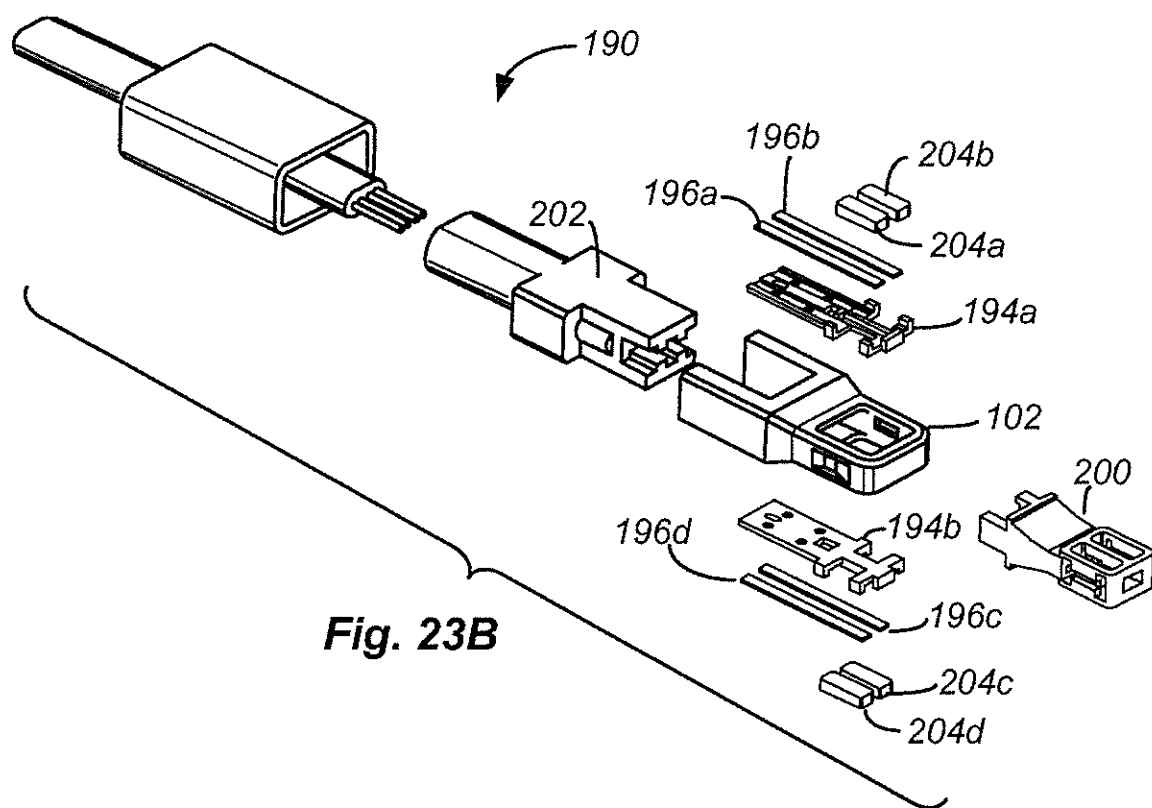


**Fig. 22H**

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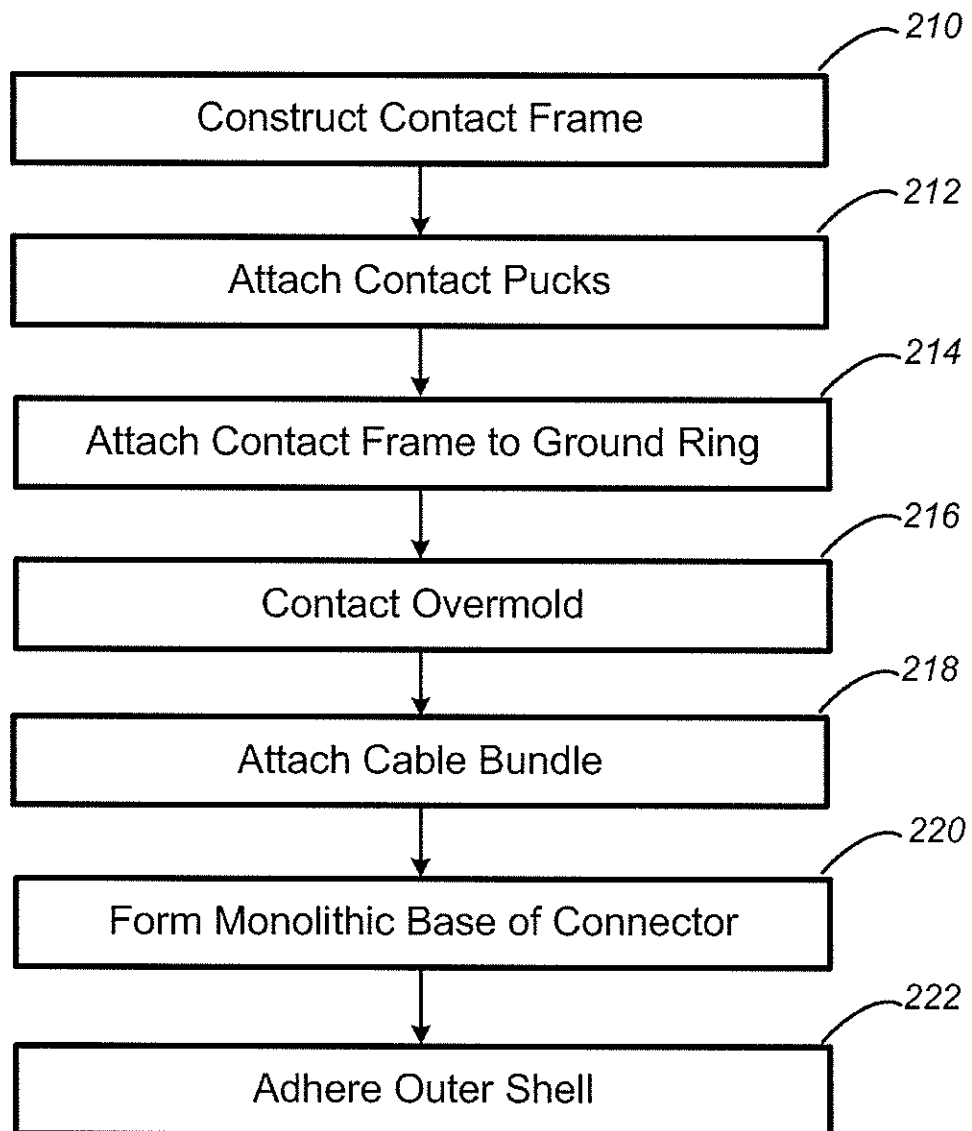


**Fig. 23A**

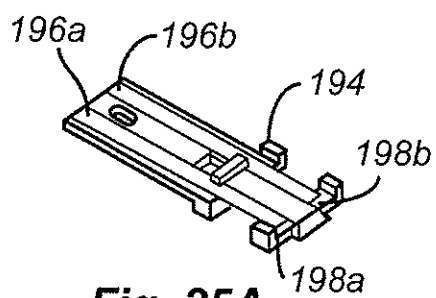


**Fig. 23B**

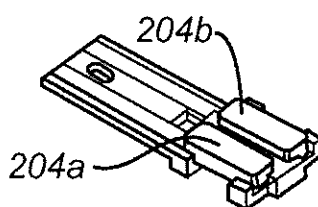
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**Fig. 24**

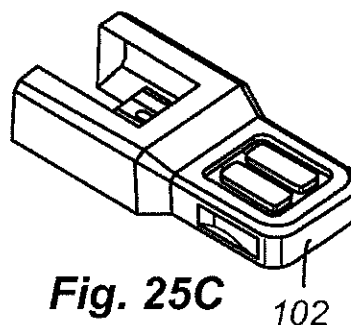
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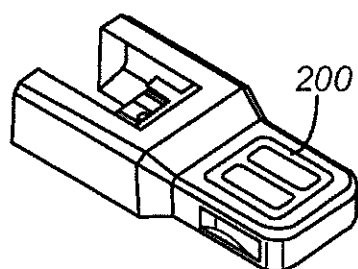
**Fig. 25A**



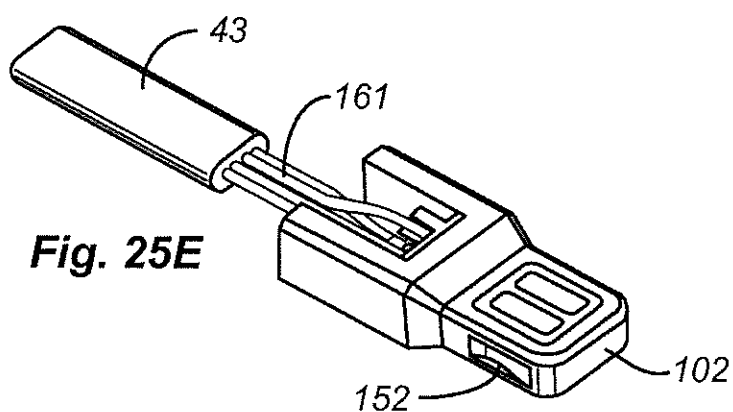
**Fig. 25B**



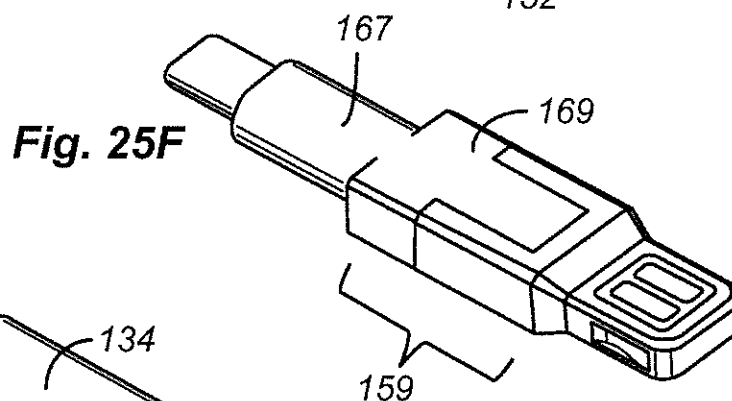
**Fig. 25C**



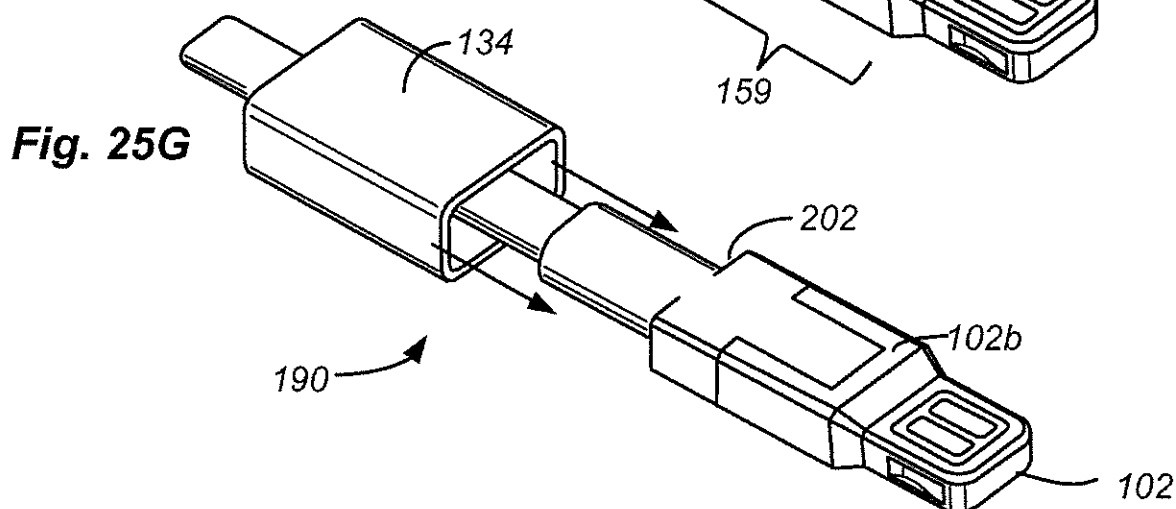
**Fig. 25D**



**Fig. 25E**

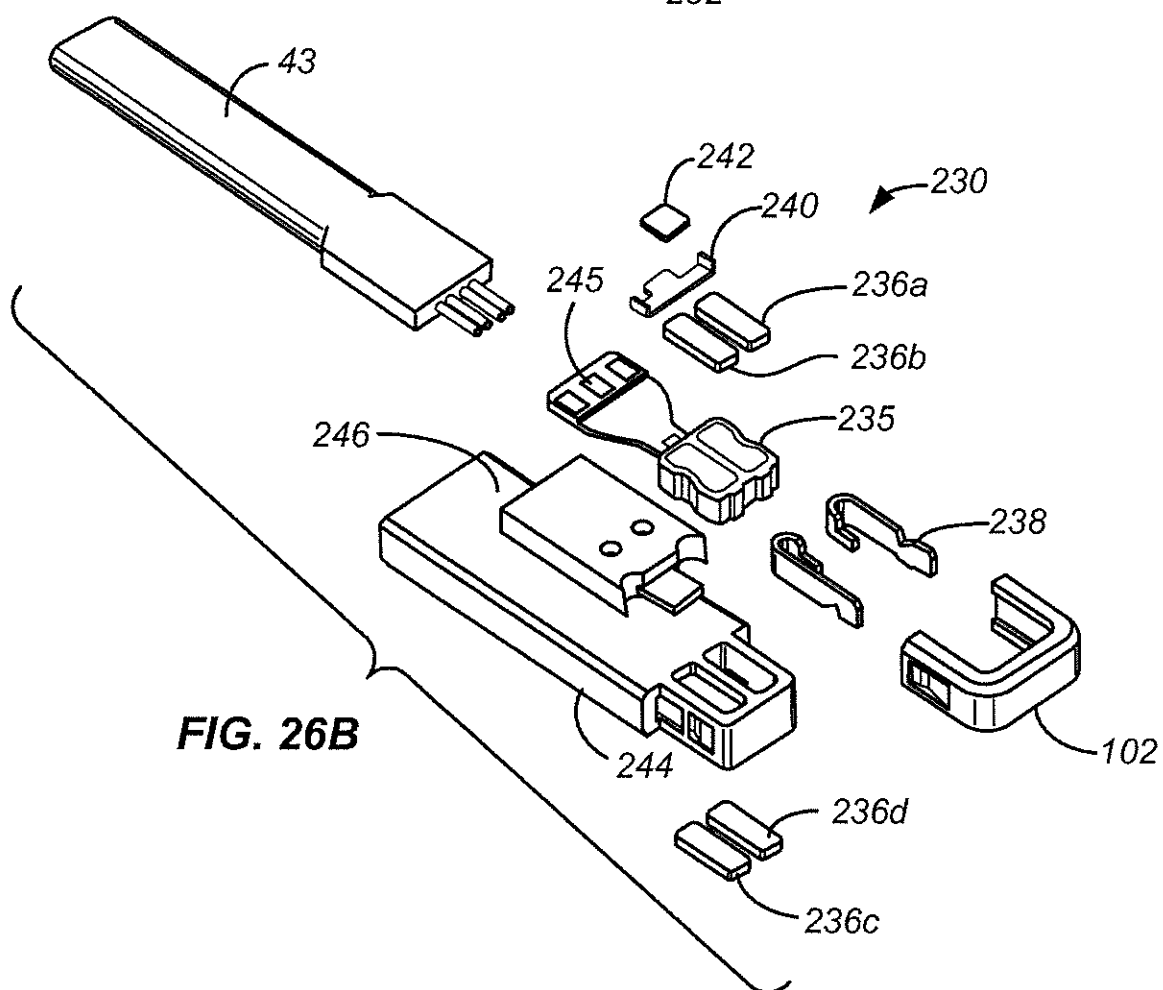
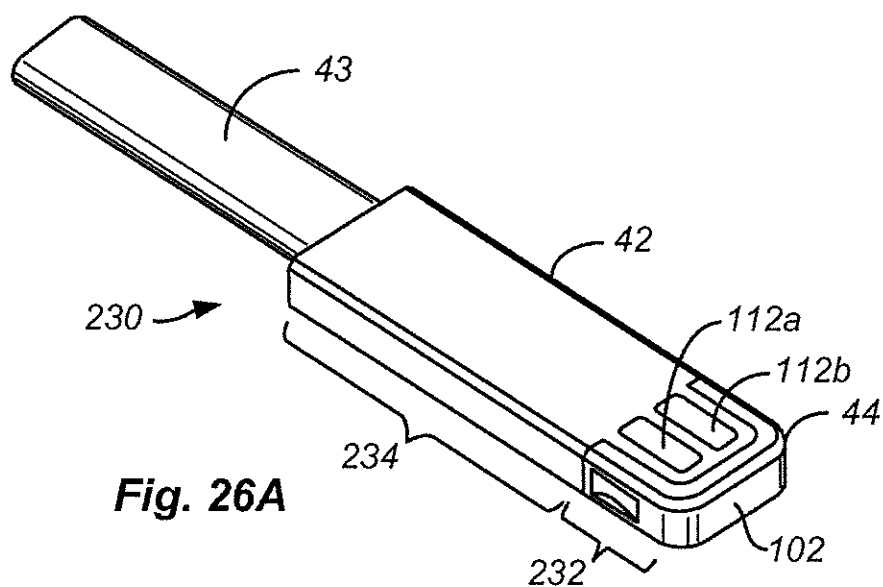


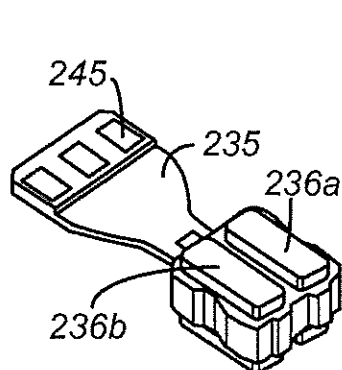
**Fig. 25F**



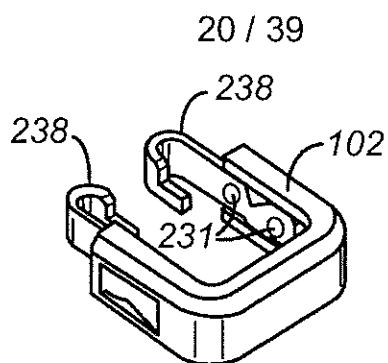
**Fig. 25G**

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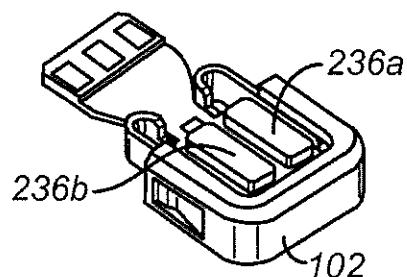




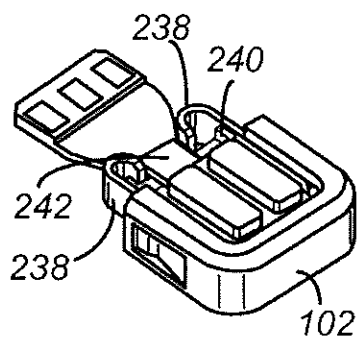
**Fig. 27A**



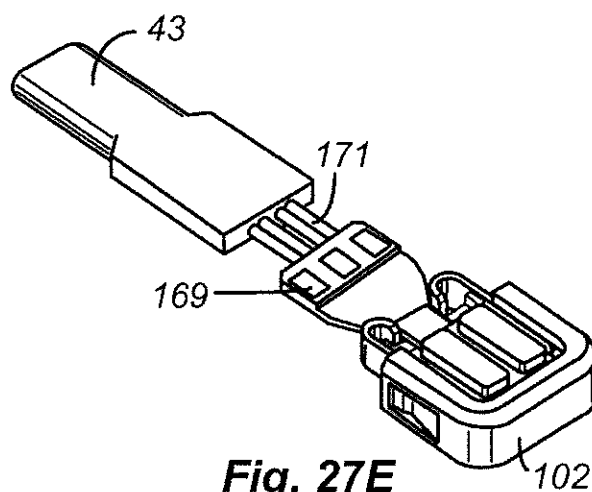
**Fig. 27B**



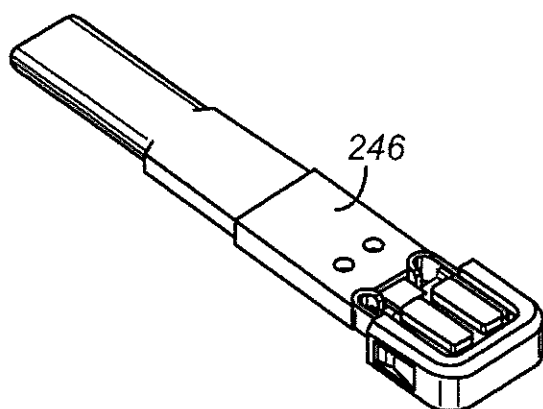
**Fig. 27C**



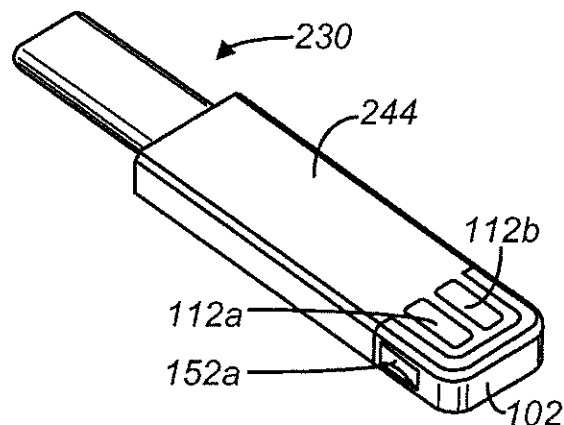
**Fig. 27D**



**Fig. 27E**



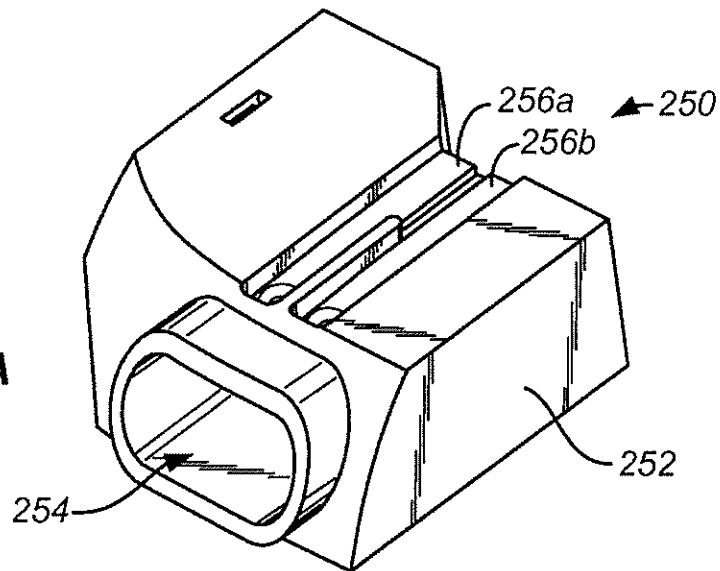
**Fig. 27F**



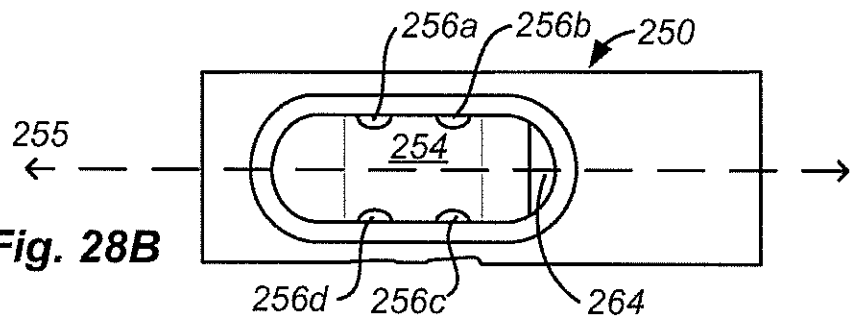
**Fig. 27G**

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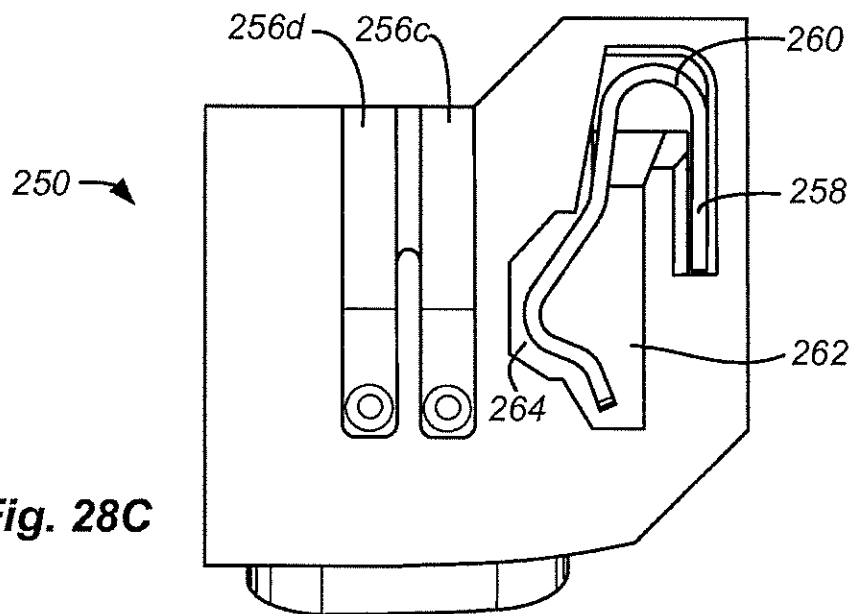
**Fig. 28A**



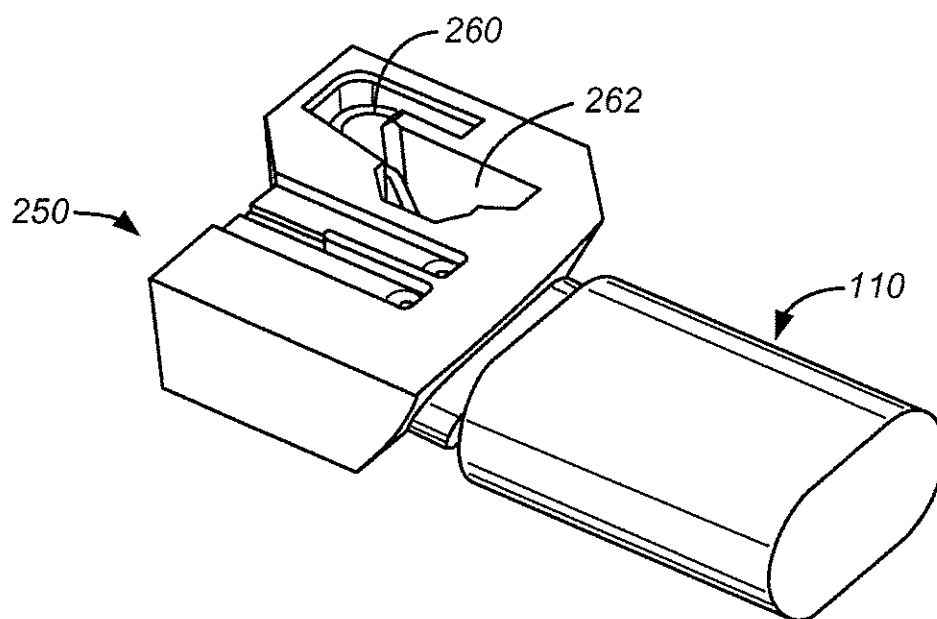
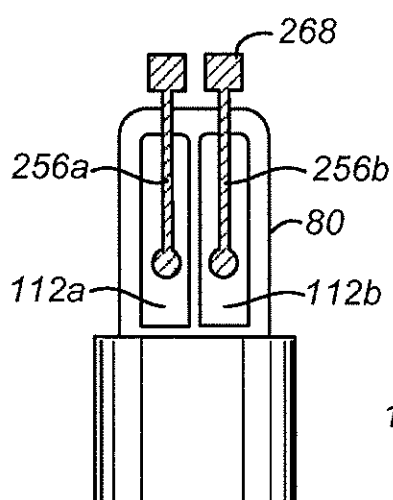
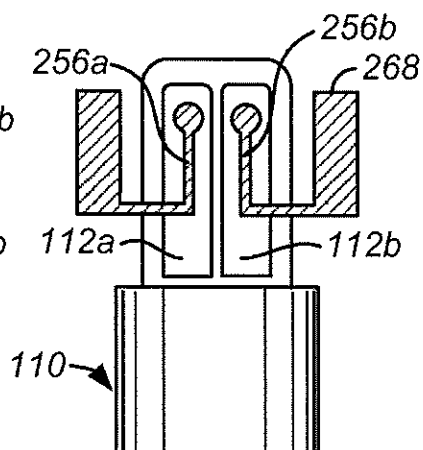
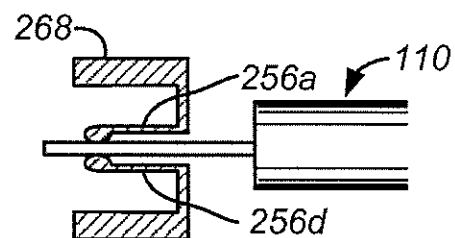
**Fig. 28B**

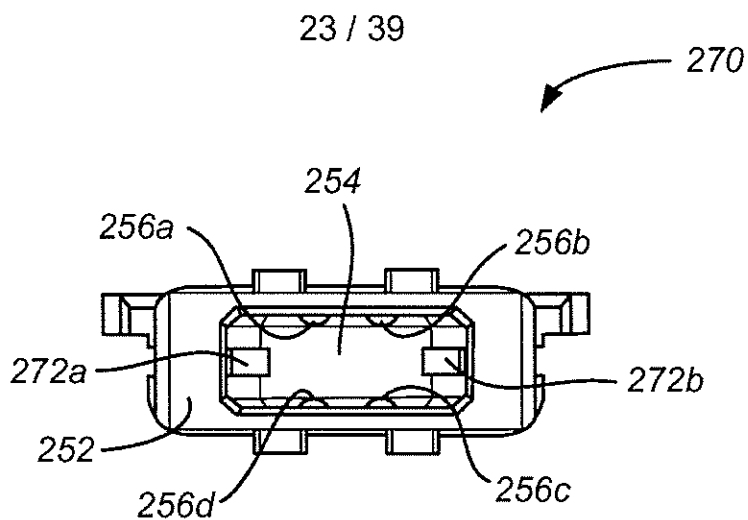


**Fig. 28C**

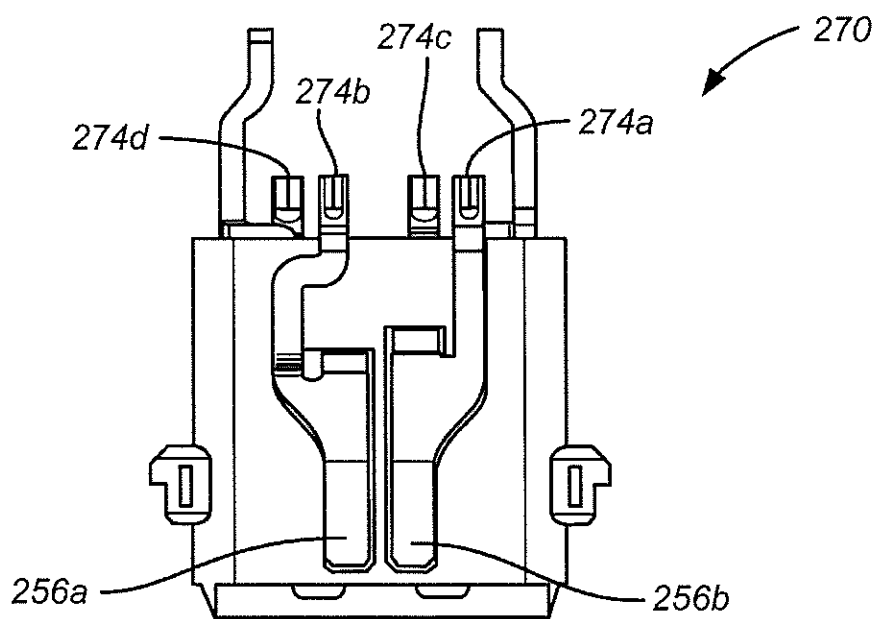


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**Fig. 29****FIG. 30A****FIG. 30B****FIG. 30C**

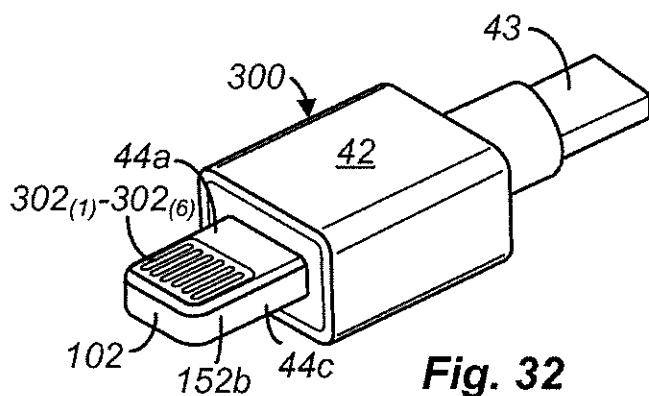
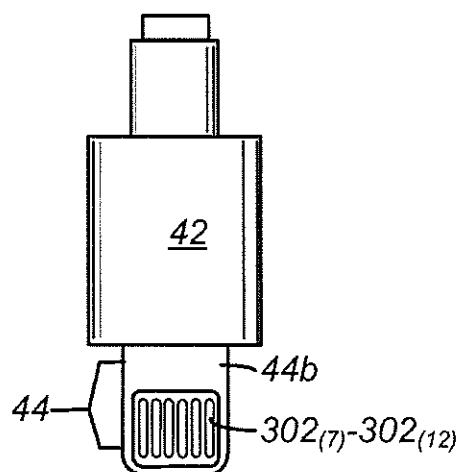
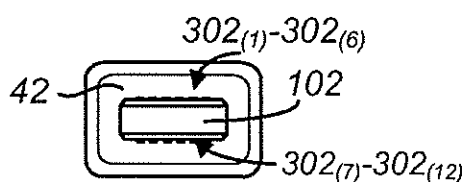
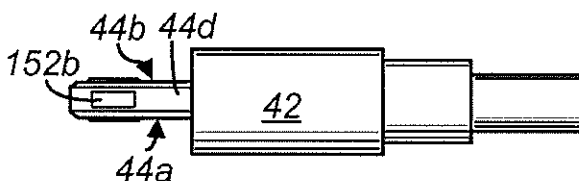
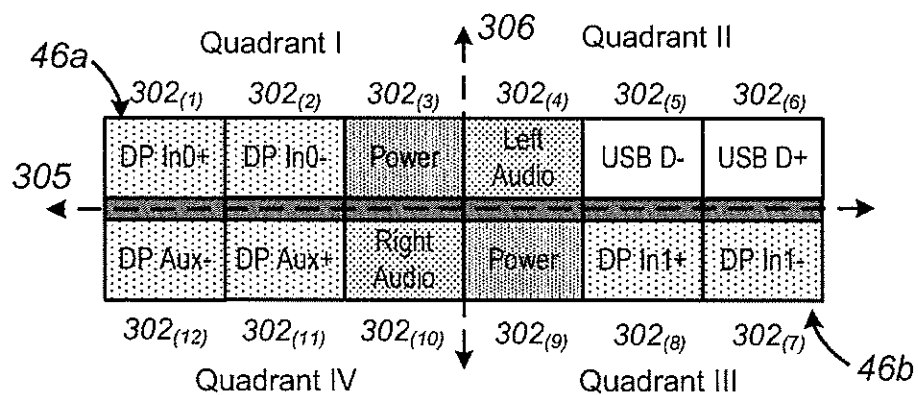
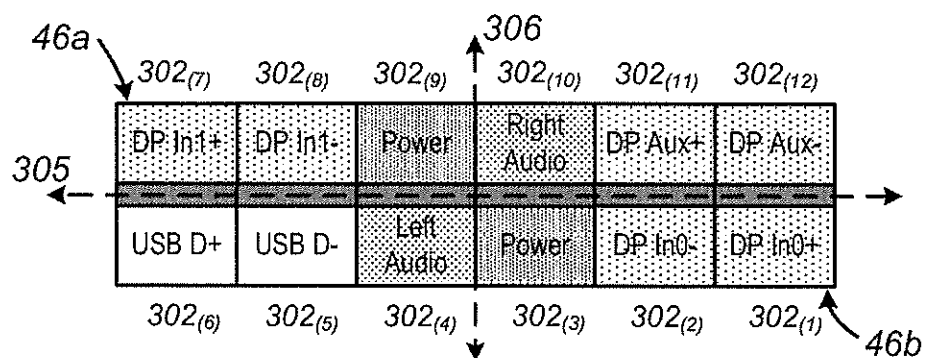


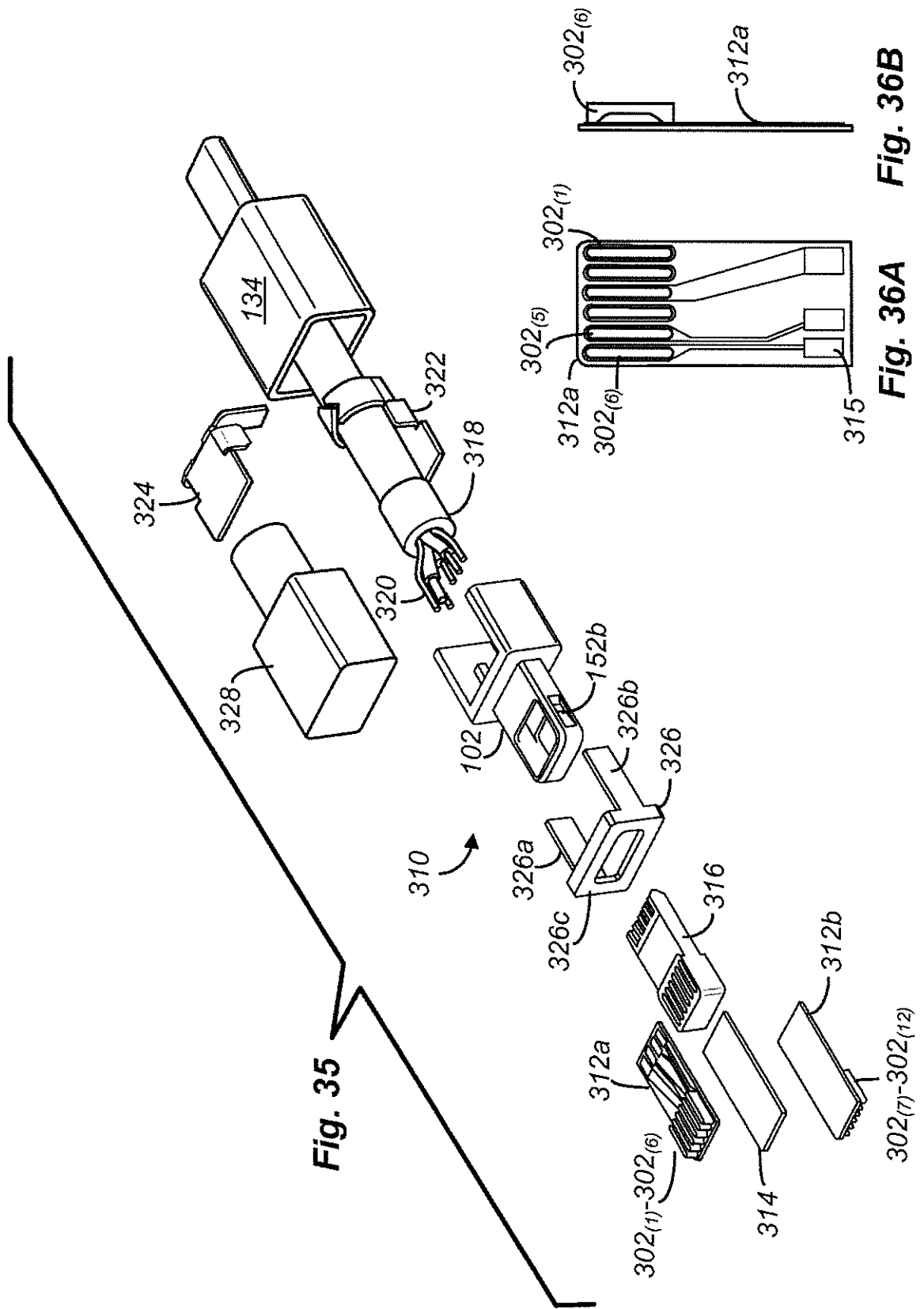
**Fig. 31A**



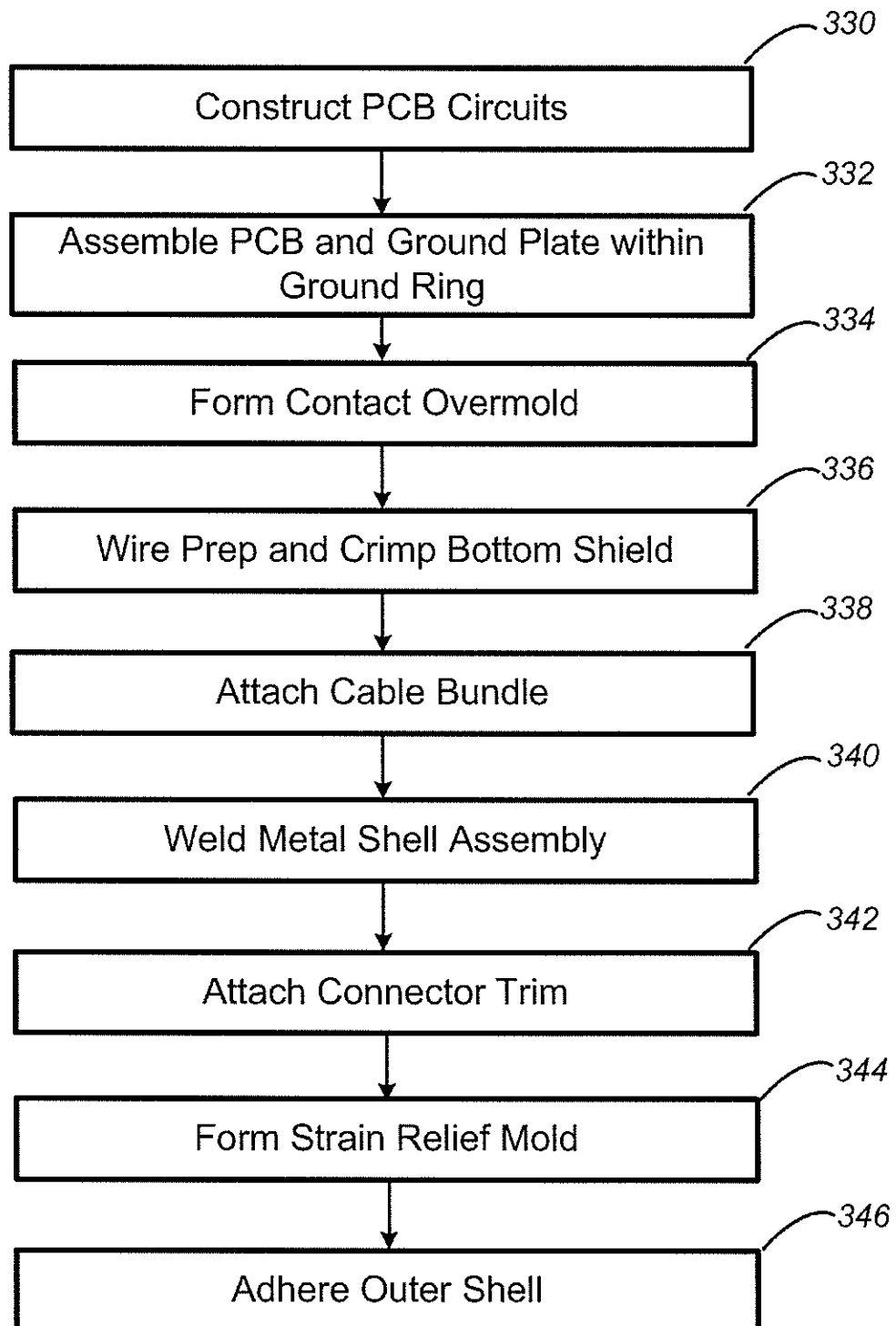
**Fig. 31B**

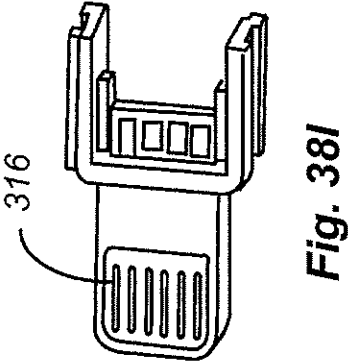
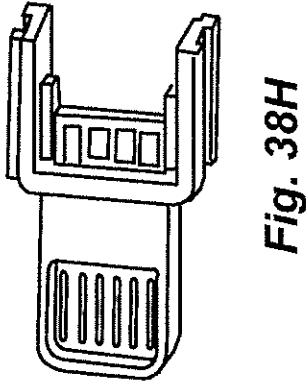
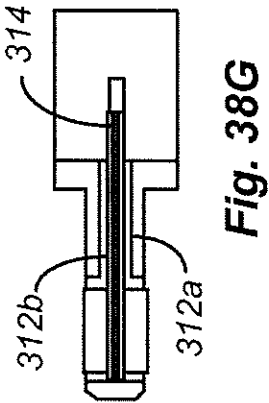
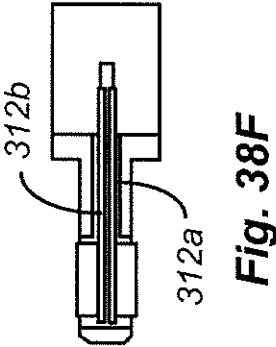
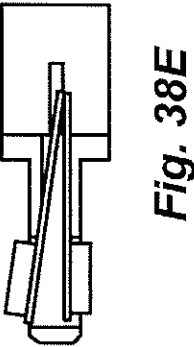
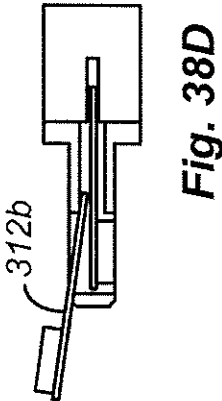
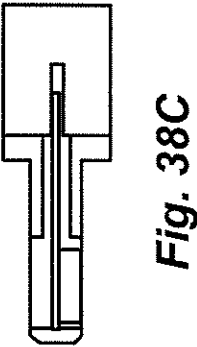
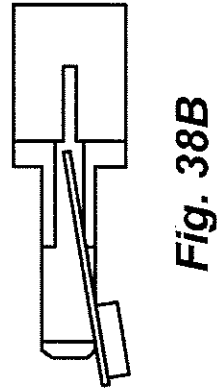
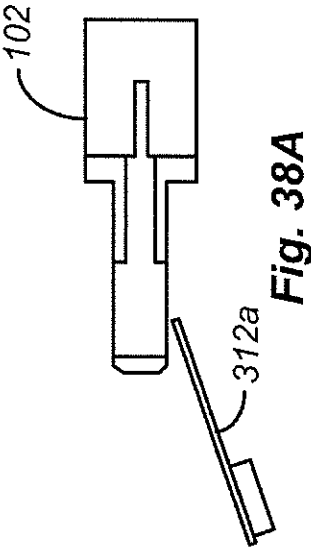
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**Fig. 32****Fig. 33A****Fig. 33B****Fig. 33C****Fig. 34A****Fig. 34B**

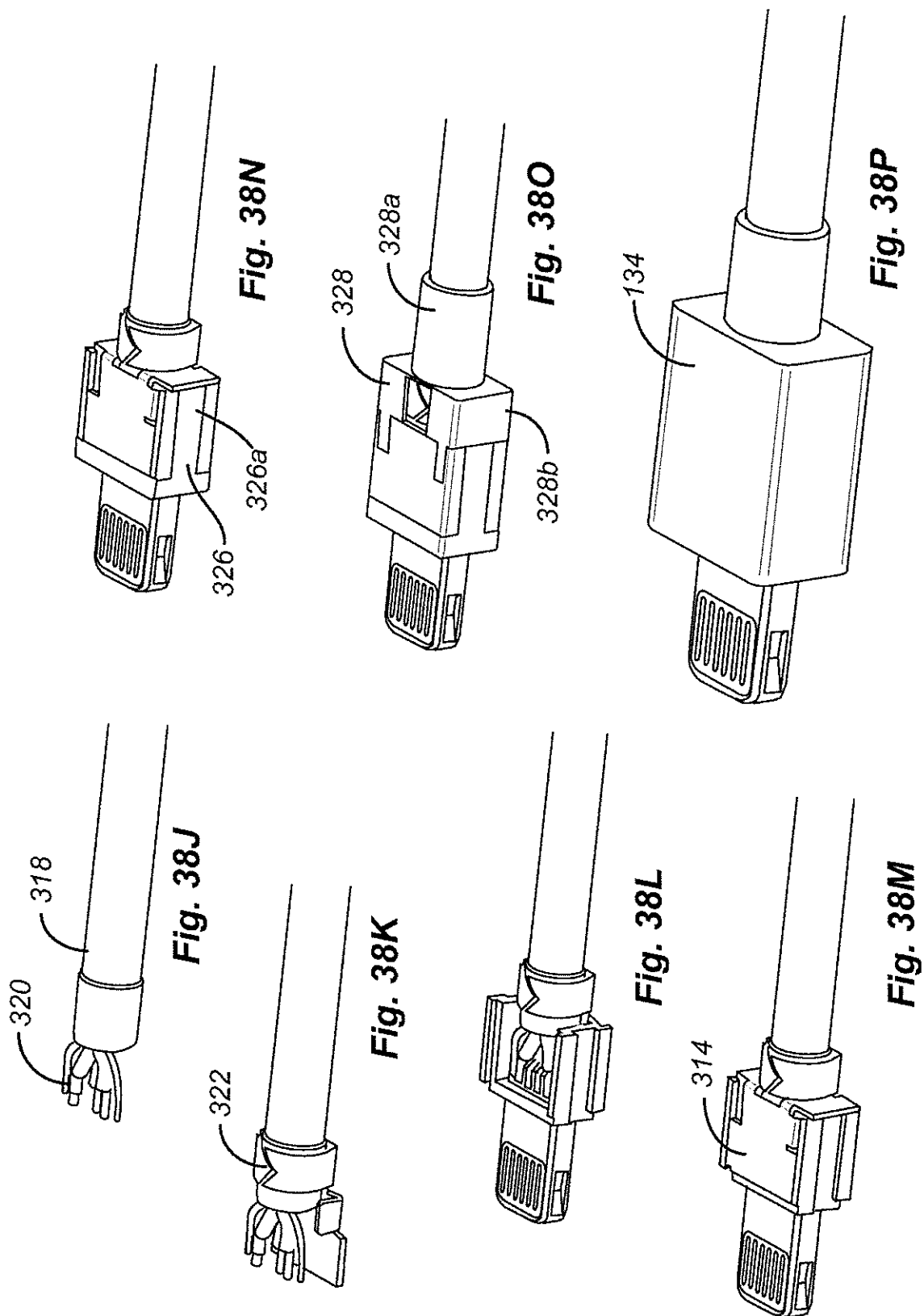


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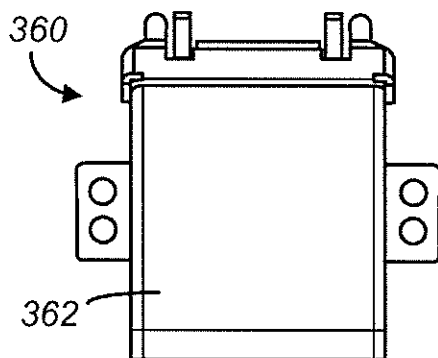
**Fig. 37**



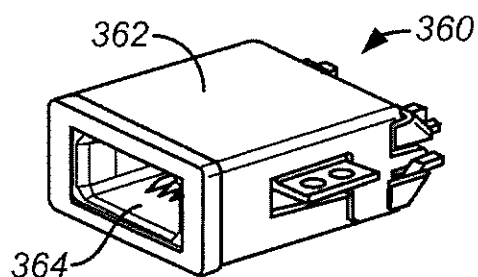
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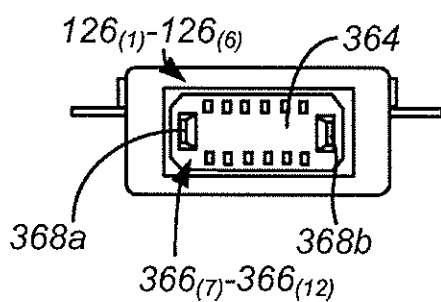
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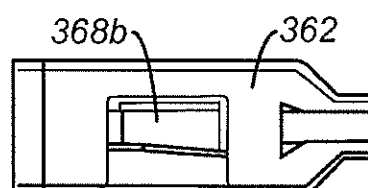
**Fig. 39A**



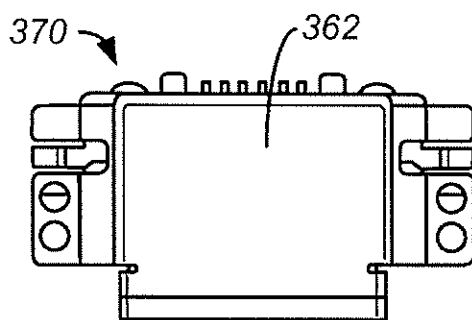
**Fig. 39B**



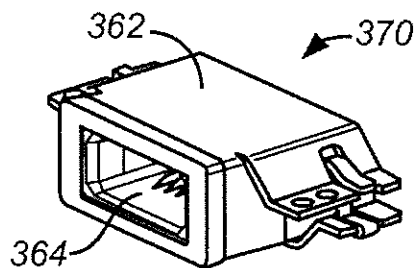
**Fig. 39C**



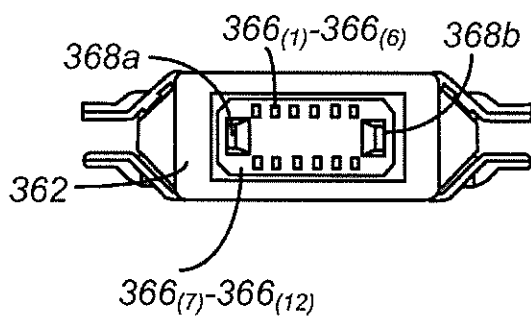
**Fig. 39D**



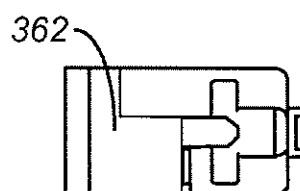
**Fig. 40A**



**Fig. 40B**

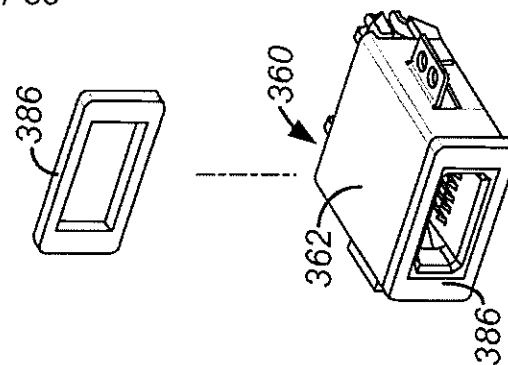
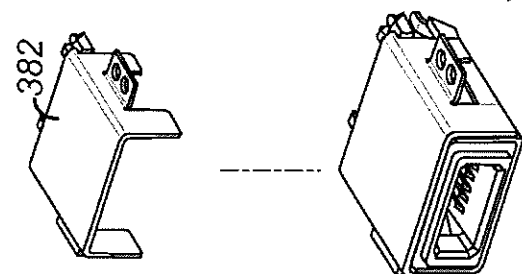
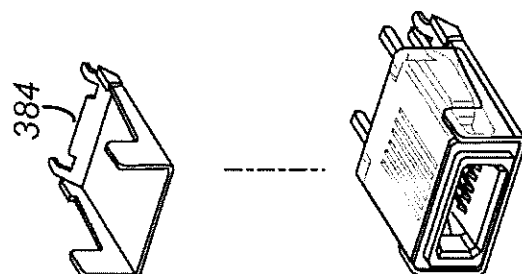
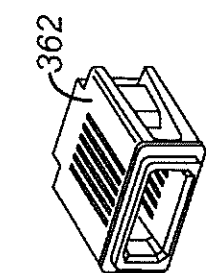
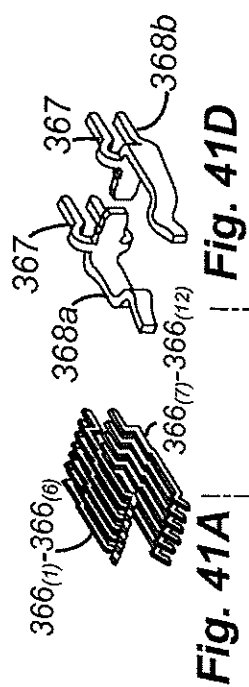


**Fig. 40C**

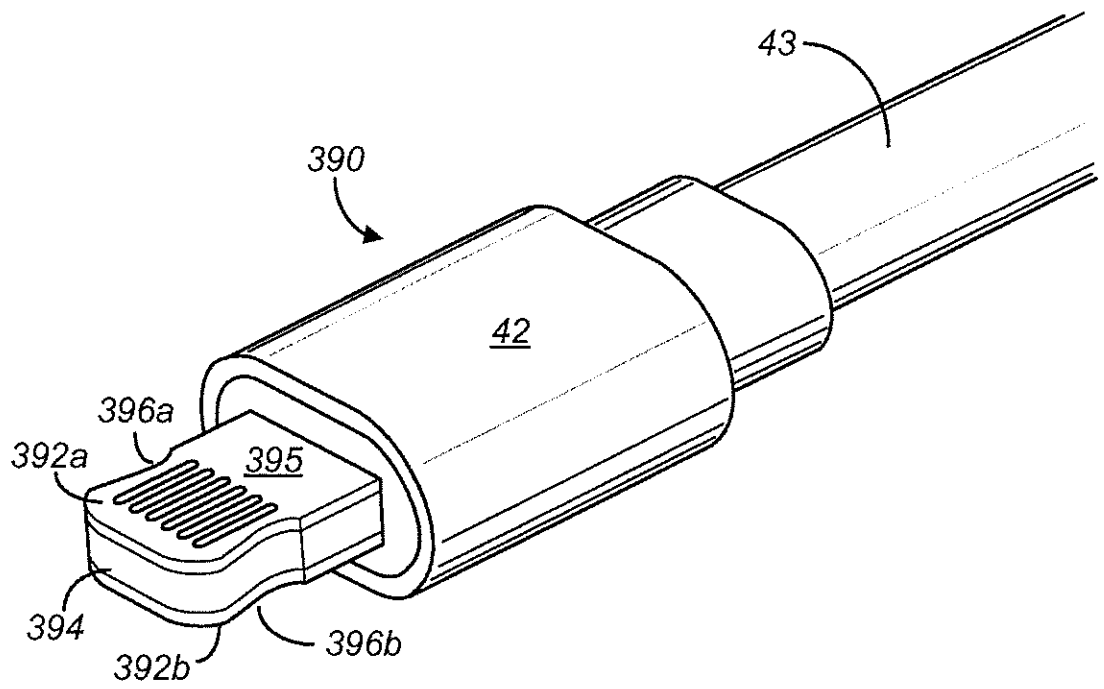


**Fig. 40D**

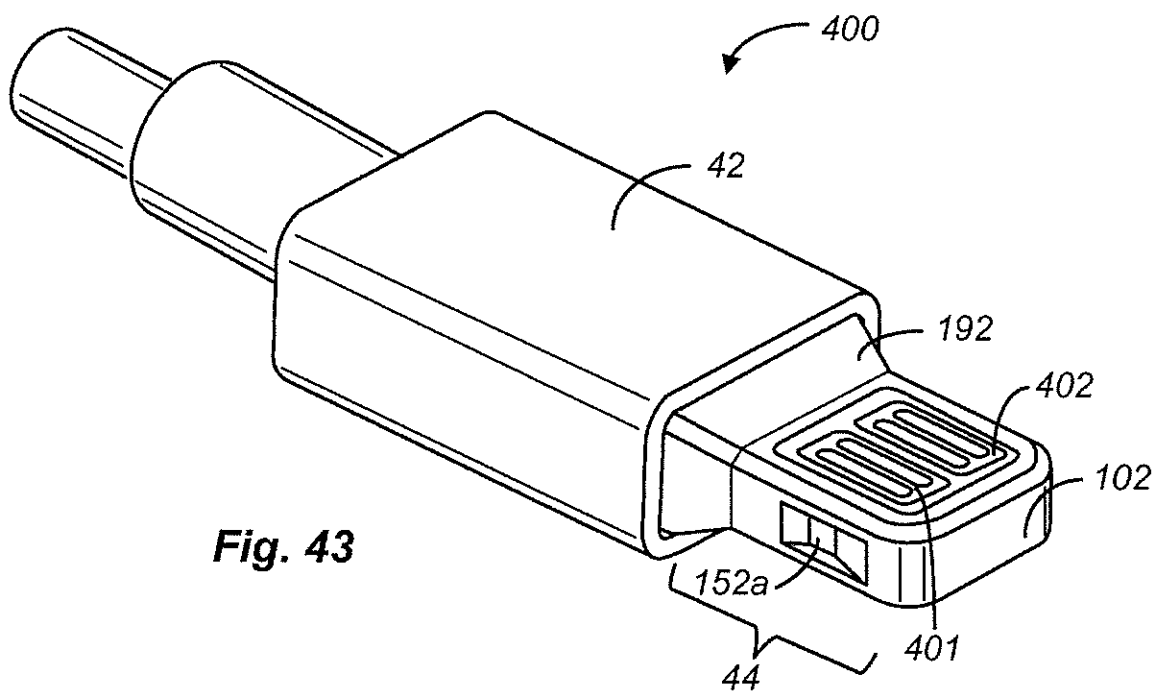
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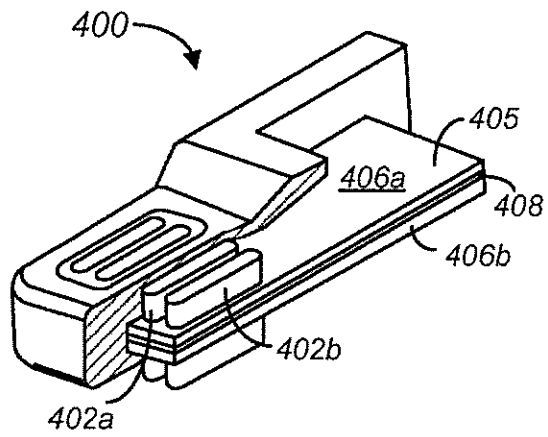
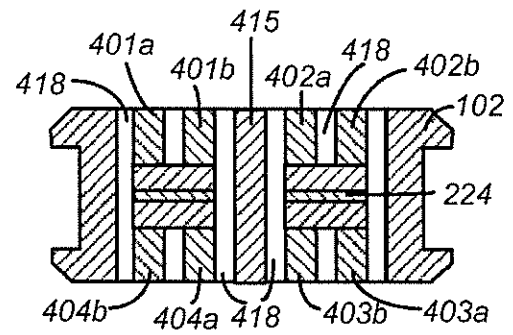
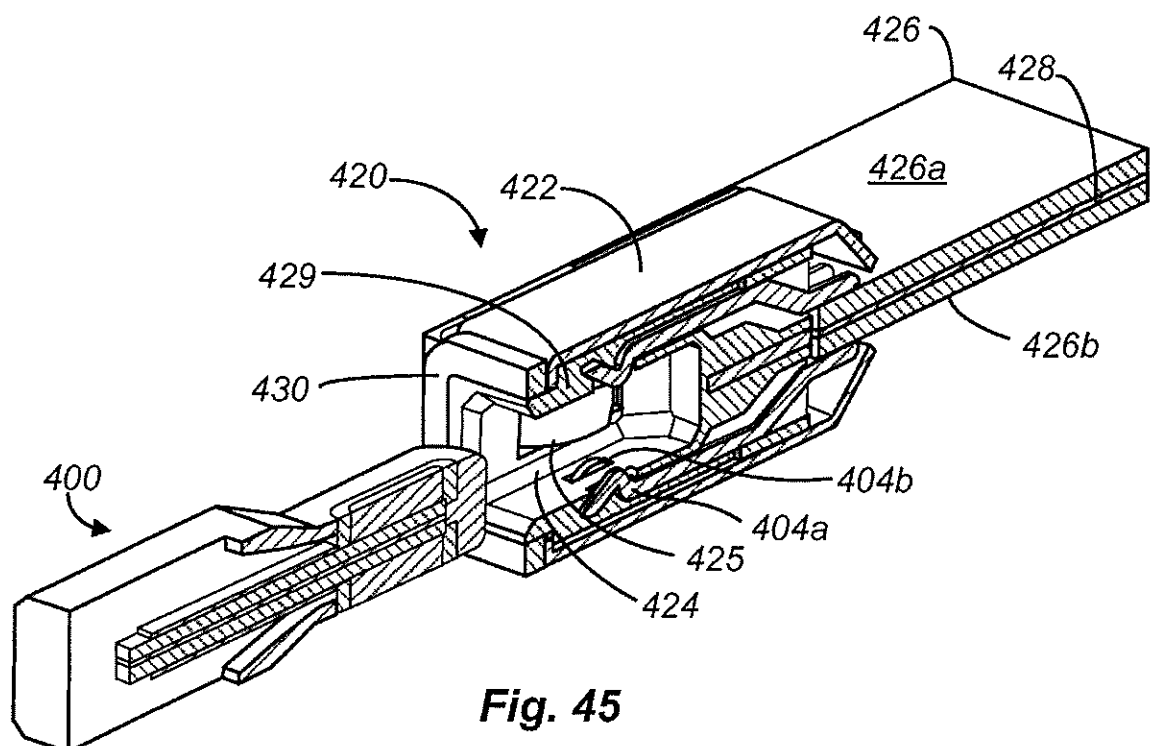


**Fig. 42**

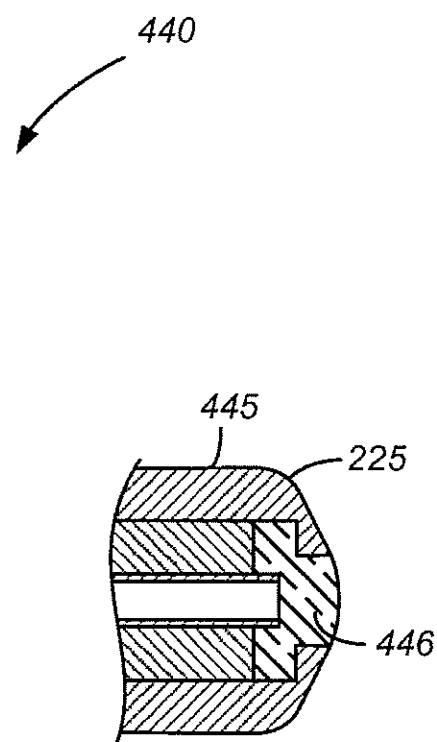
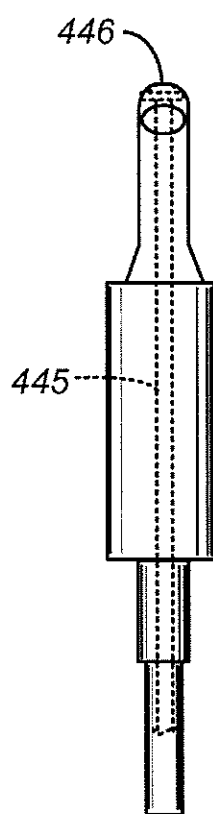
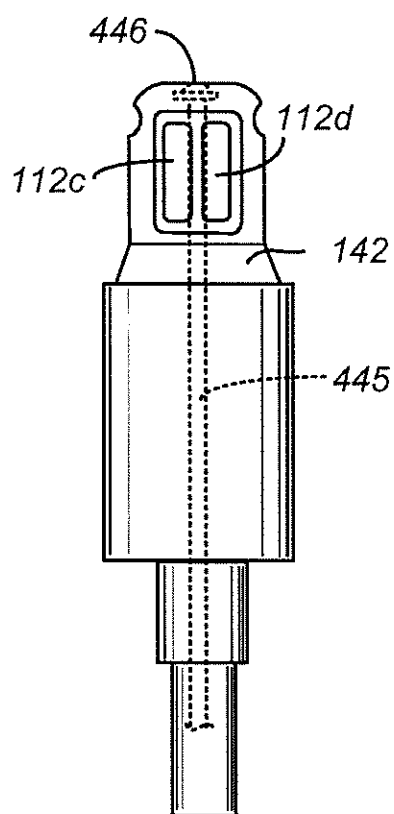
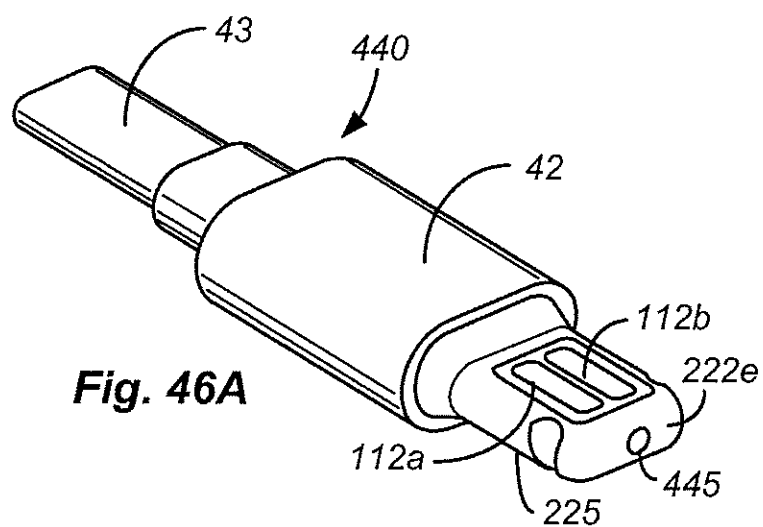


**Fig. 43**

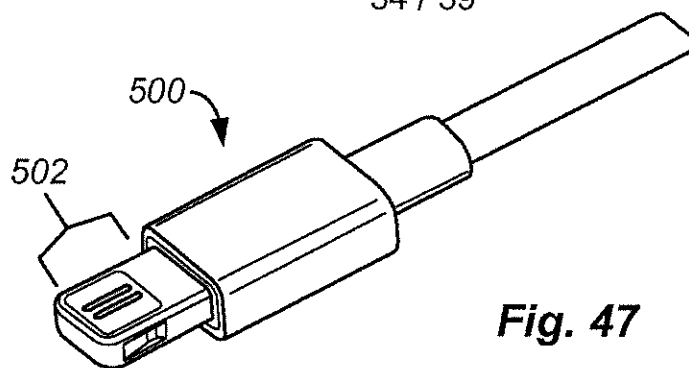
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**Fig. 44A****Fig. 44B****Fig. 45**

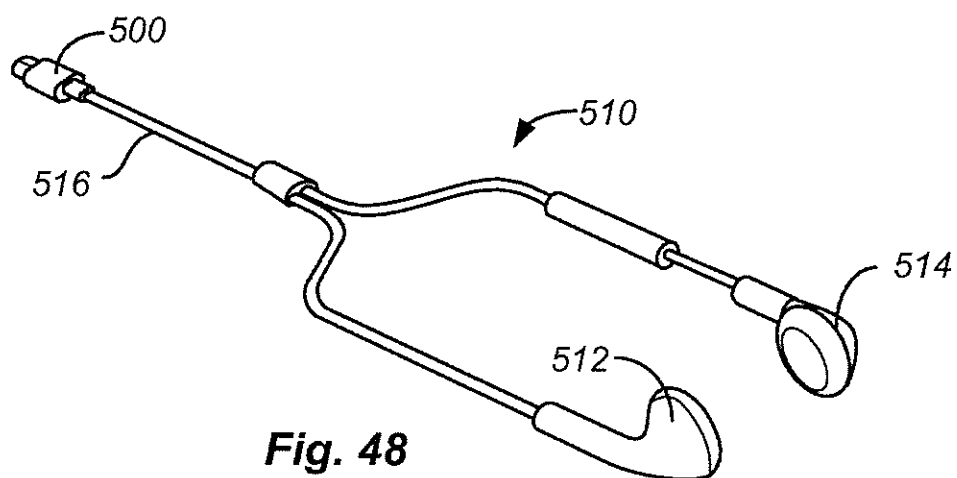
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**Fig. 47**



**Fig. 48**

**Fig. 49A**

|  |  |                |               |  |  |
|--|--|----------------|---------------|--|--|
|  |  | Power<br>out   | Left<br>Audio |  |  |
|  |  | Right<br>Audio | Power<br>out  |  |  |

**Fig. 49B**

|  |  |                |               |  |  |
|--|--|----------------|---------------|--|--|
|  |  | Mic Bias       | Left<br>Audio |  |  |
|  |  | Right<br>Audio |               |  |  |

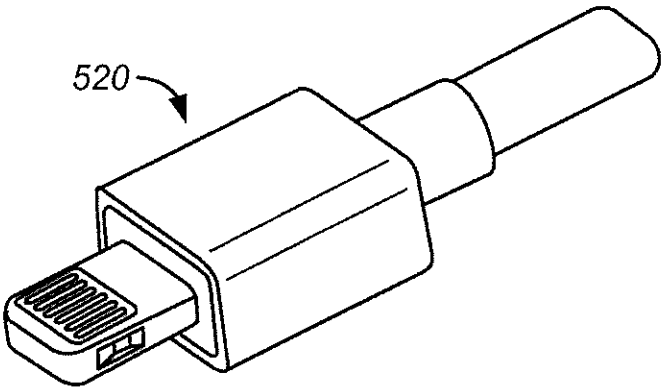


Fig. 50

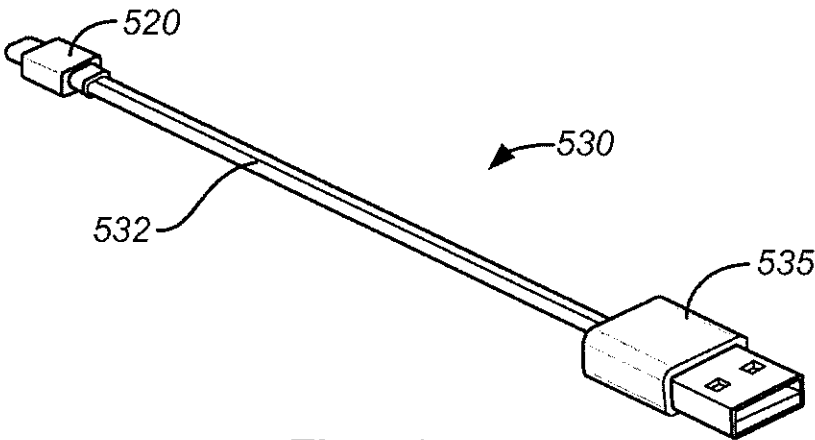
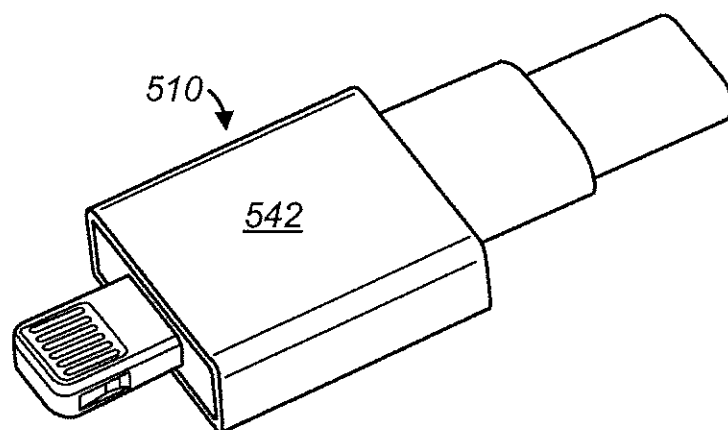


Fig. 51

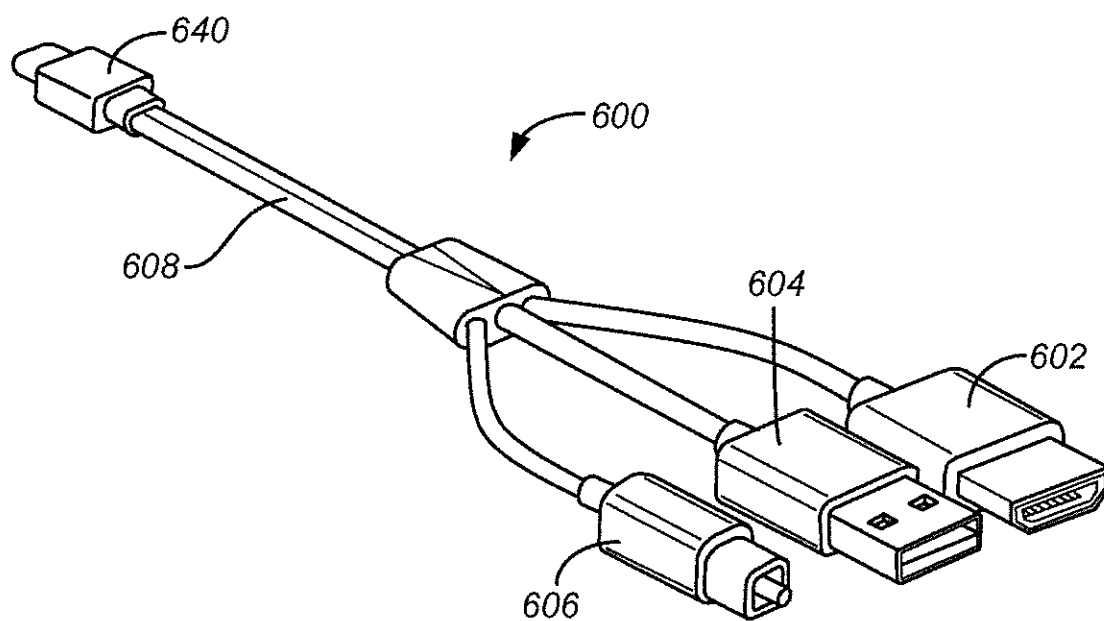
|  |  |          |          |        |        |
|--|--|----------|----------|--------|--------|
|  |  | Power In |          | USB D+ | USB D- |
|  |  |          | Power In |        |        |

Fig. 52

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**Fig. 53**



**Fig. 54**

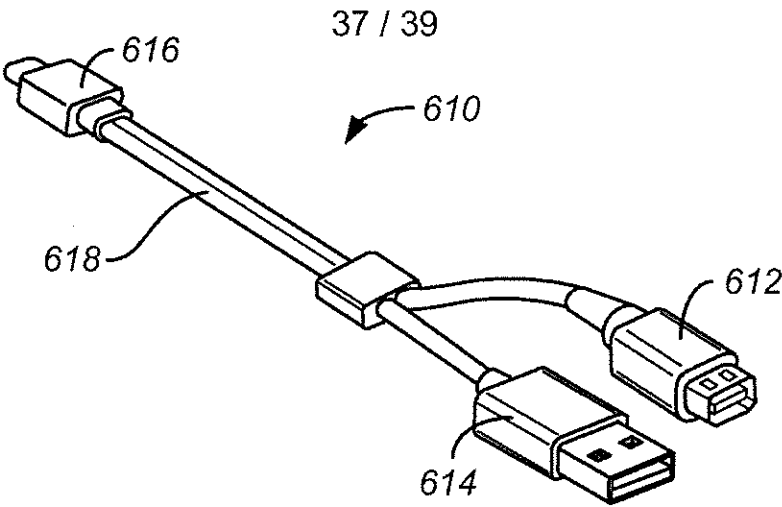


Fig. 55

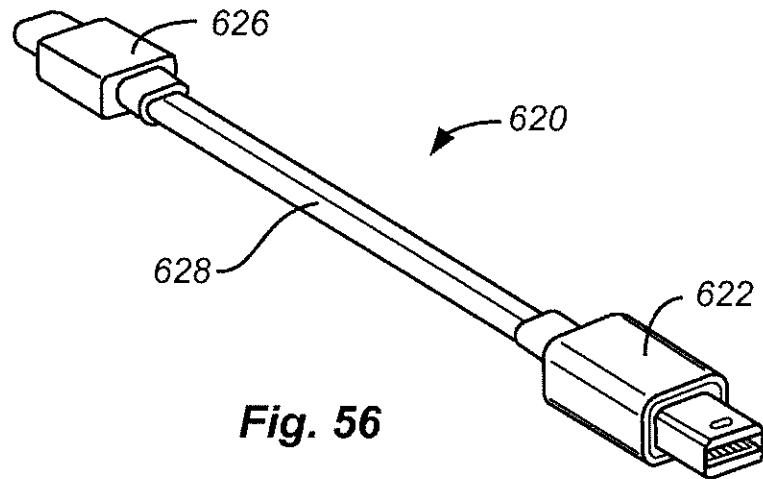


Fig. 56

|        |        |             |            |         |         |
|--------|--------|-------------|------------|---------|---------|
| HS_In+ | HS_In- | Power       | Left Audio | USB D-  | USB D+  |
|        |        | Right Audio | Power      | HS_Out+ | HS_Out- |

Fig. 57

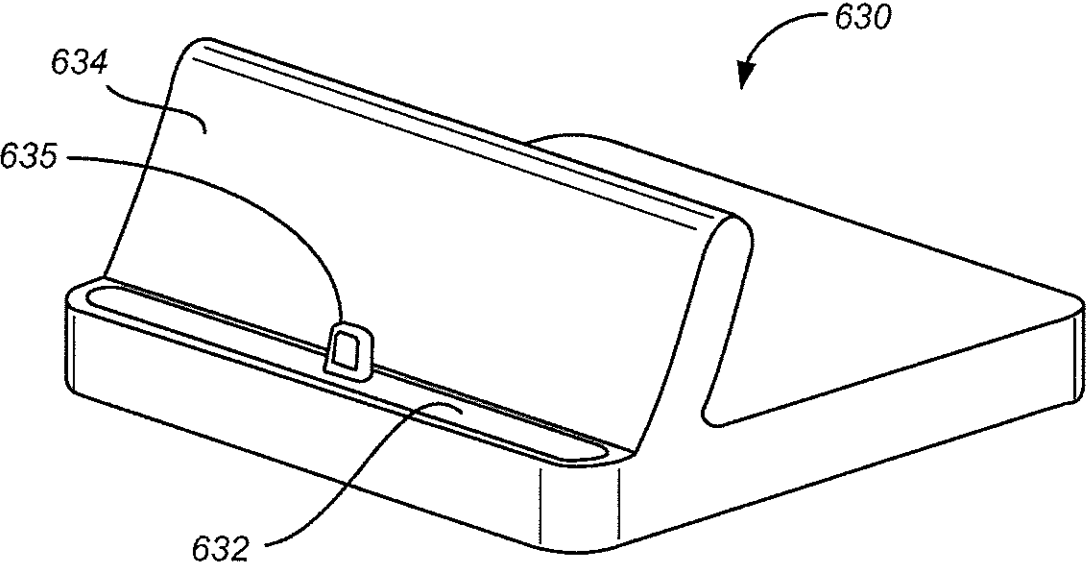
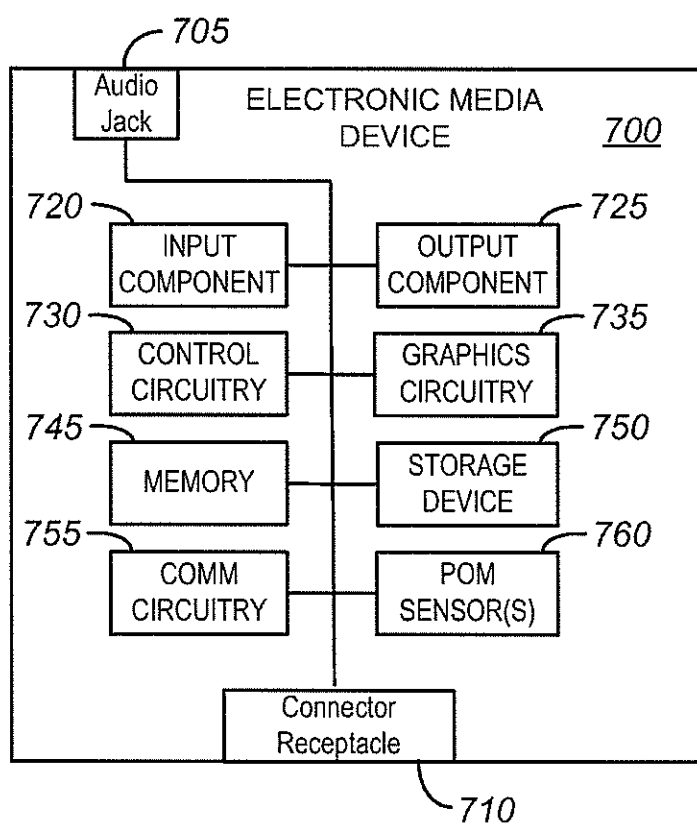
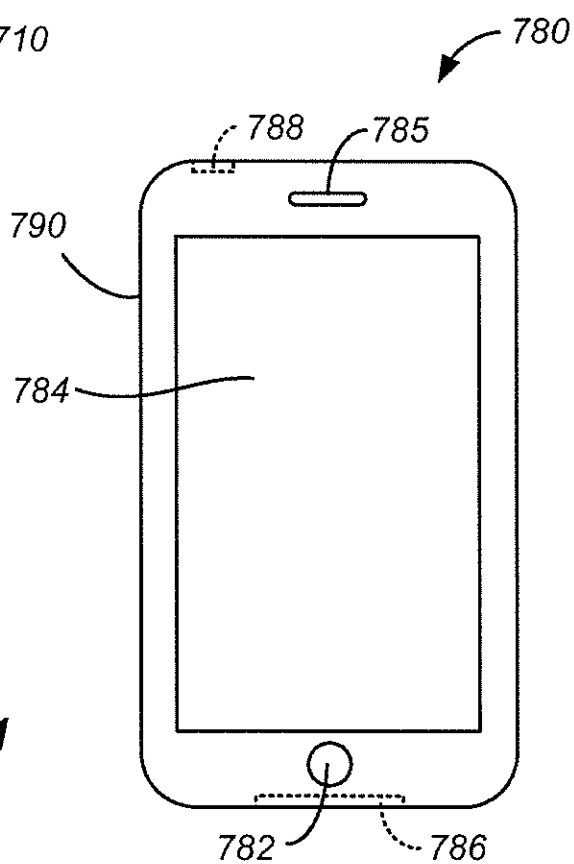


Fig. 58

|       |       |             |            |        |        |
|-------|-------|-------------|------------|--------|--------|
|       |       | Power       | Left Audio | USB D- | USB D+ |
| uart- | uart+ | Right Audio | Power      |        |        |

Fig. 59

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**FIG. 60****FIG. 61**