

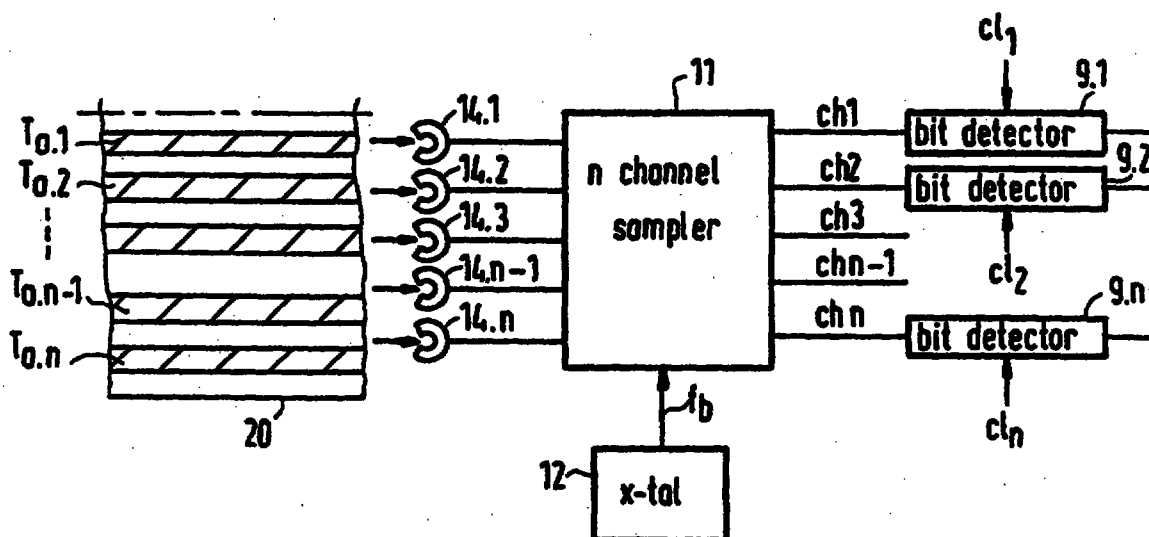


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(54) Title: **ARRANGEMENT FOR REPRODUCING N DIGITAL SIGNALS FROM N ADJACENT TRACKS ON A RECORD CARRIER**



(57) Abstract

For the clock extraction (Cl_n) from n digital signals read from each of n tracks ($T_{a,1}$ to $T_{a,n}$) there are provided n phase-locked loops (13. n). The voltage-controlled oscillator (21) is common to the n phase-locked loops. A phase-locked loop further comprises a phase-shifter unit (25. n) having an input coupled to the output of the voltage-controlled oscillator (21) and an output coupled to the second input of the phase comparator (17. n) in the loop. The phase shift in the phase-shifter unit (25. n) is controlled under the influence of a control signal which is derived by a control signal generator (26. n) and applied to a control signal input (29) of the phase-shifter unit (25. n). The control signal generator unit (26. n) is an integrating element.

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