

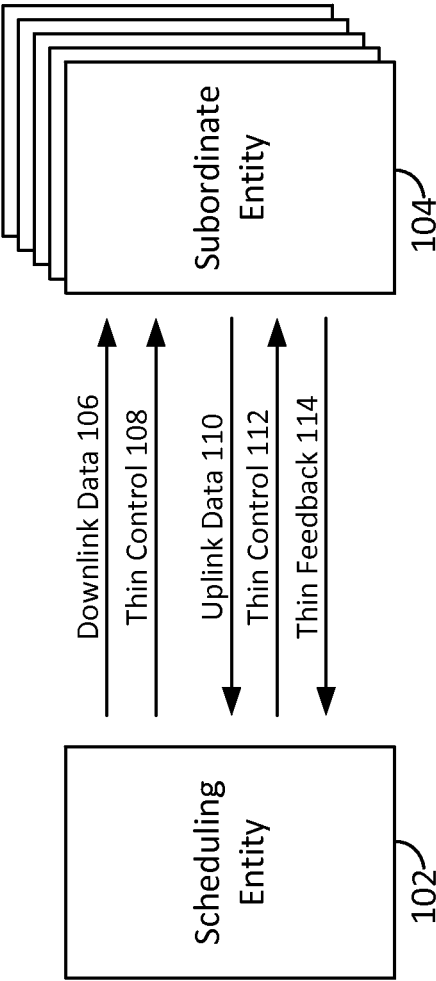


FIG. 1

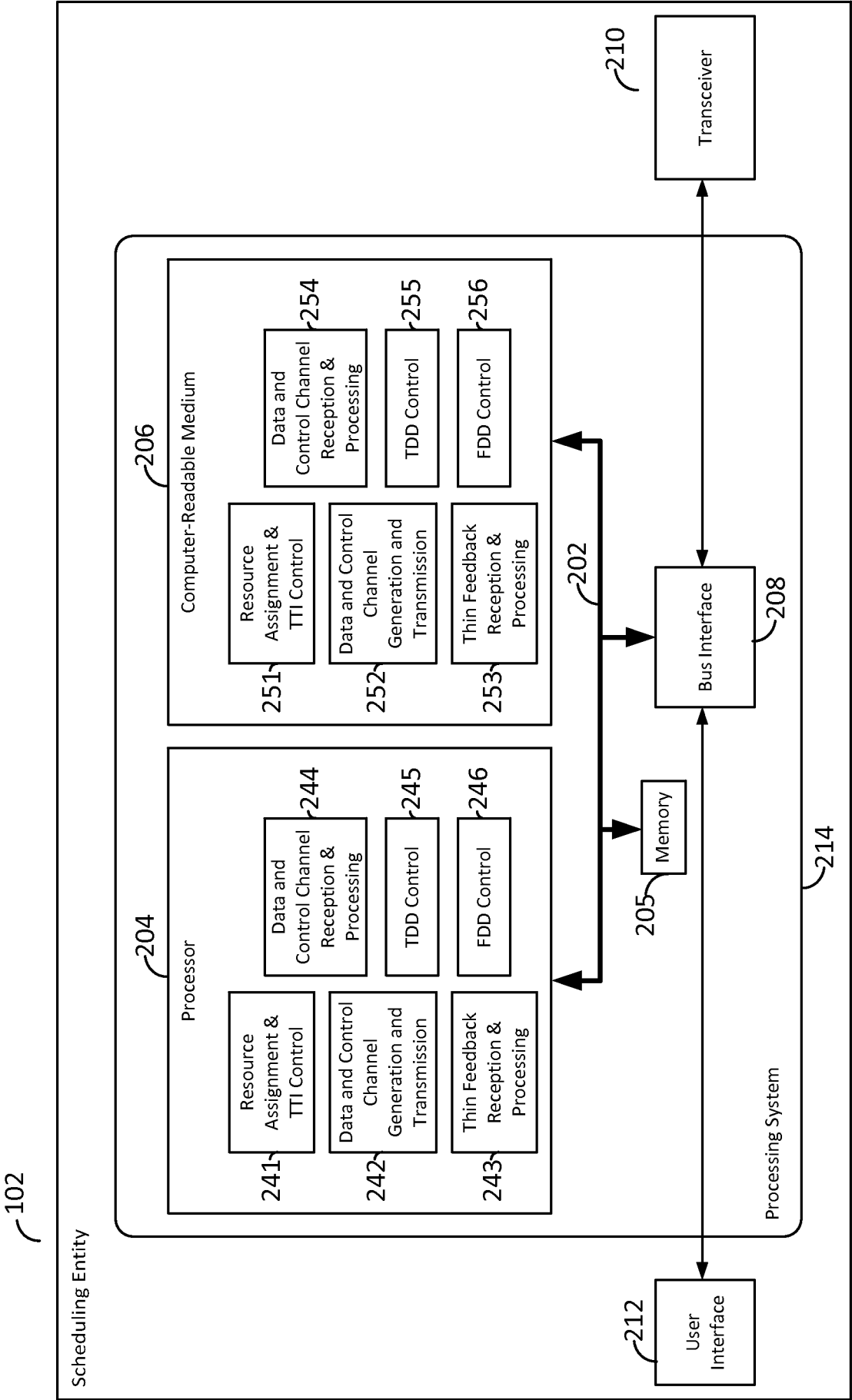


FIG. 2

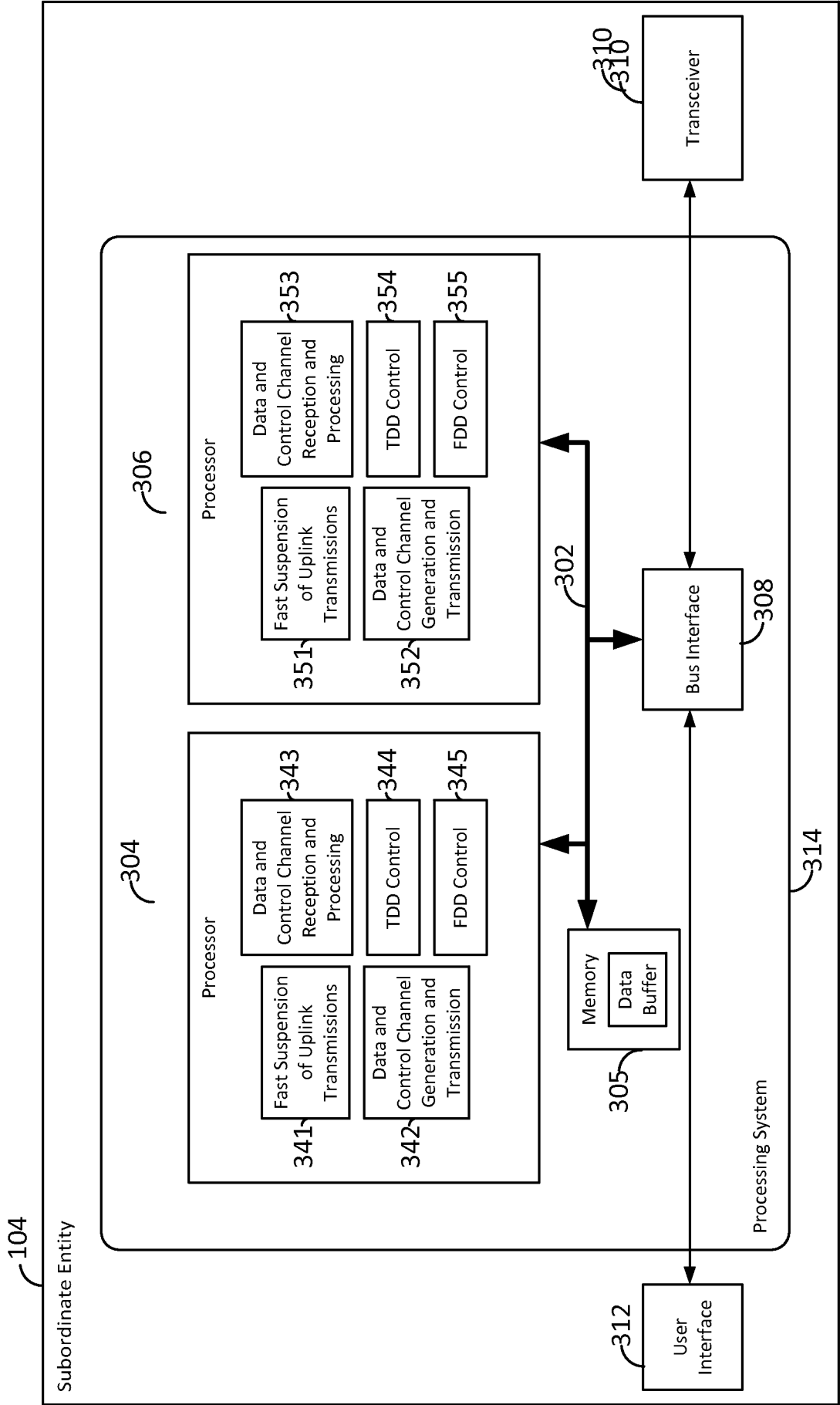
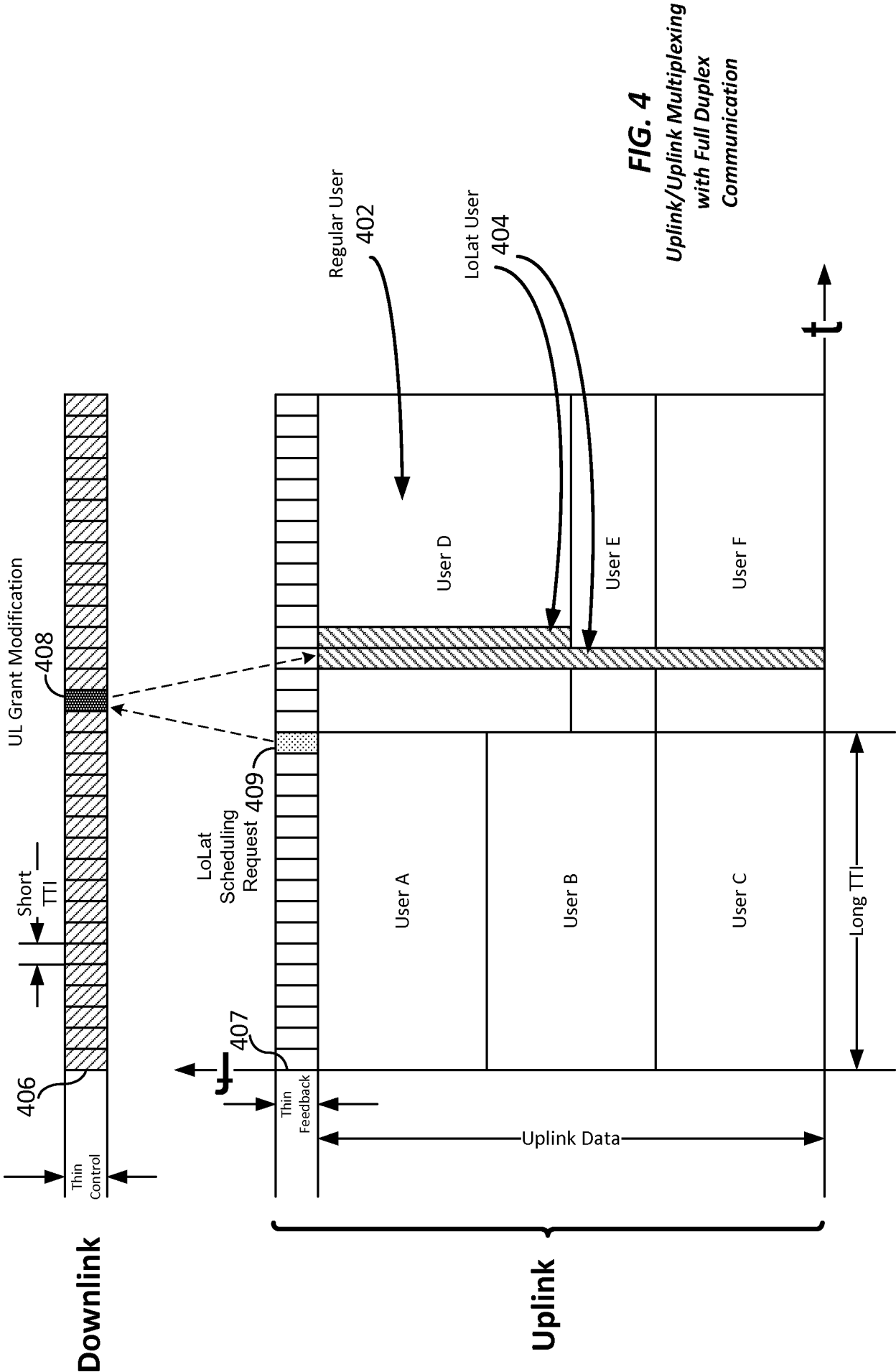
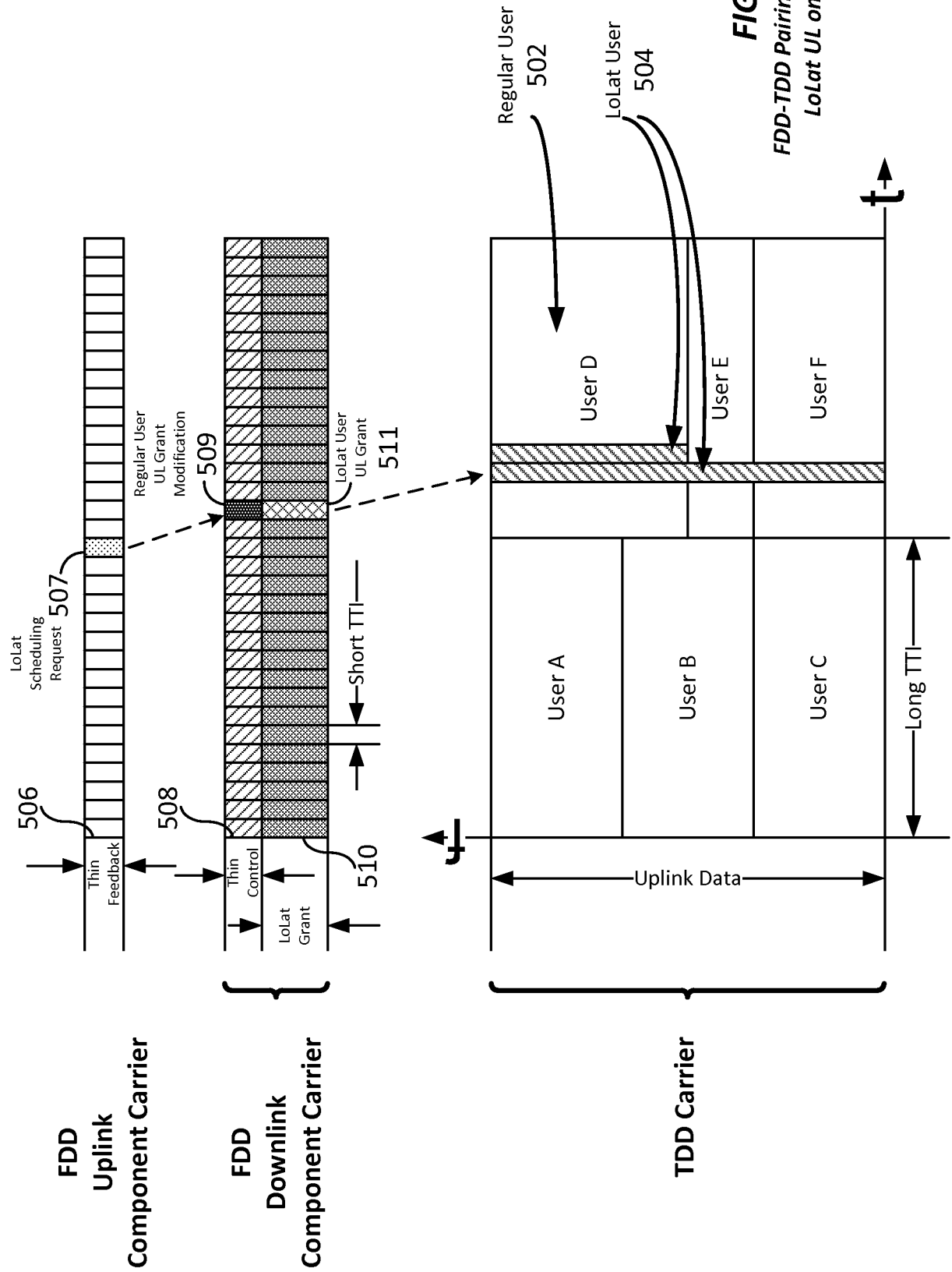


FIG. 3





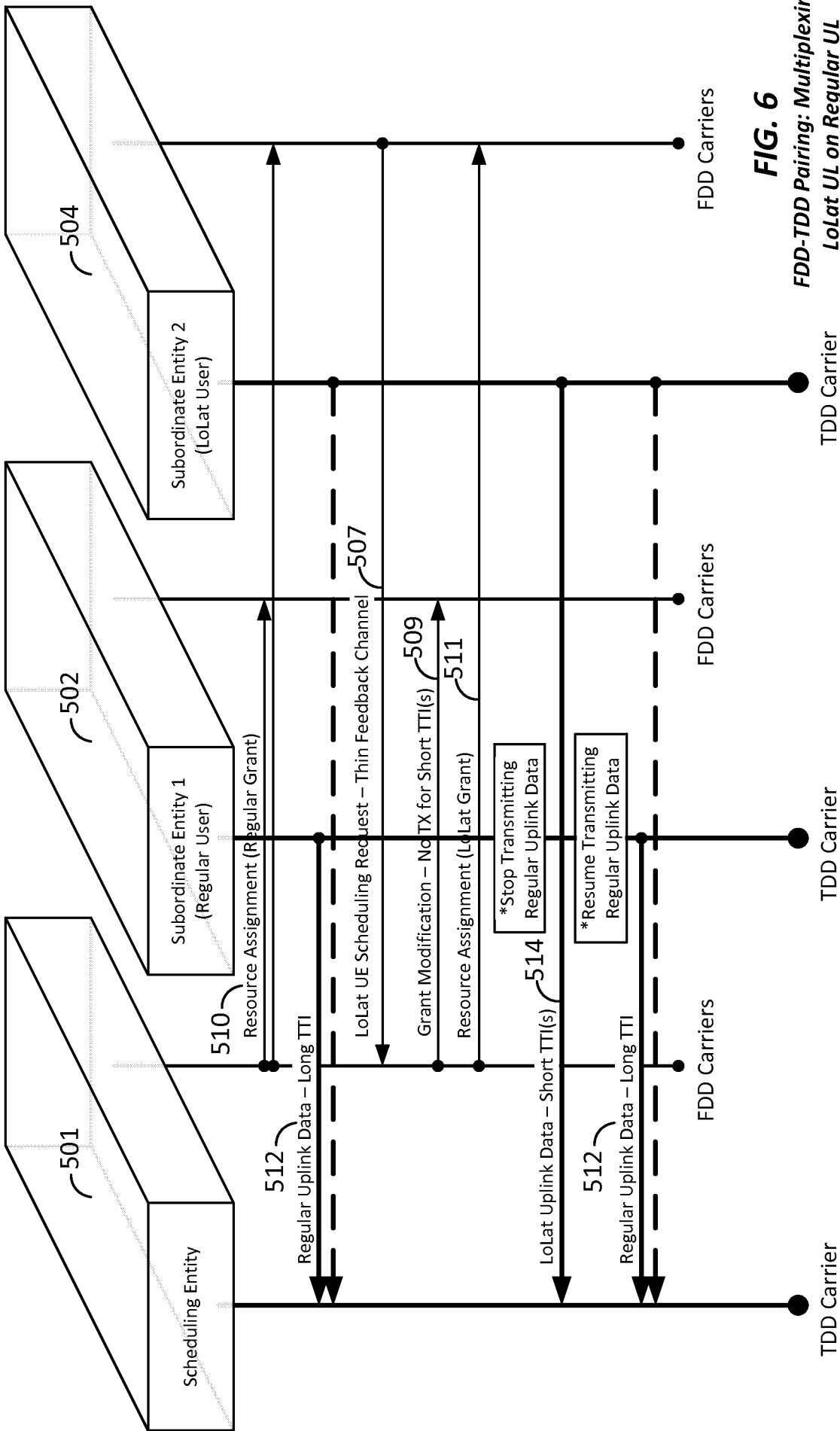


FIG. 6

*FDD-TDD Pairing: Multiplexing
LoLat UL on Regular UL*

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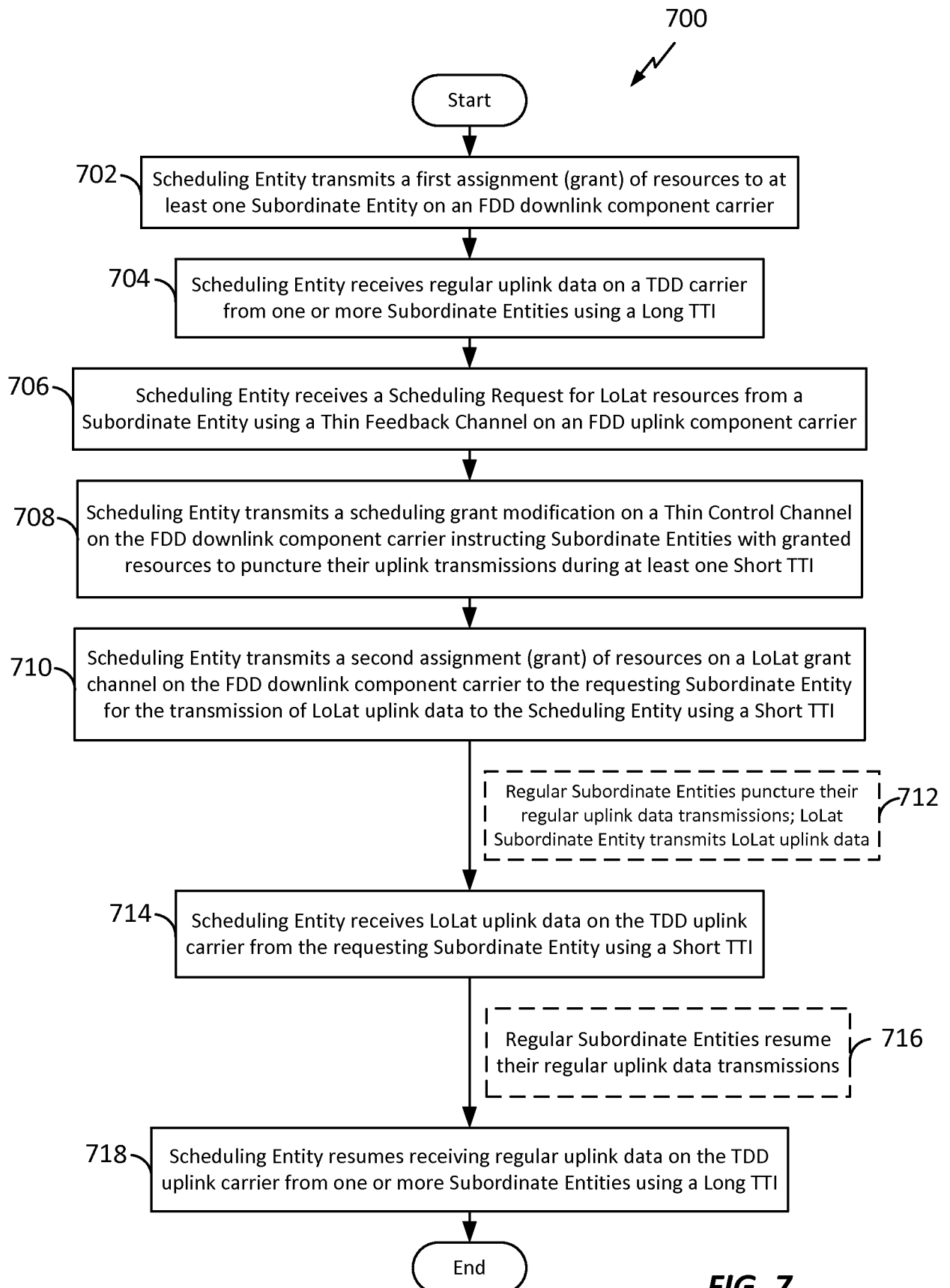


FIG. 7
FDD-TDD Pairing: Multiplexing
LoLat UL on Regular UL

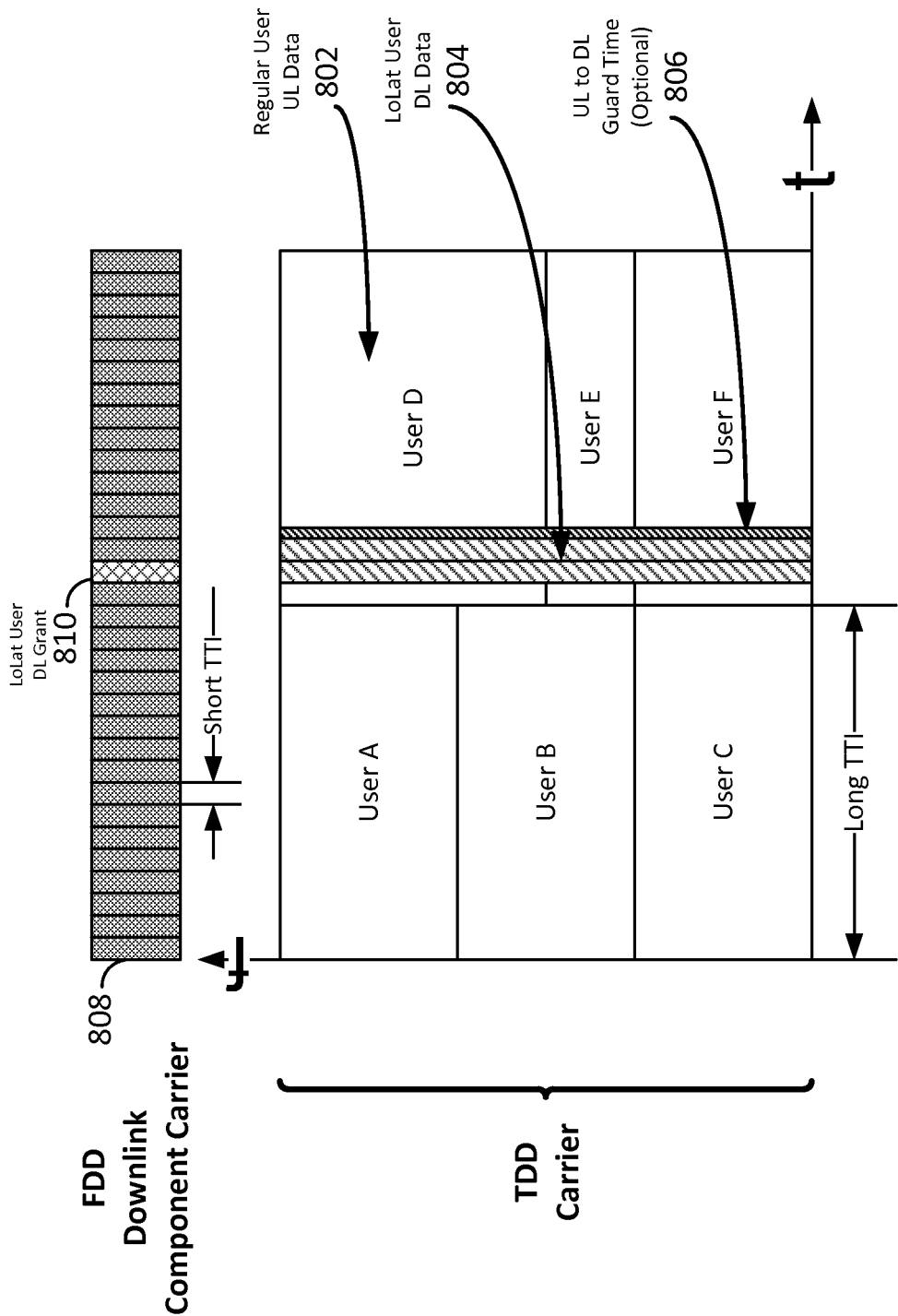
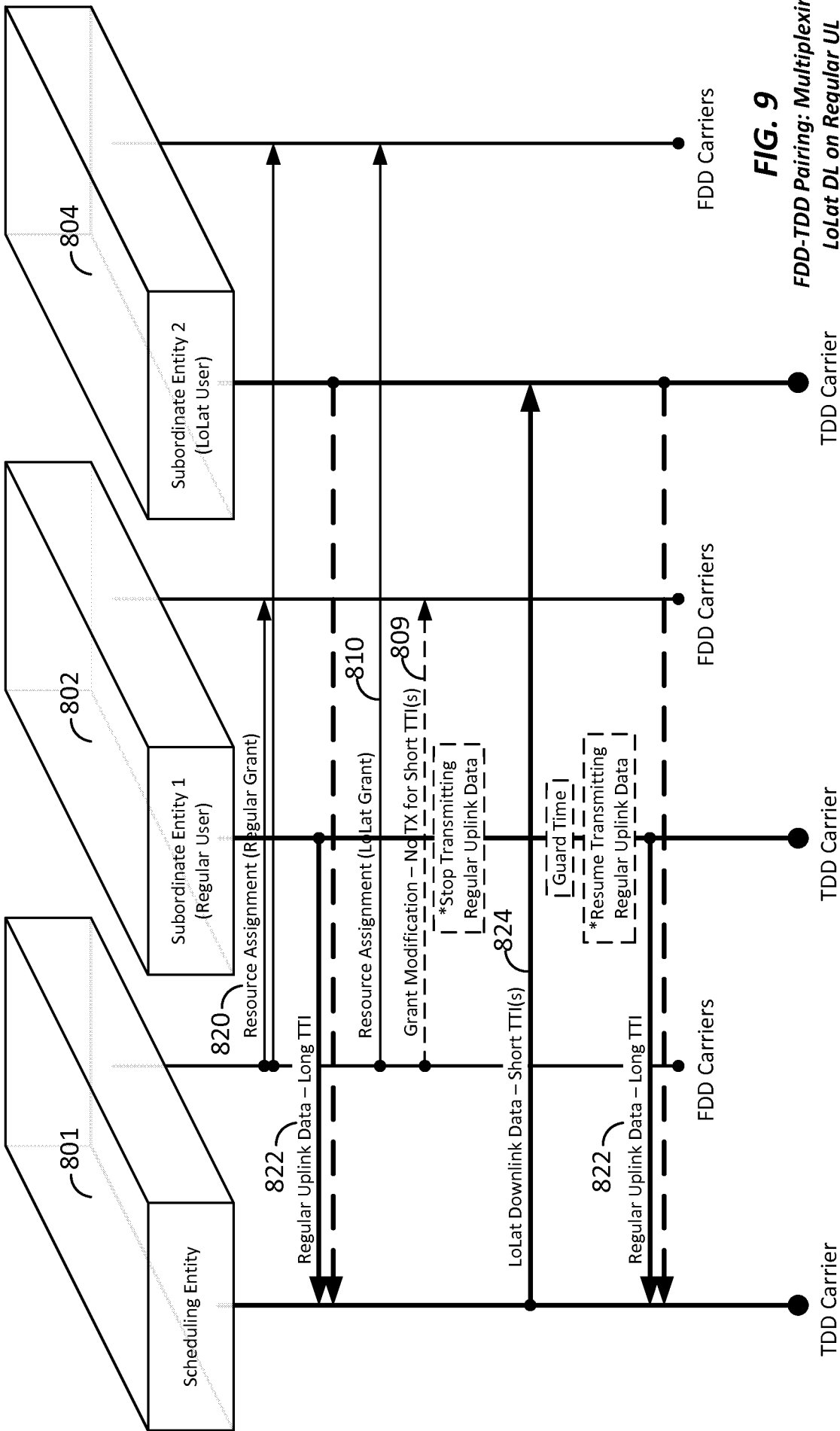
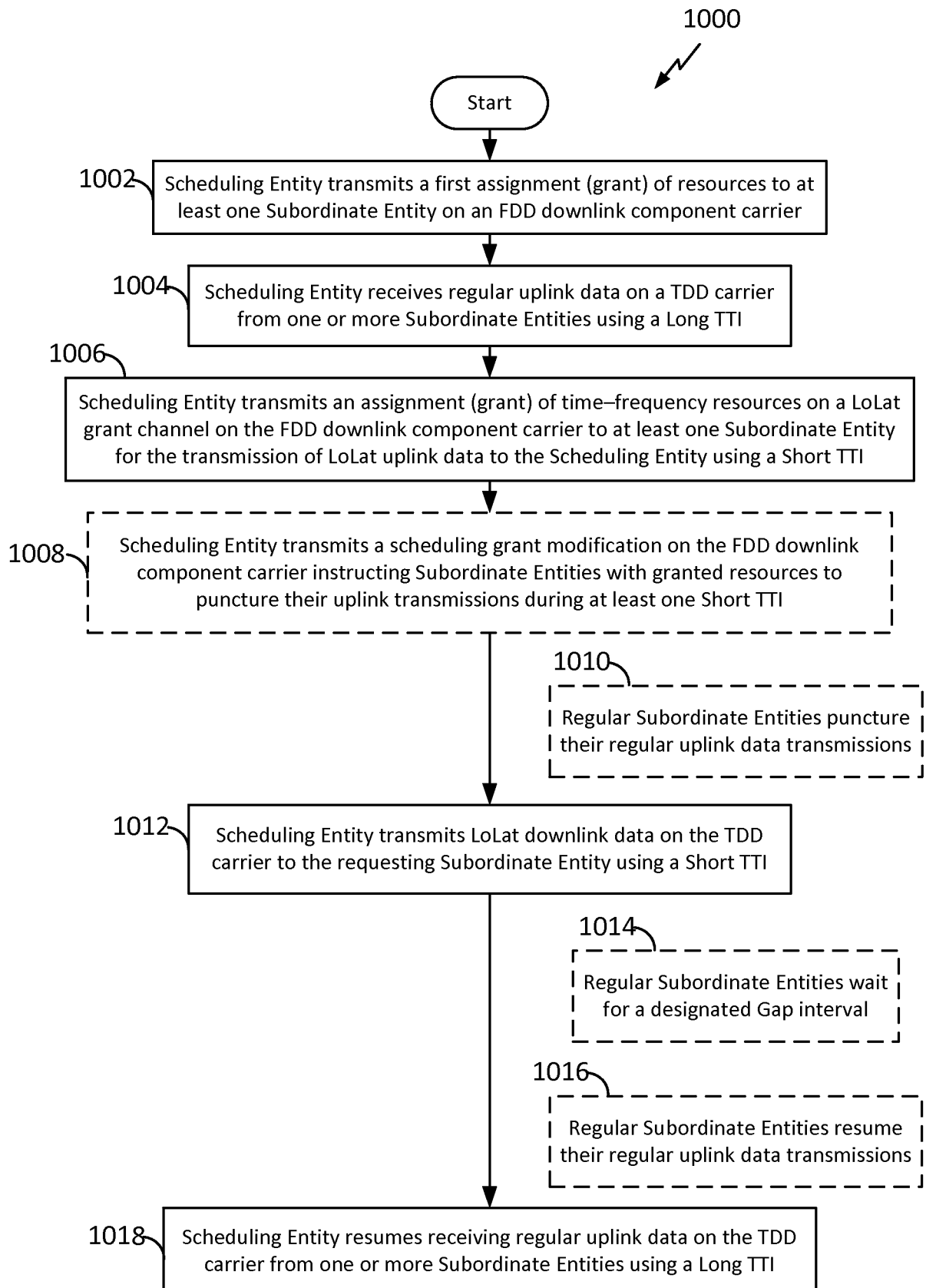


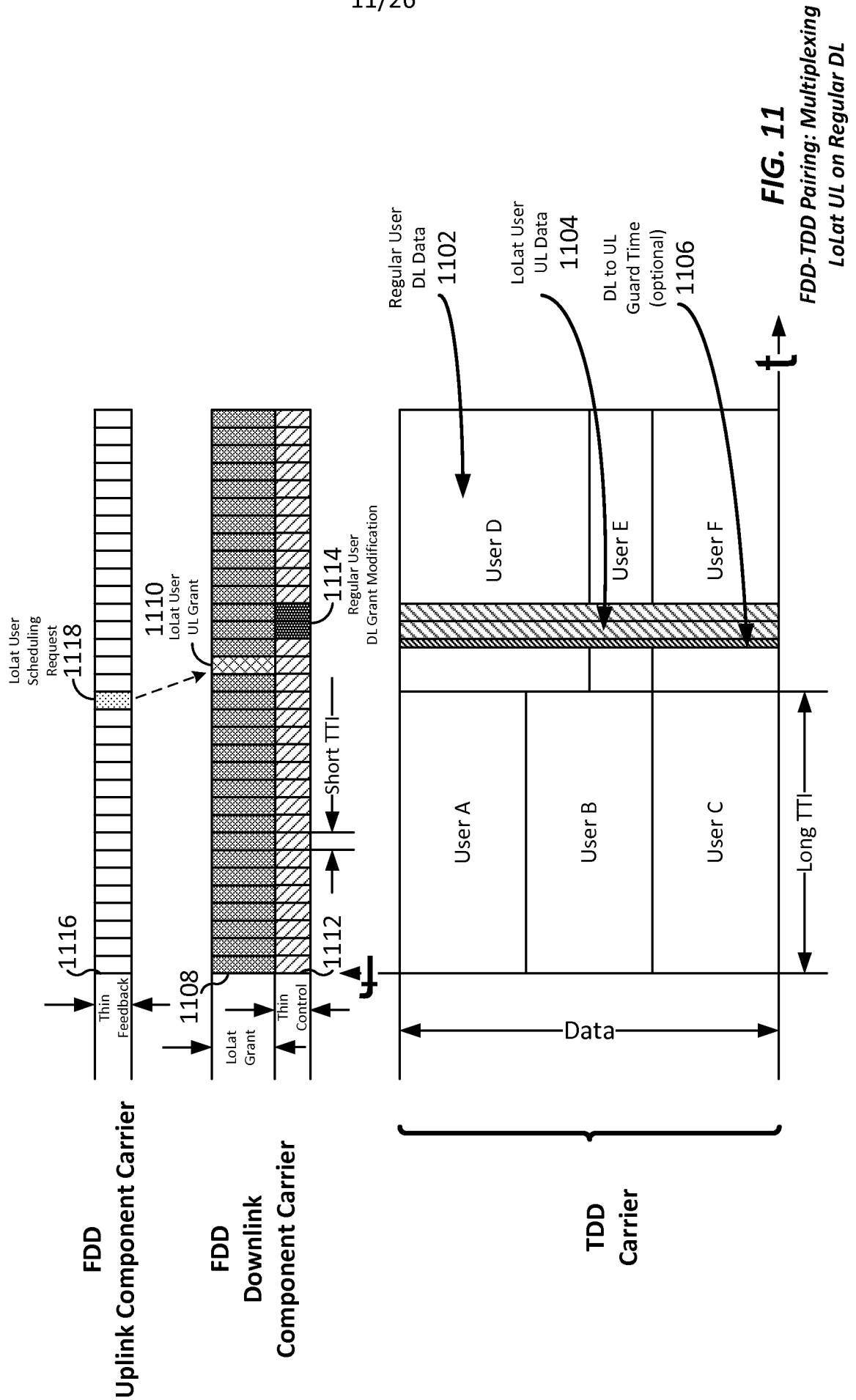
FIG. 8
*FDD-TDD Pairing: Multiplexing
LoLat DL on Regular UL*



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**FIG. 10**

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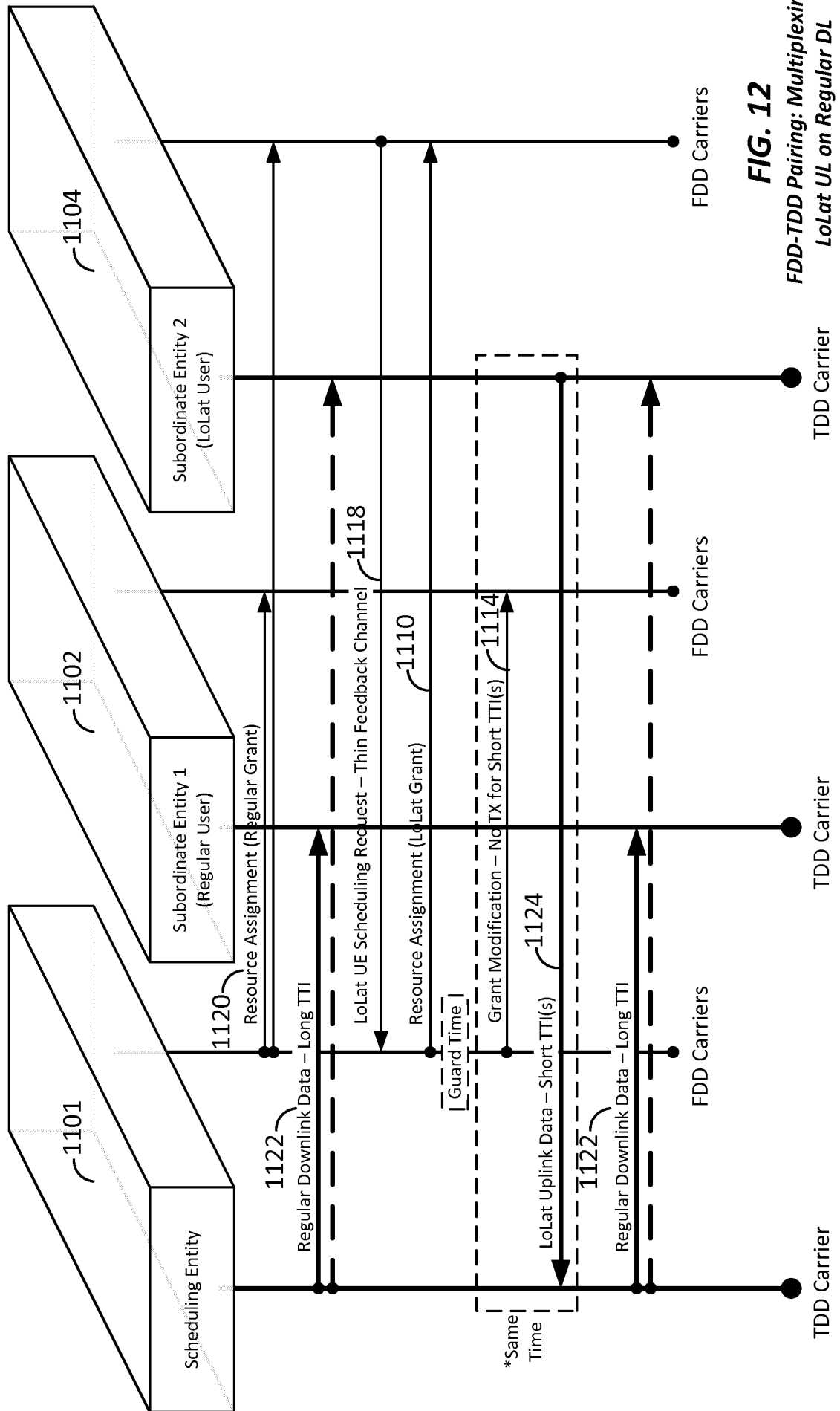
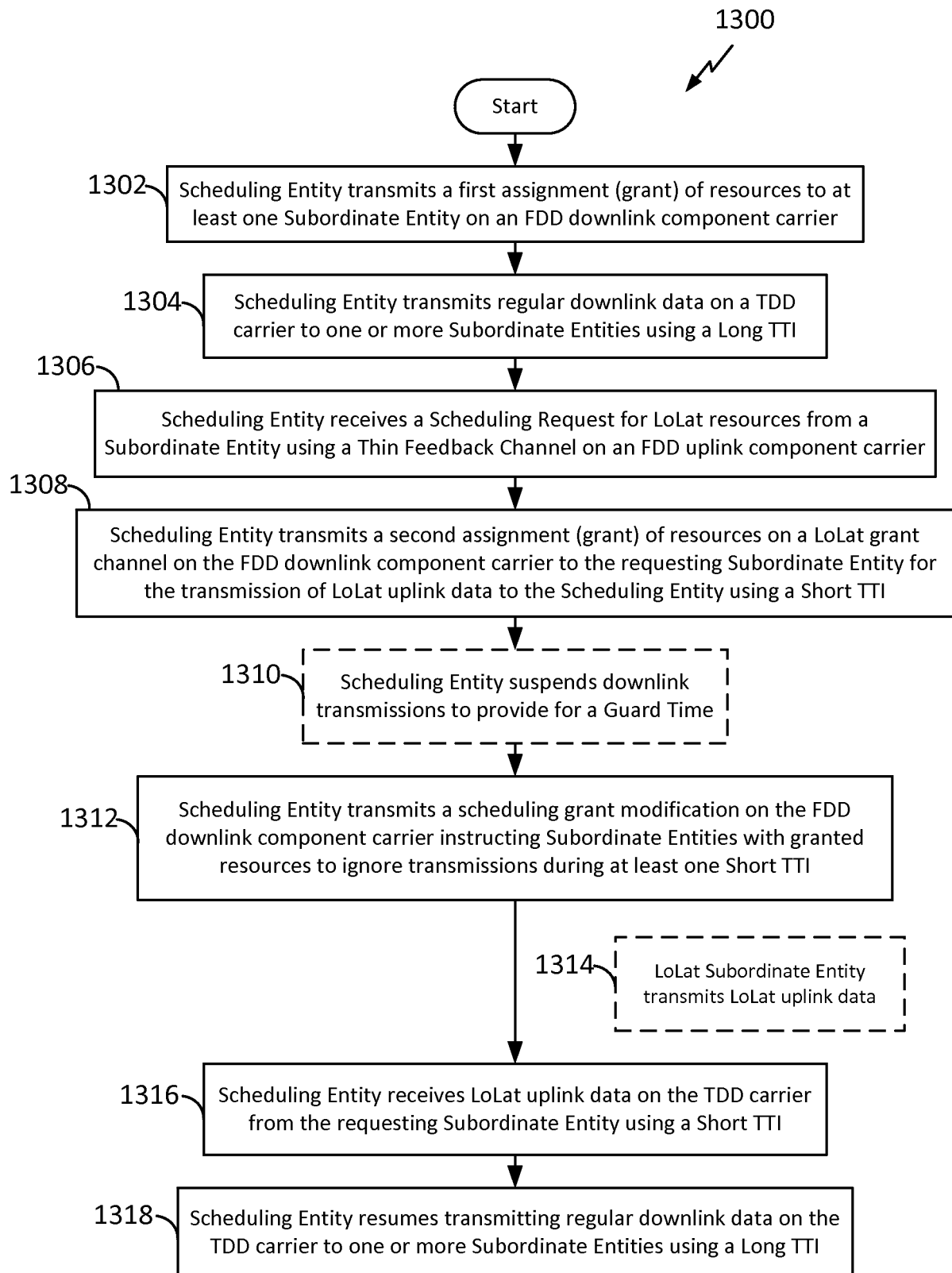


FIG. 12

**FDD-TDD Pairing: Multiplexing
LoLat UL on Regular DL**

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**FIG. 13**

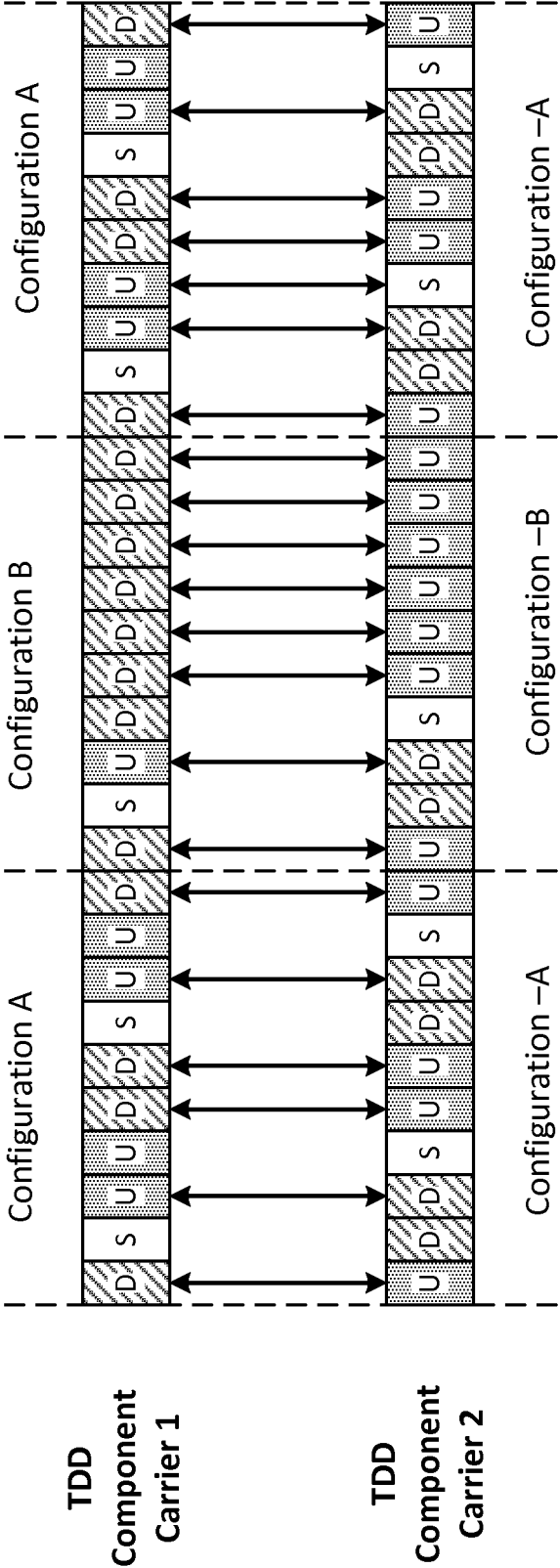


FIG. 14
*TDD-TDD Inverse
(Conjugate) Pairing*

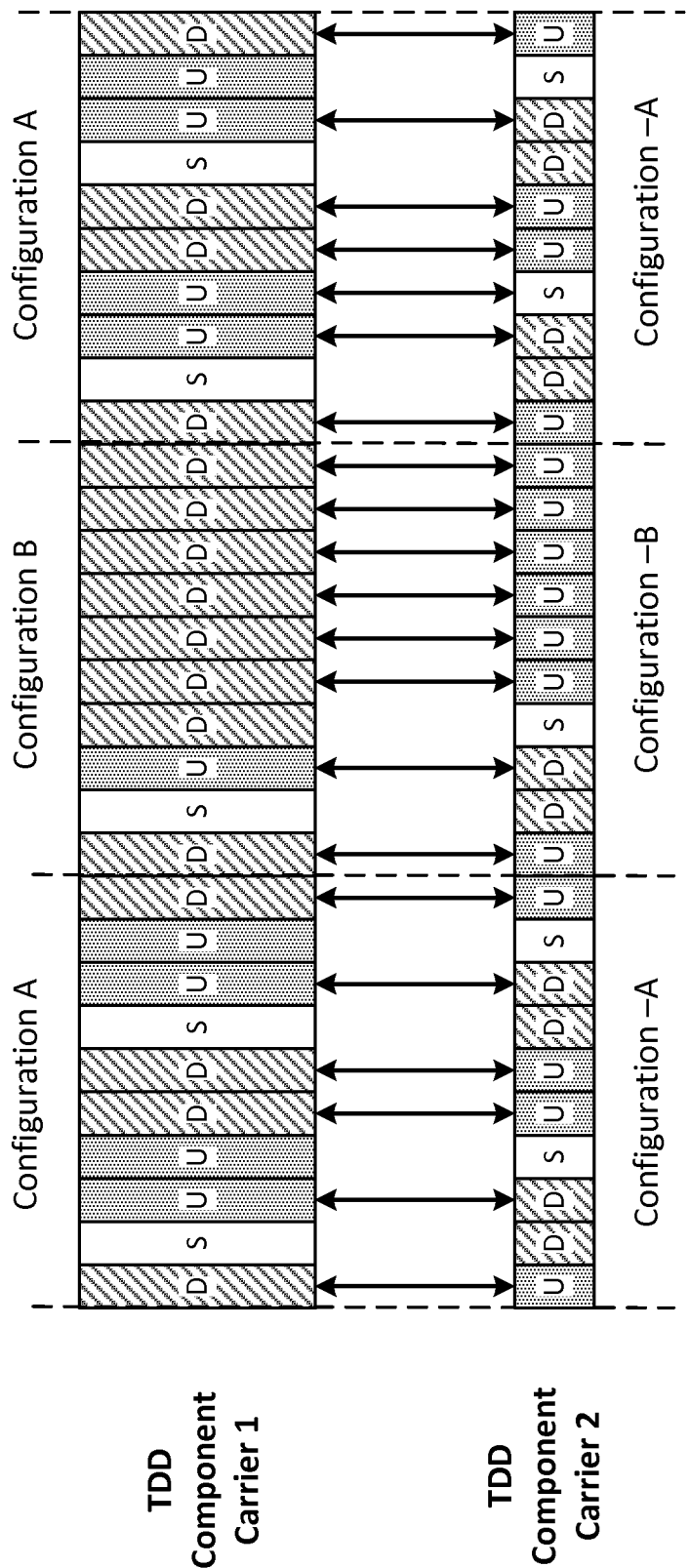
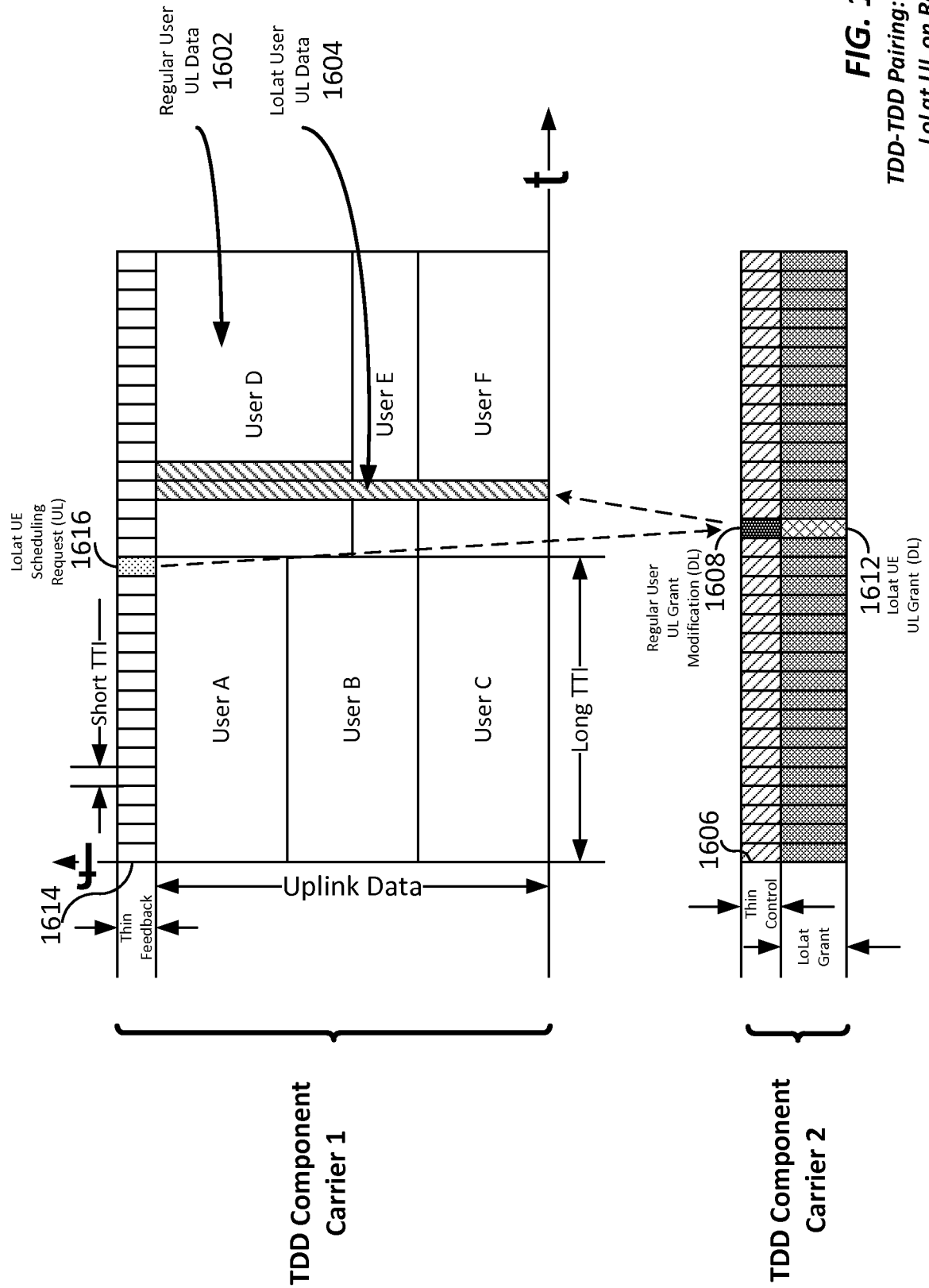


FIG. 15
*TDD-TDD Inverse
(Conjugate) Pairing*



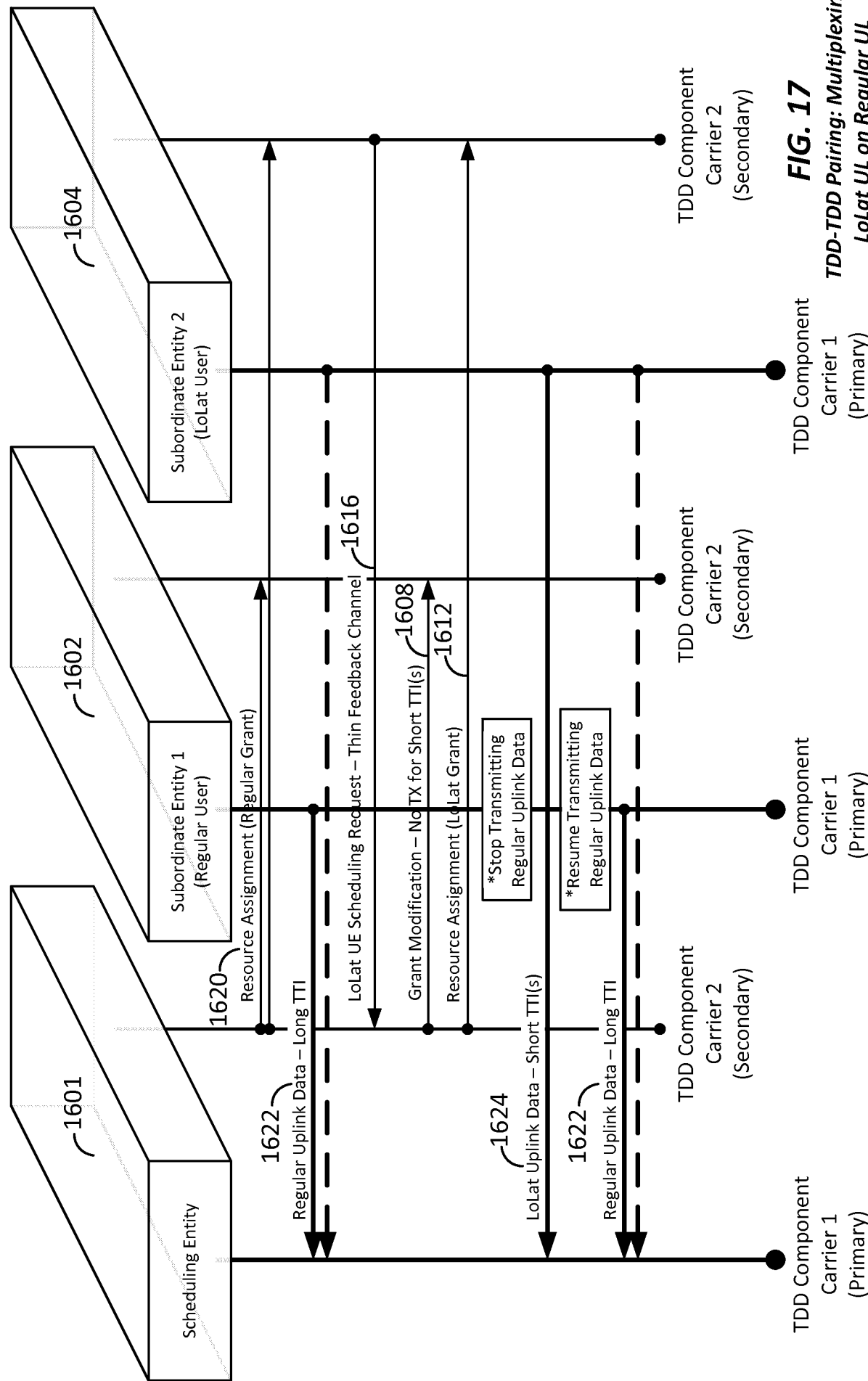
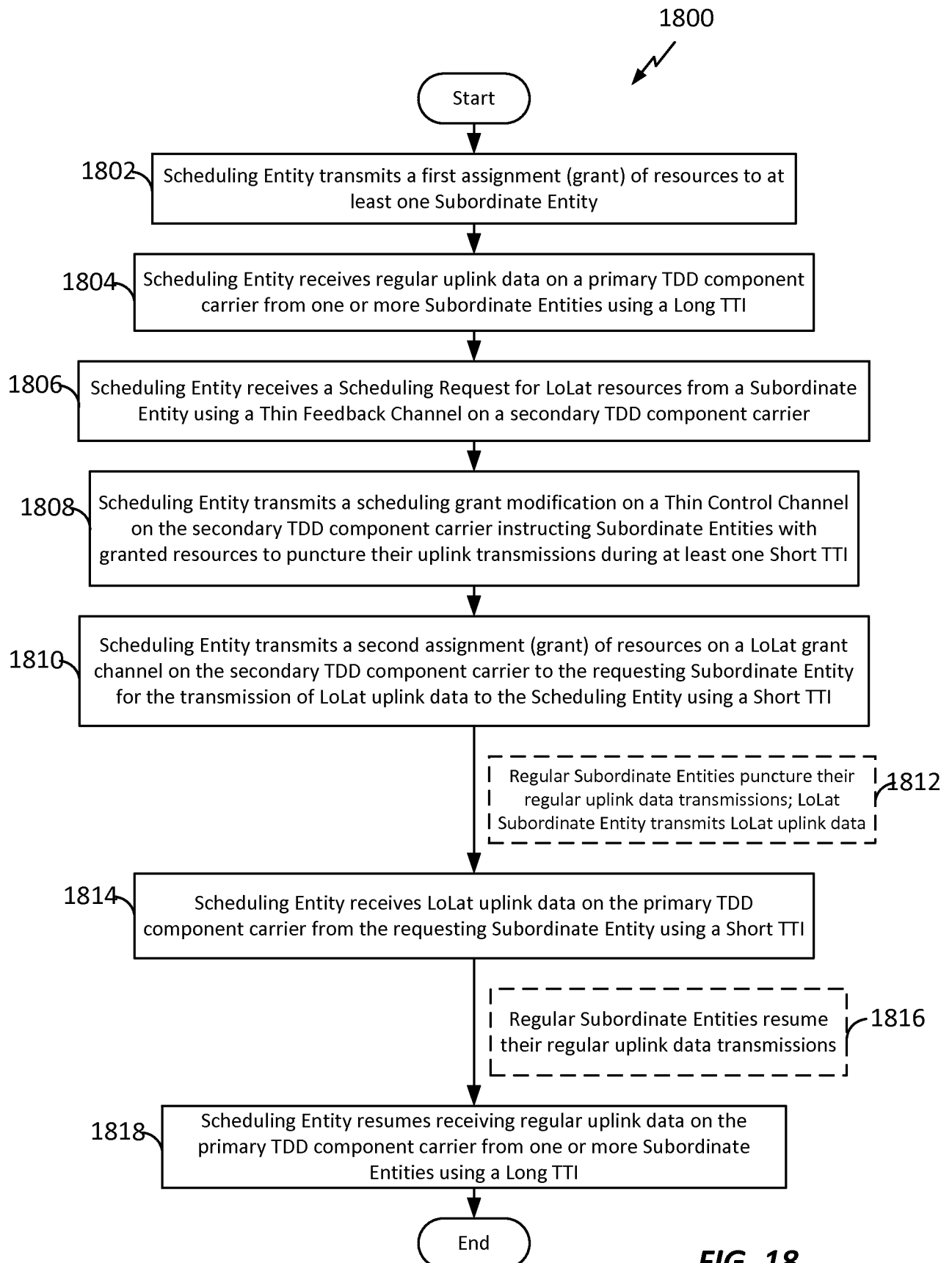


FIG. 17

TDD-TDD Pairing: Multiplexing LoLat UL on Regular UL

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**FIG. 18**

**FDD-TDD Pairing: Multiplexing
LoLat UL on Regular UL**

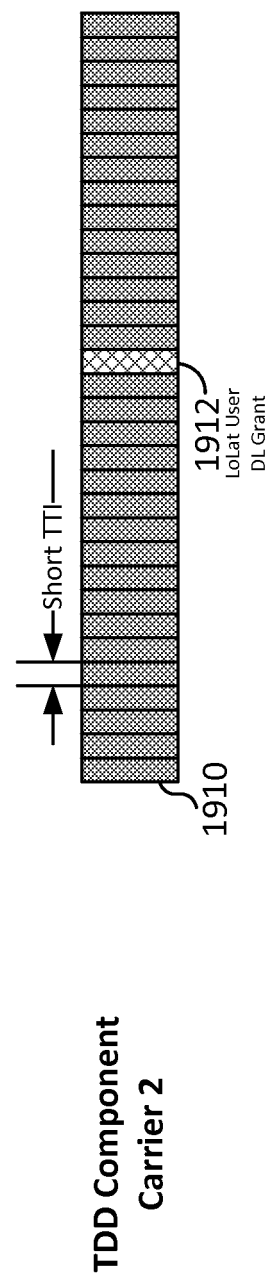
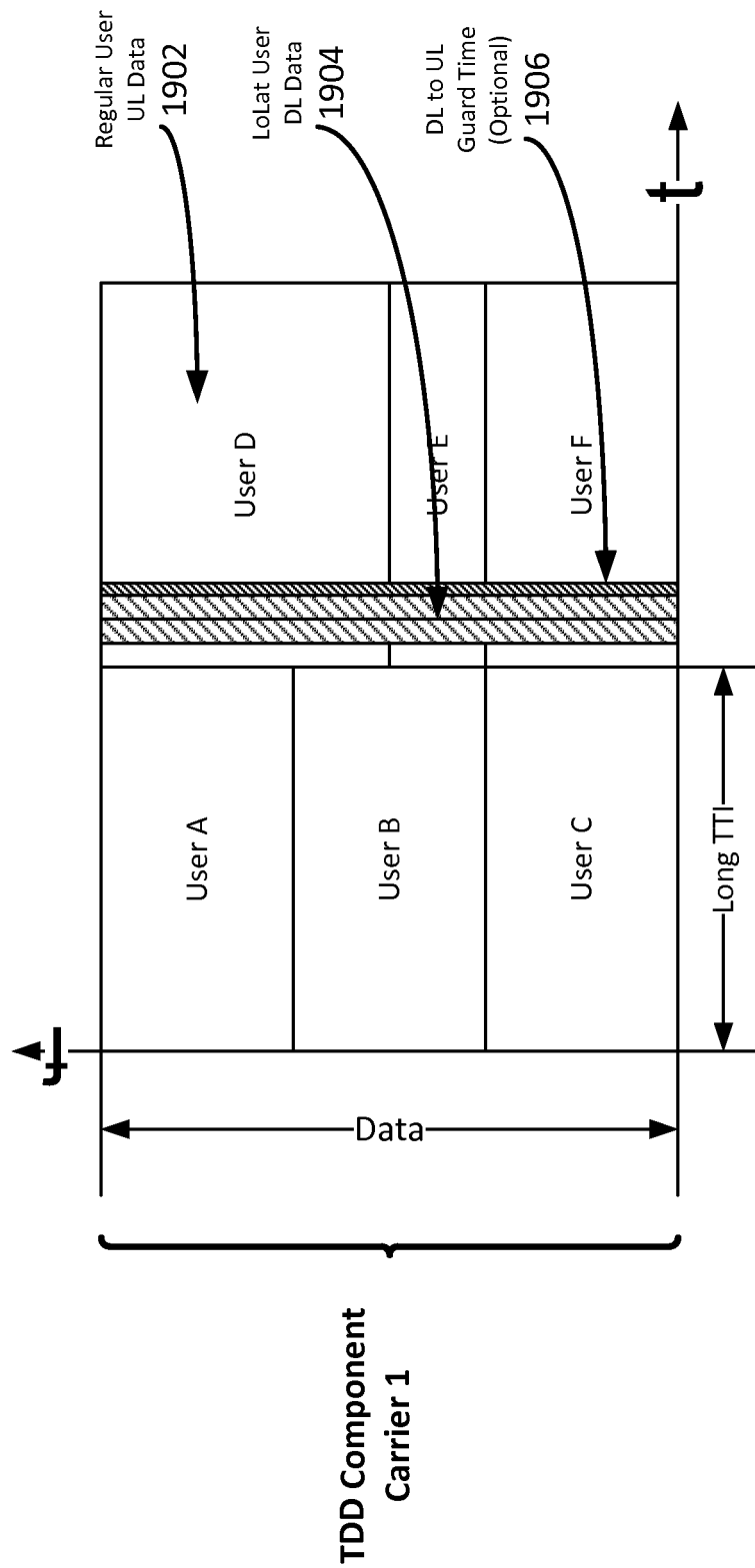


FIG. 19
*TDD-TDD Pairing: Multiplexing
LoLat DL on Regular UL*

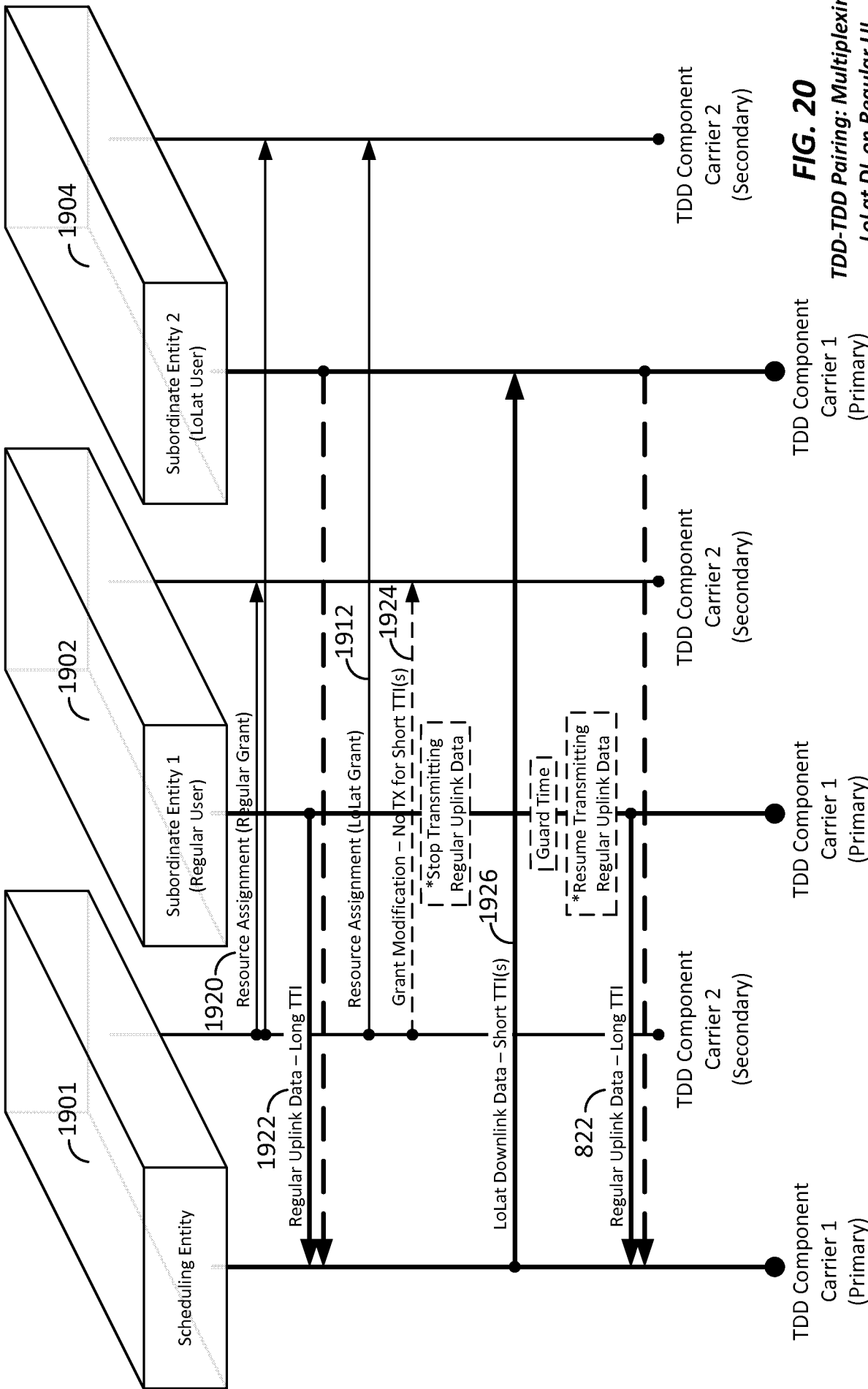
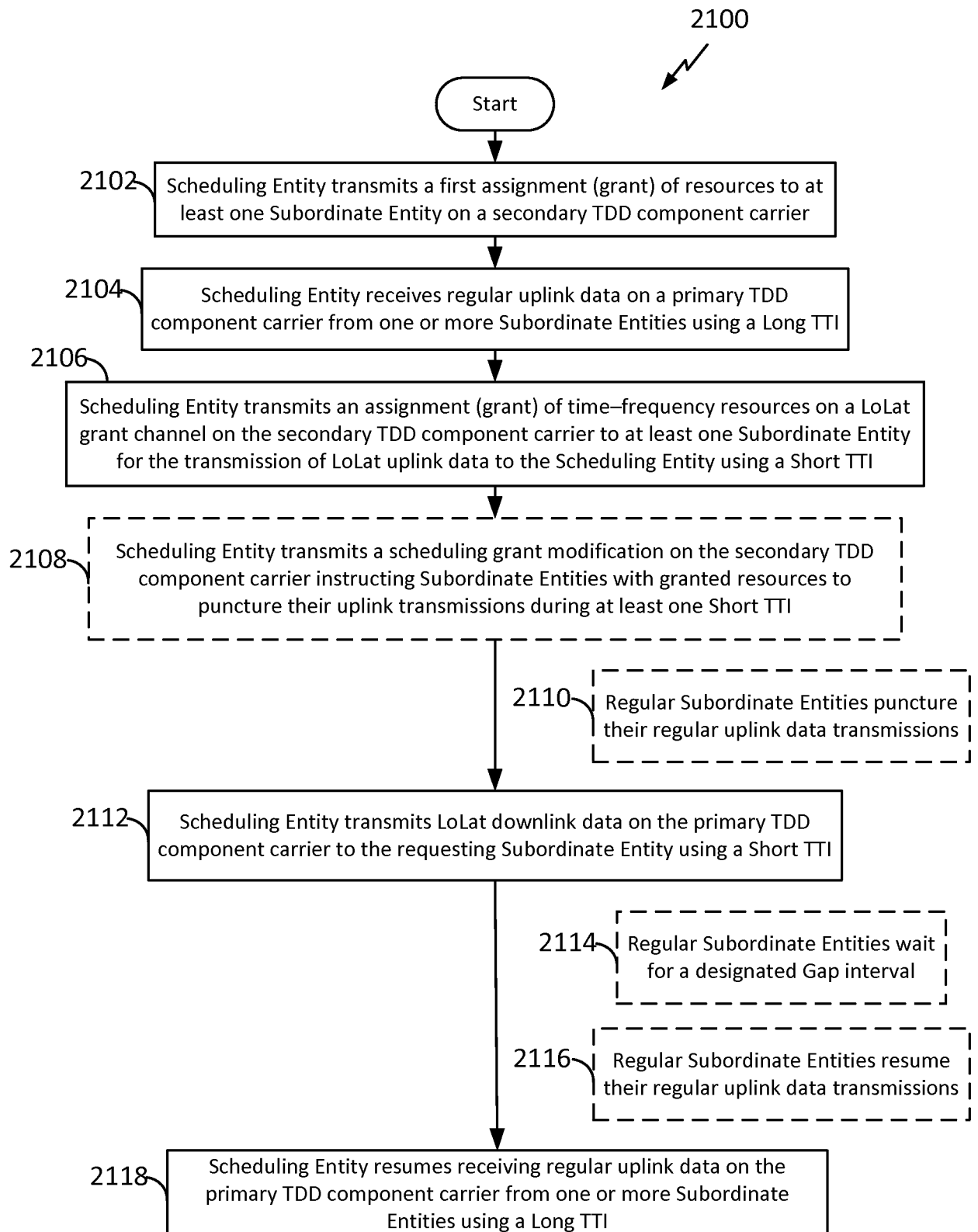


FIG. 20

TDD-TDD Pairing: Multiplexing LoLat DL on Regular UL

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**FIG. 21**

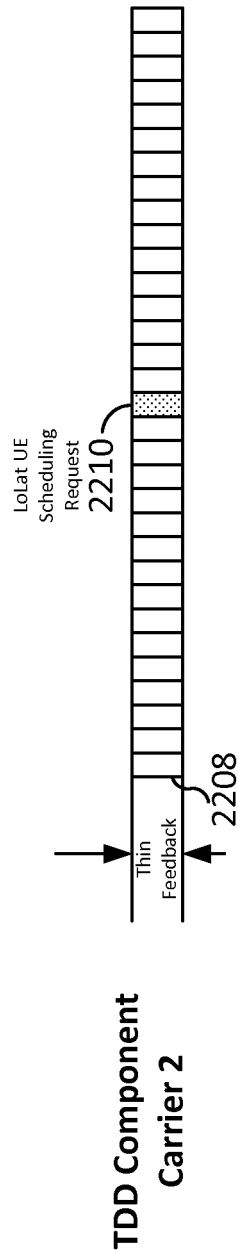
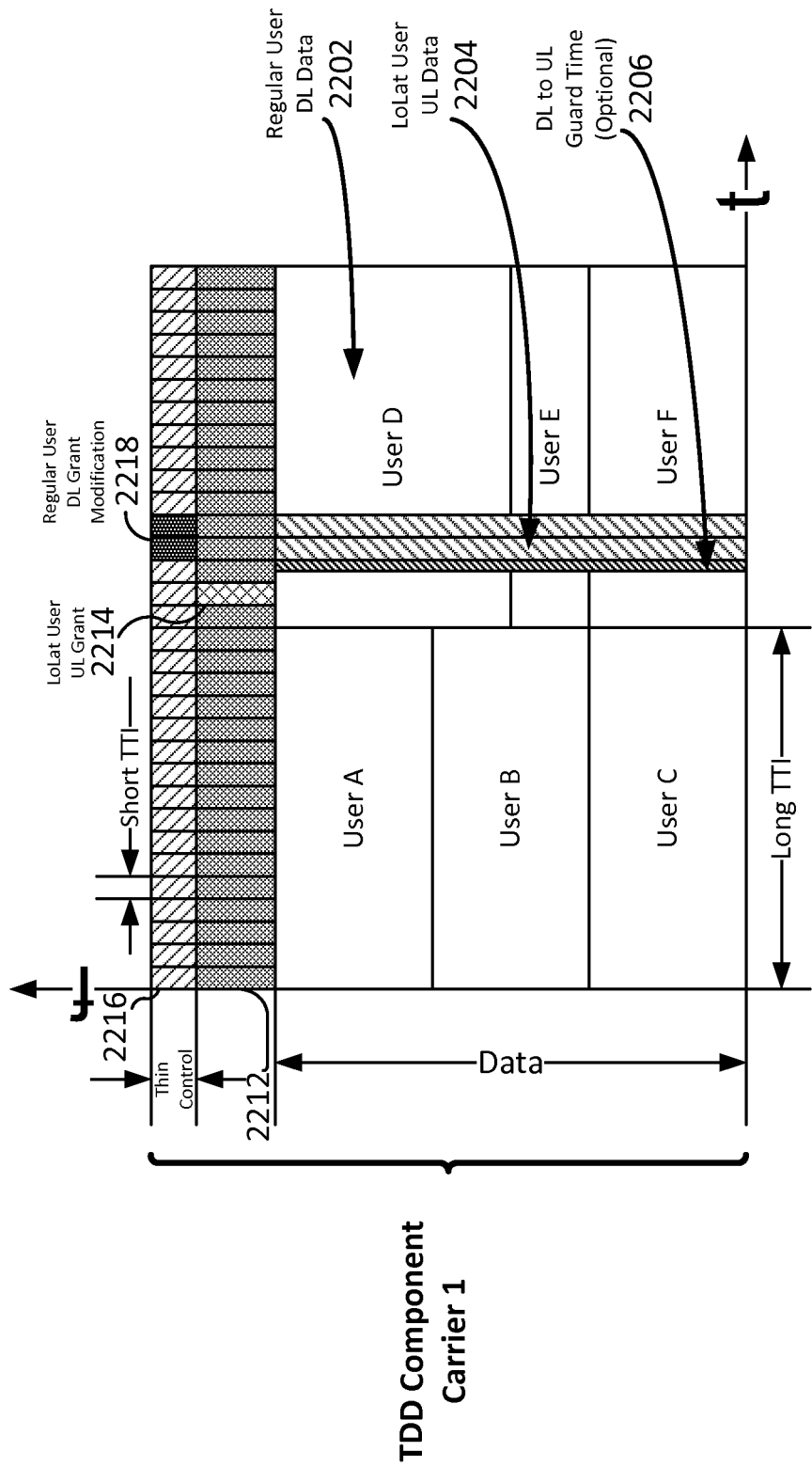


FIG. 22
TDD-TDD Pairing: Multiplexing
LoLat UL on Regular DL

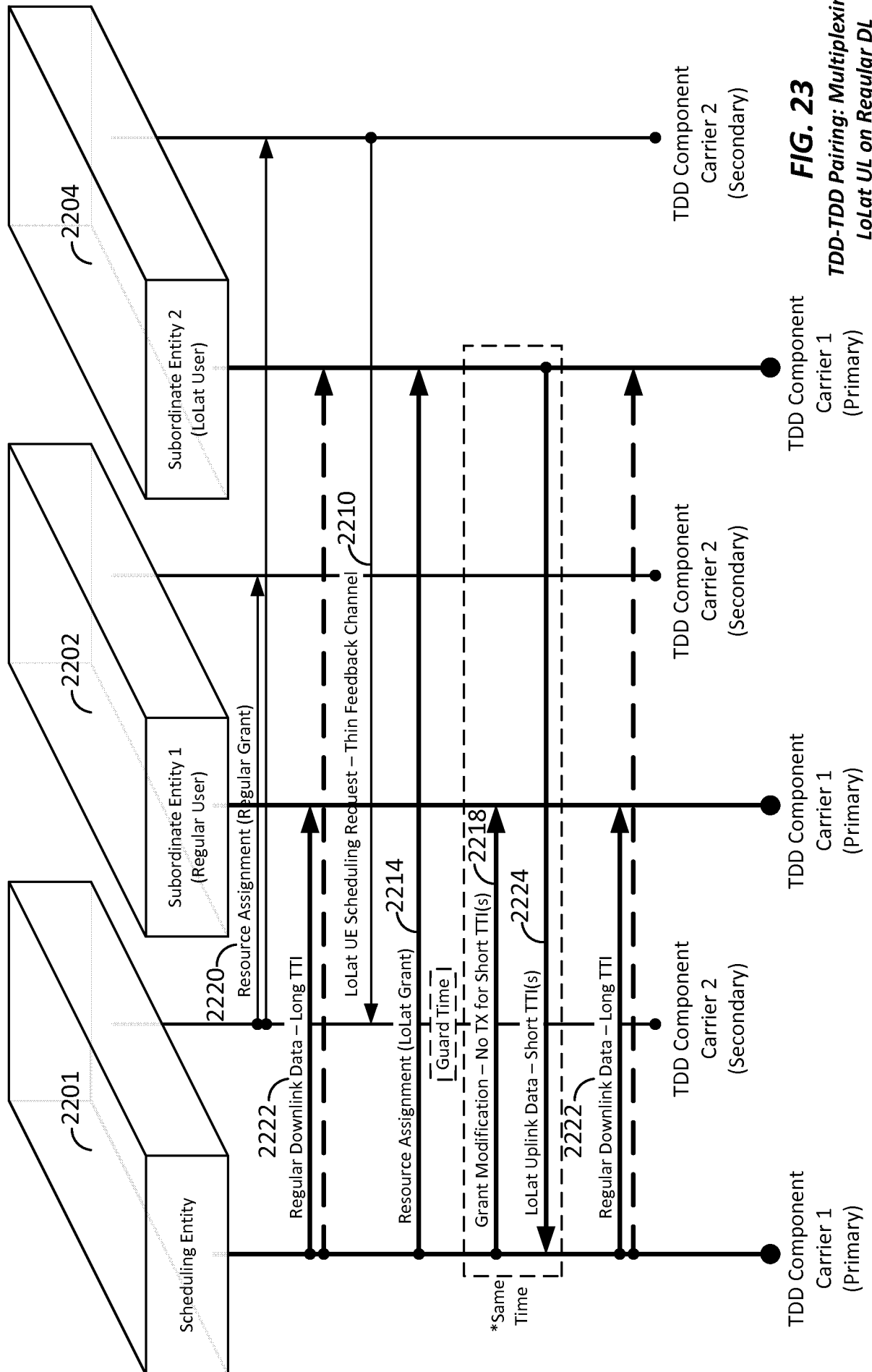
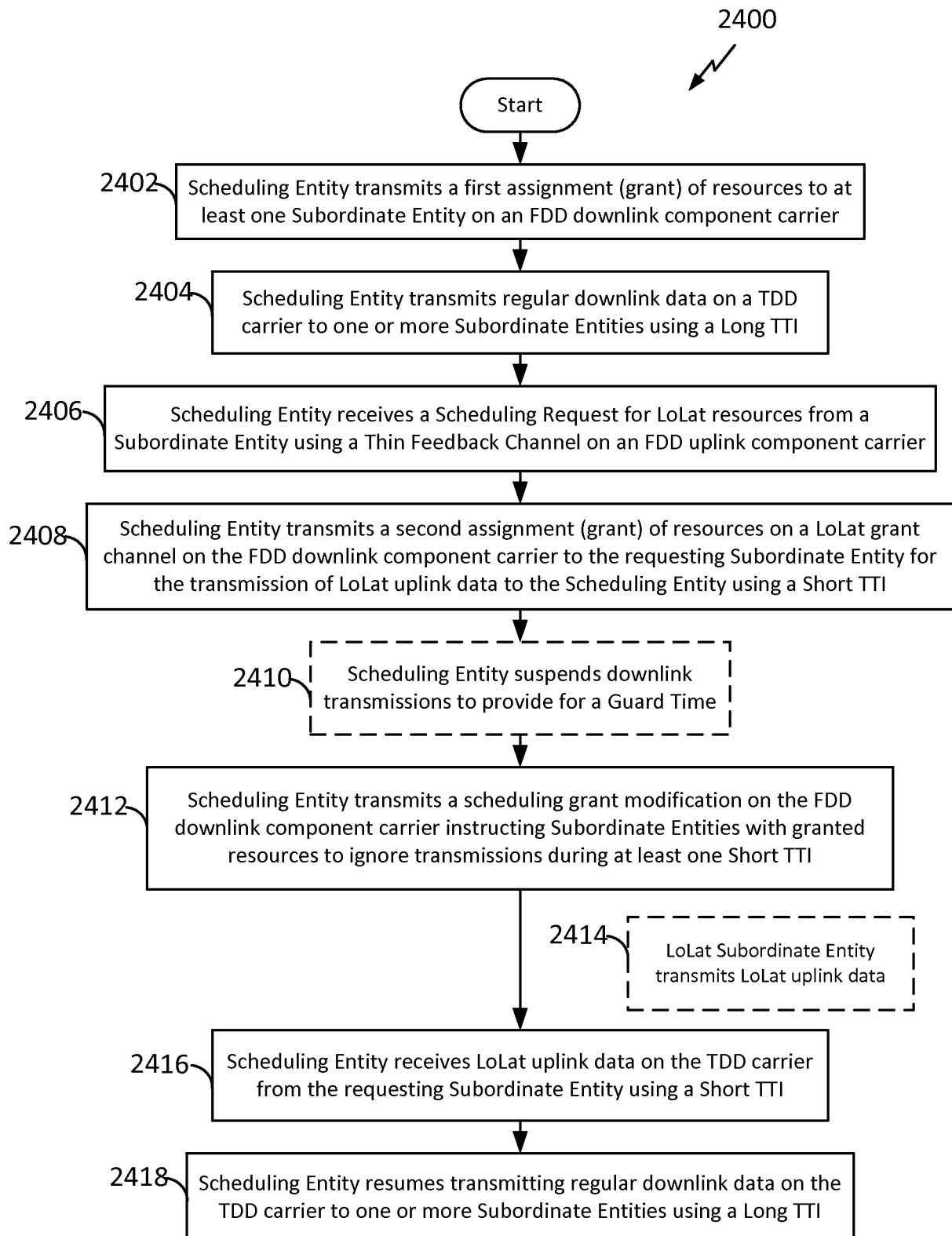
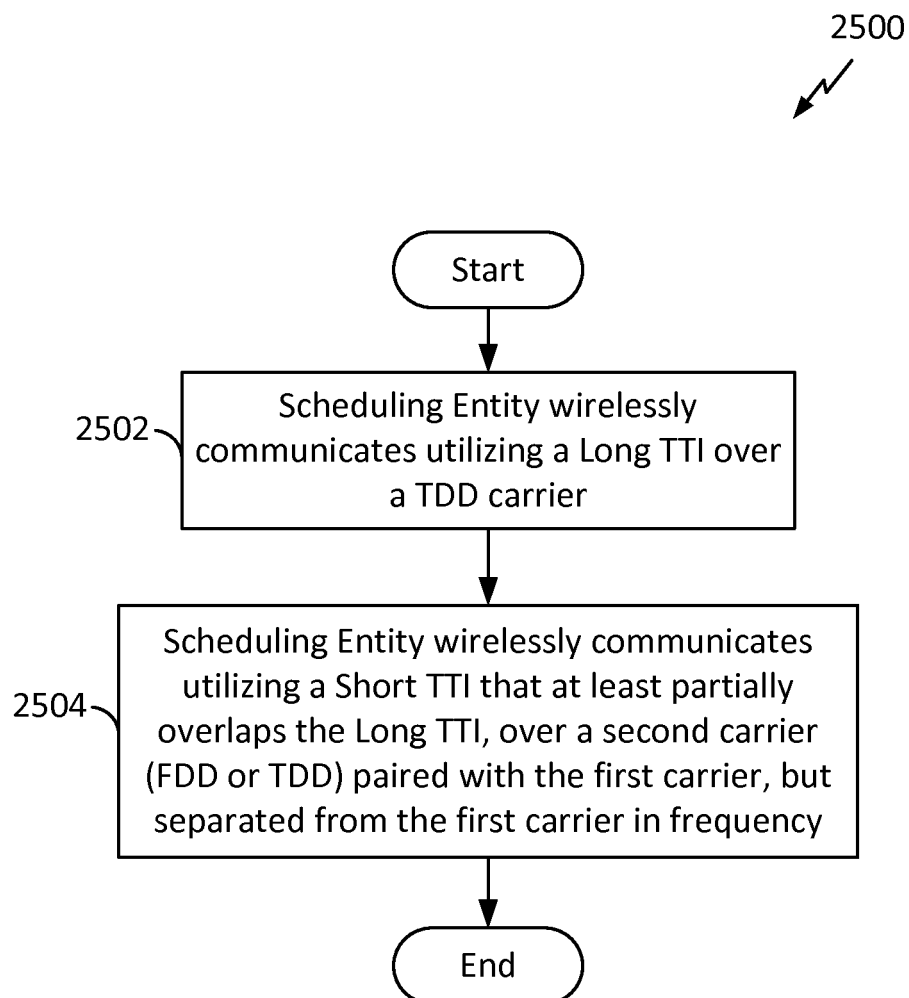


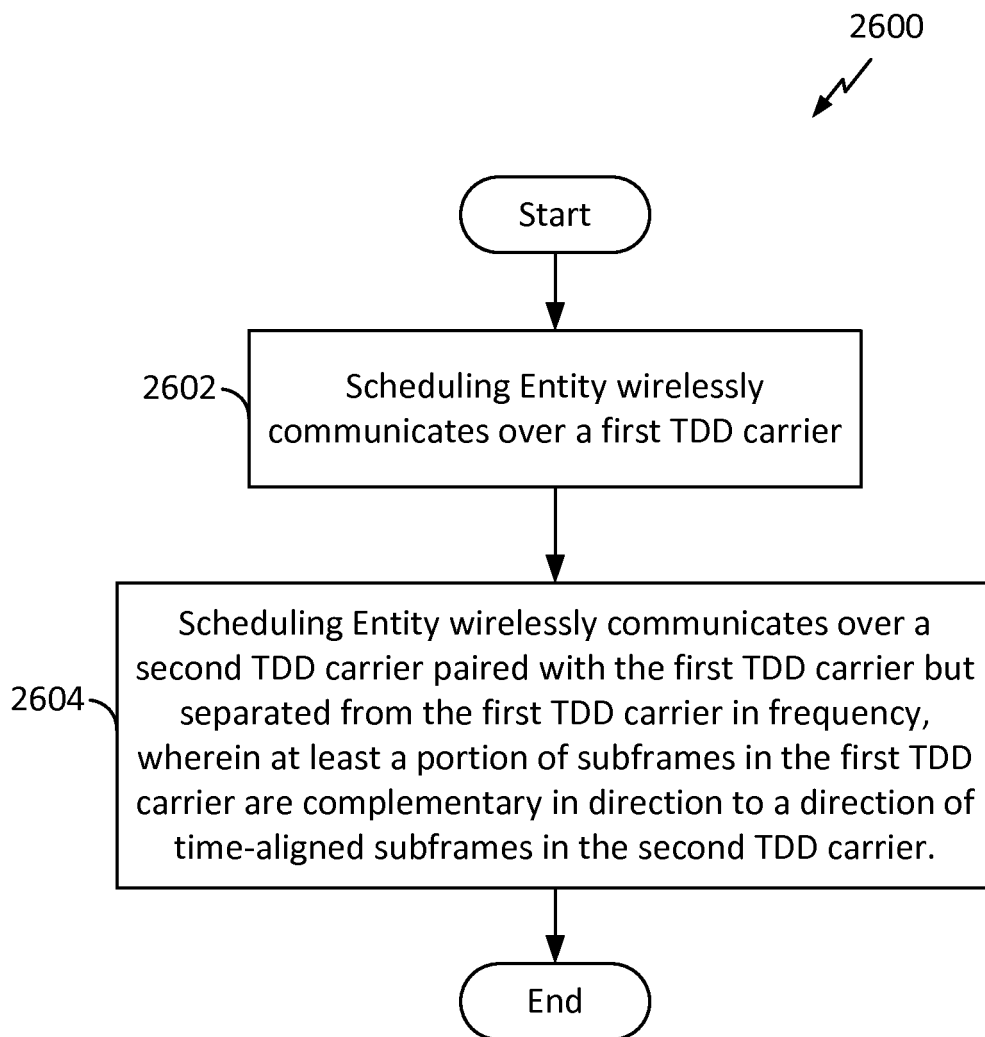
FIG. 23

**TDD-TDD Pairing: Multiplexing
LoLat UL on Regular DL**

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**FIG. 24**

**FIG. 25**

**FIG. 26**