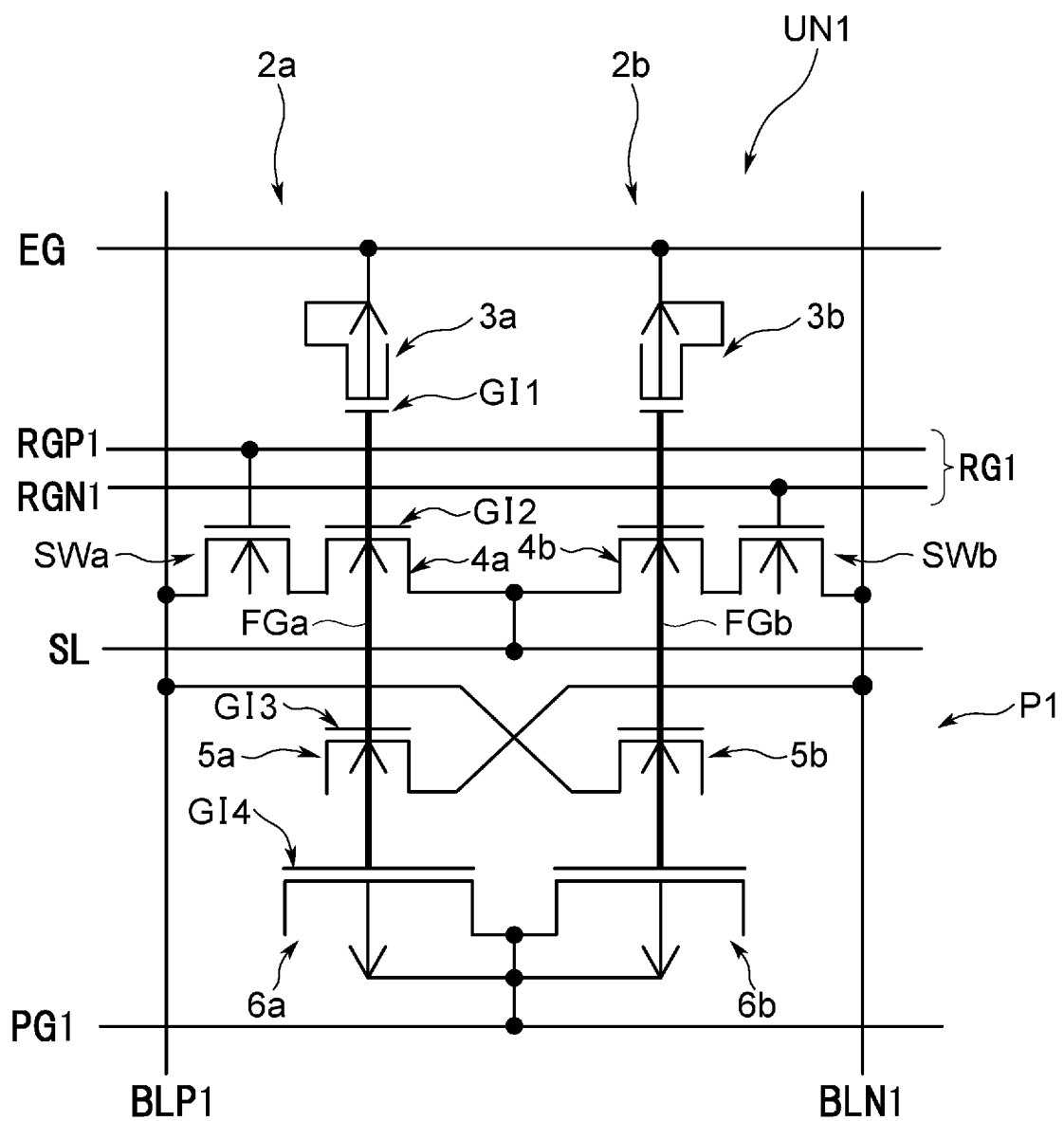
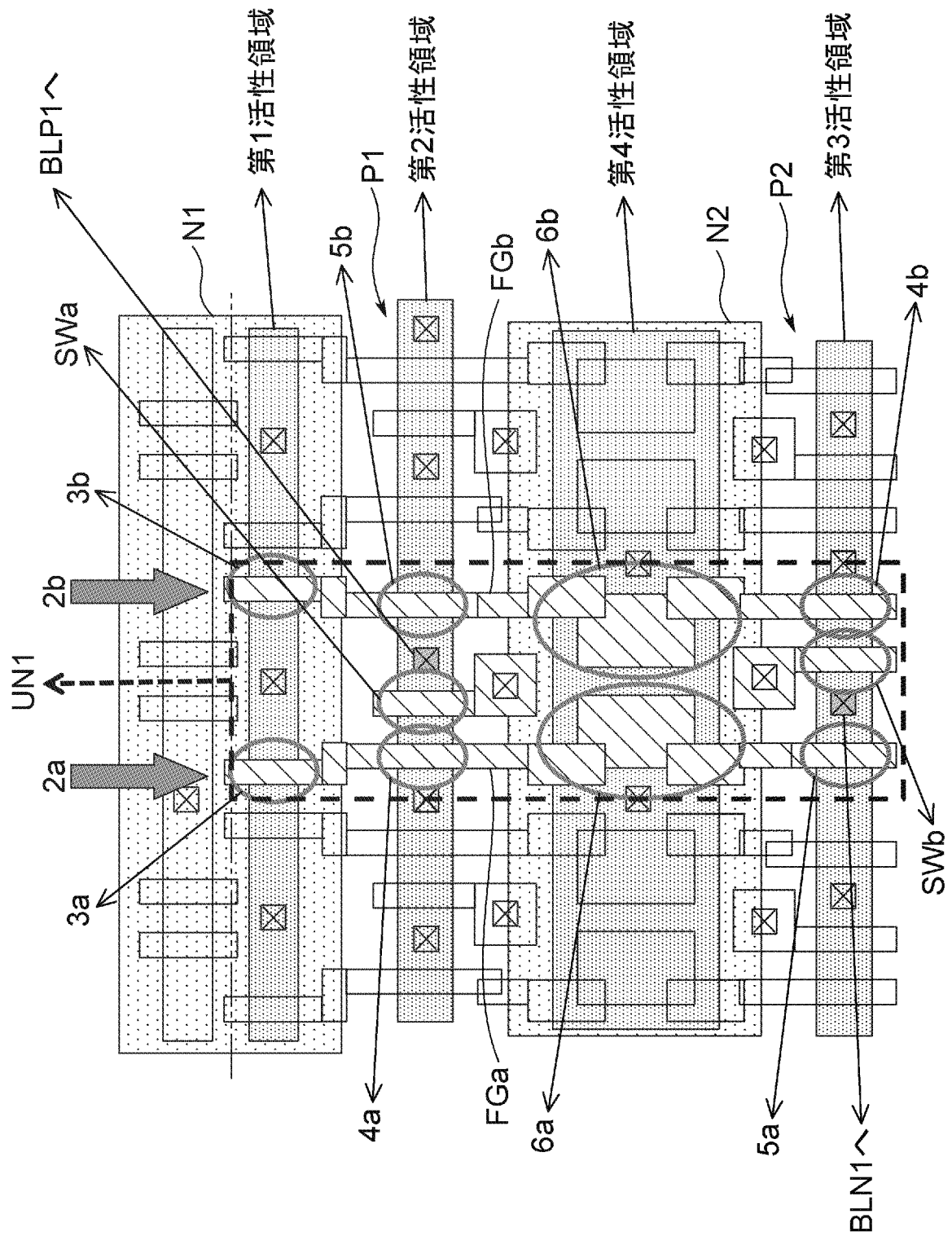
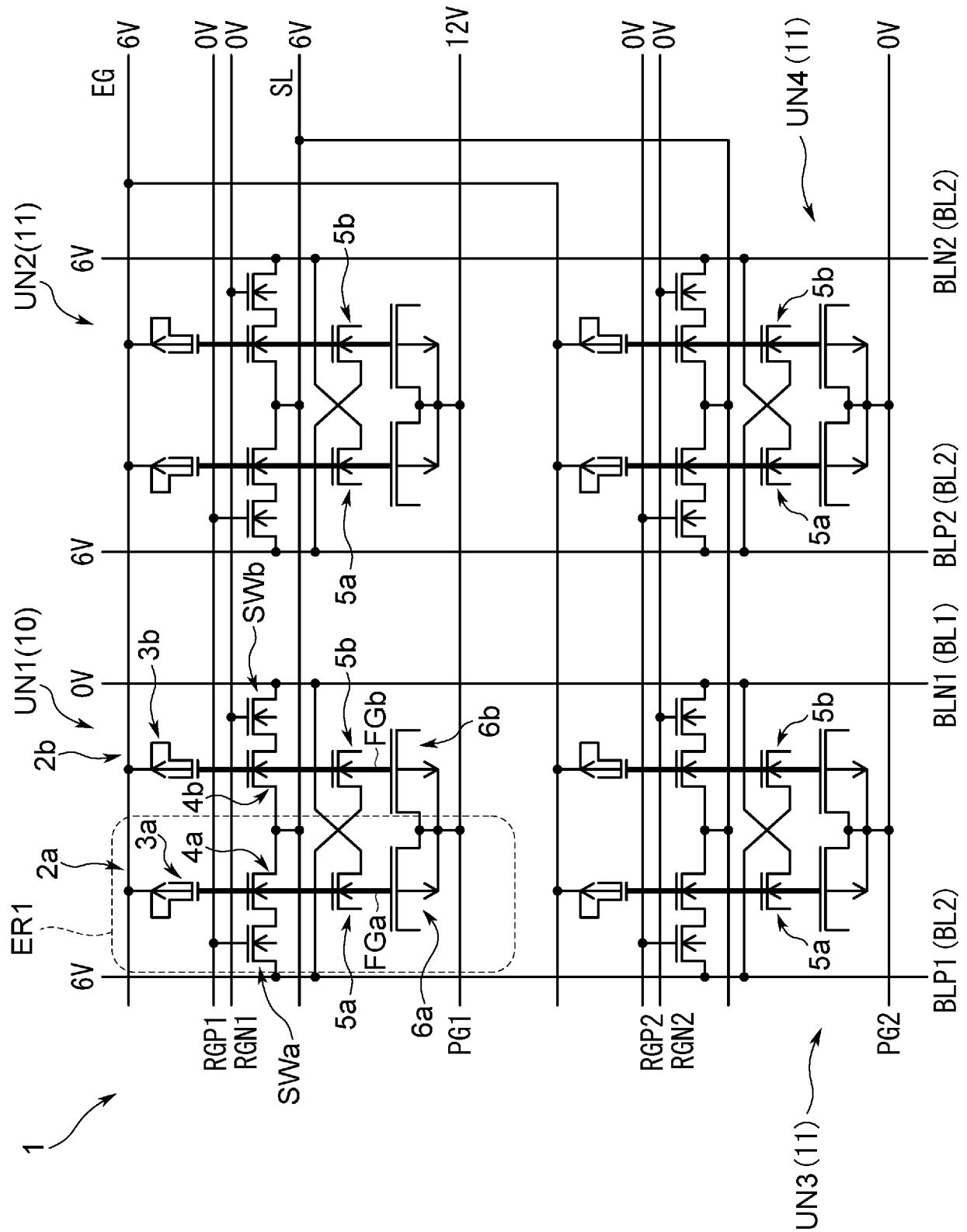




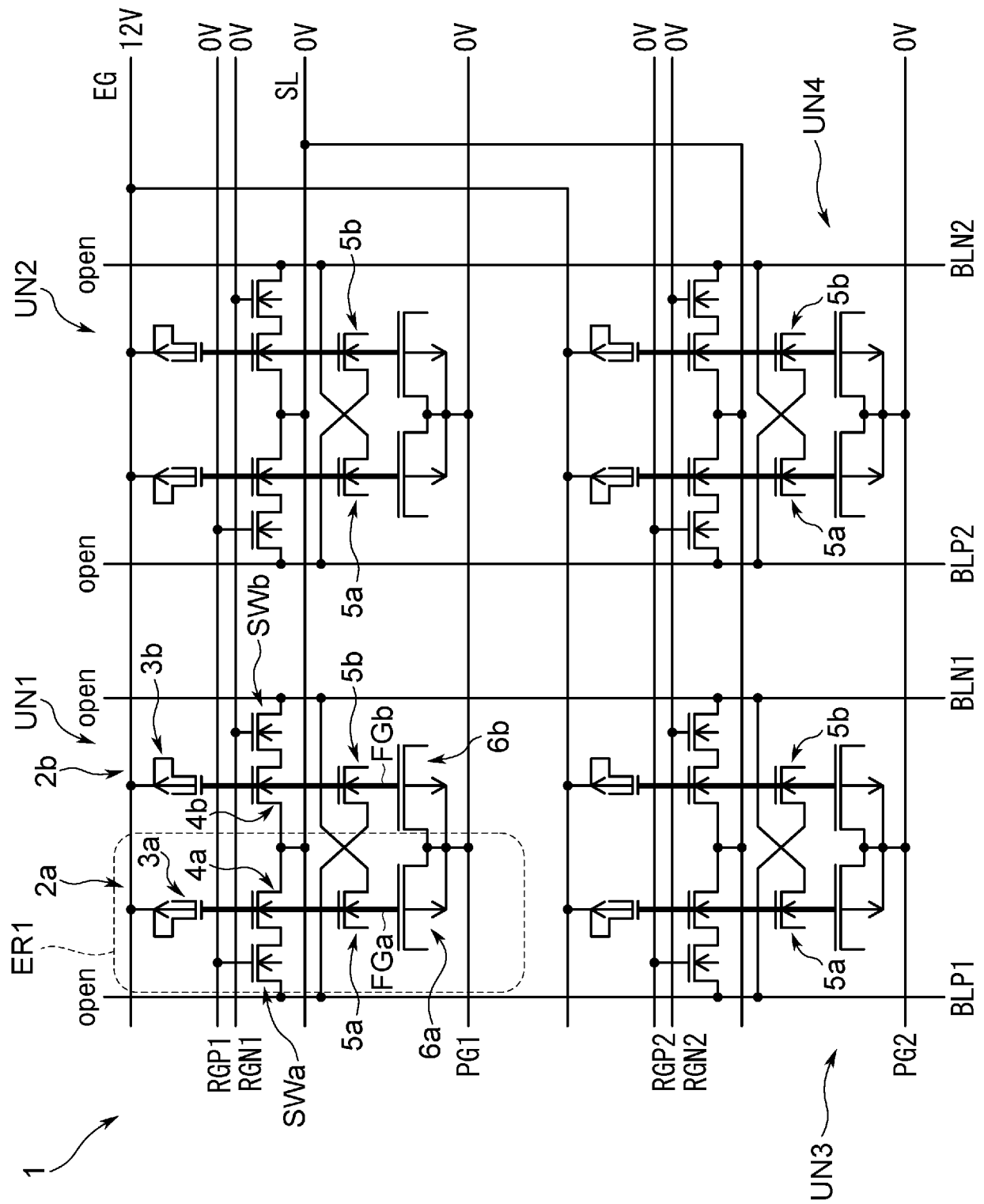
[図2]



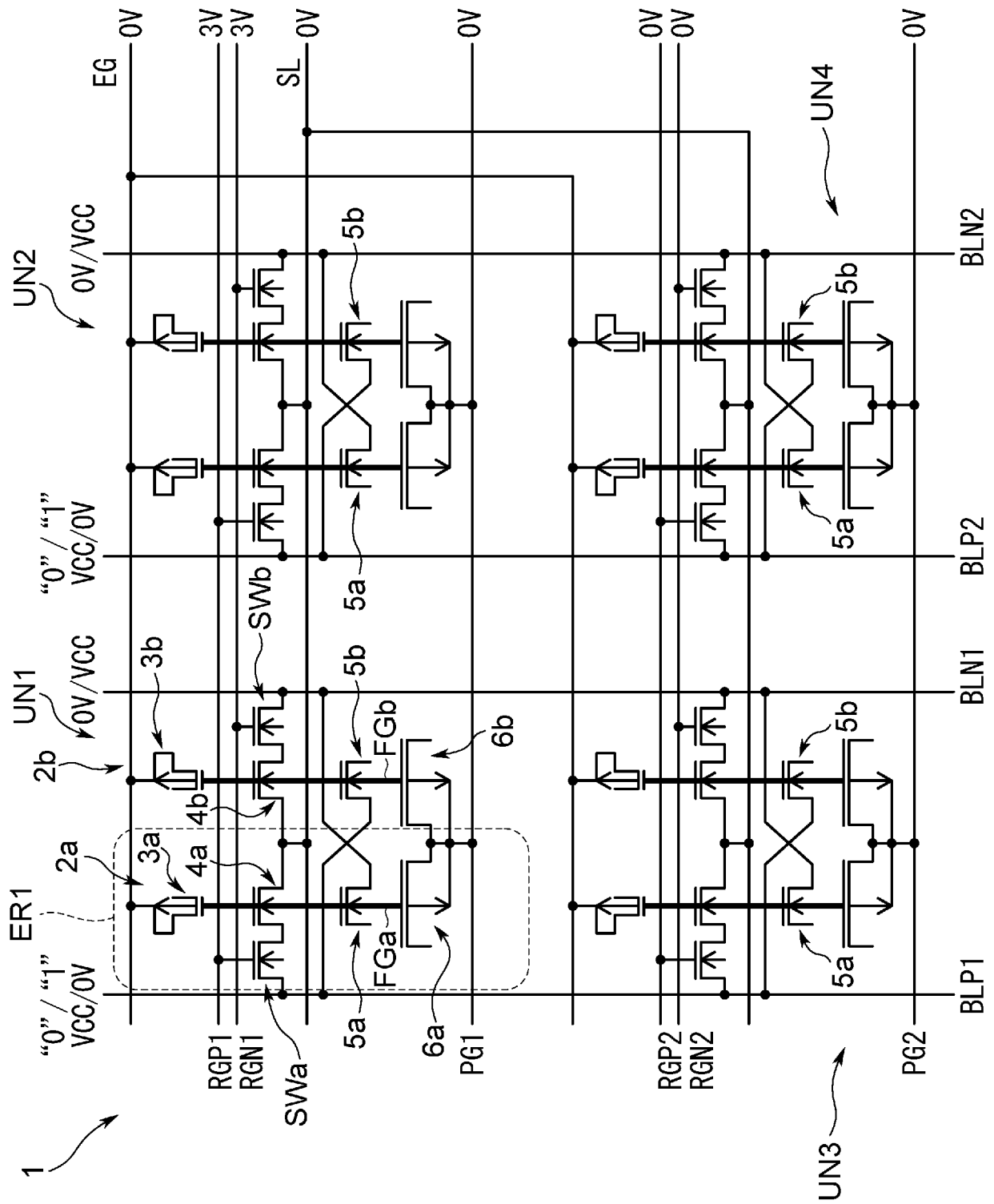
[図3]



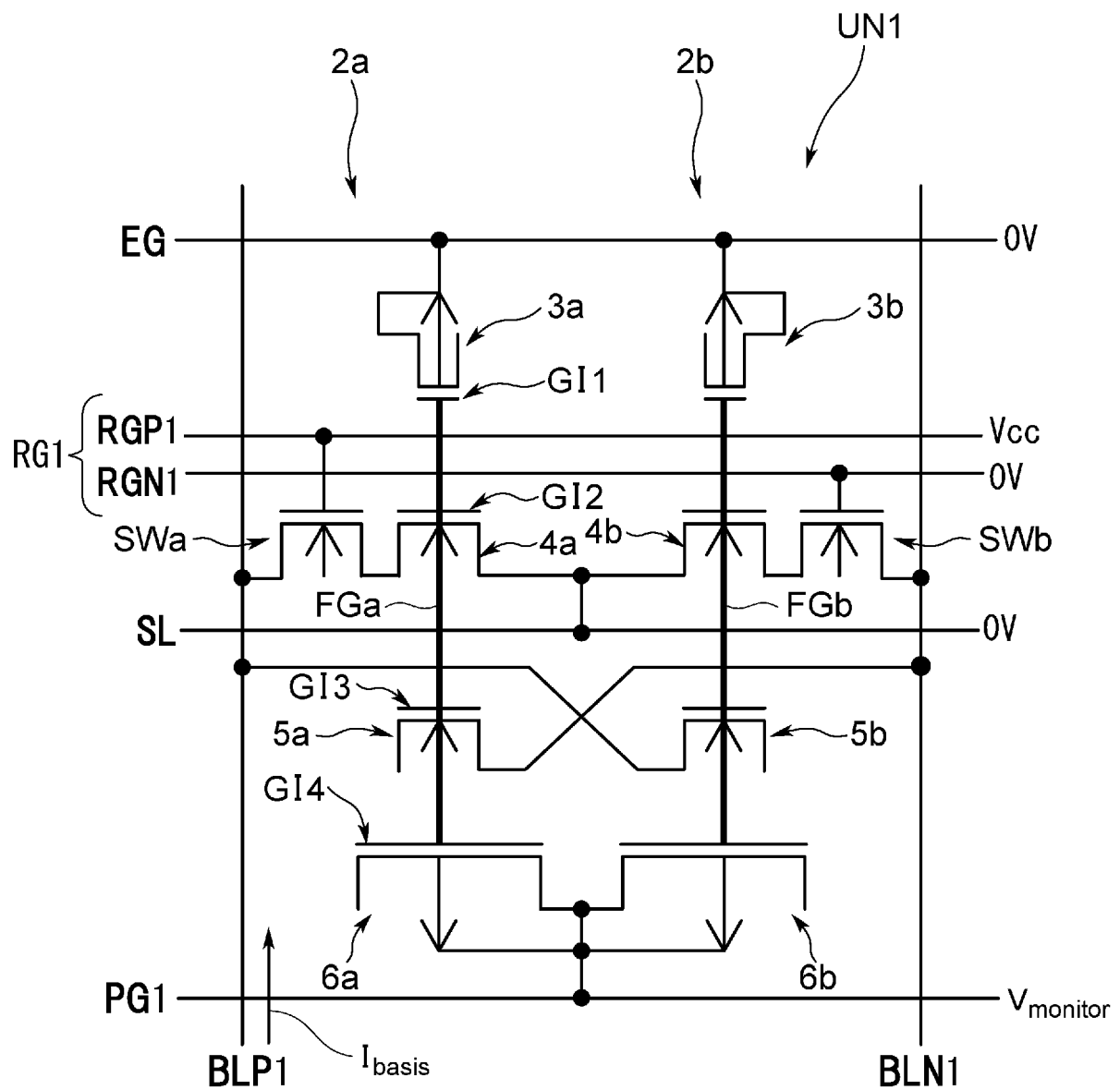
[図4]



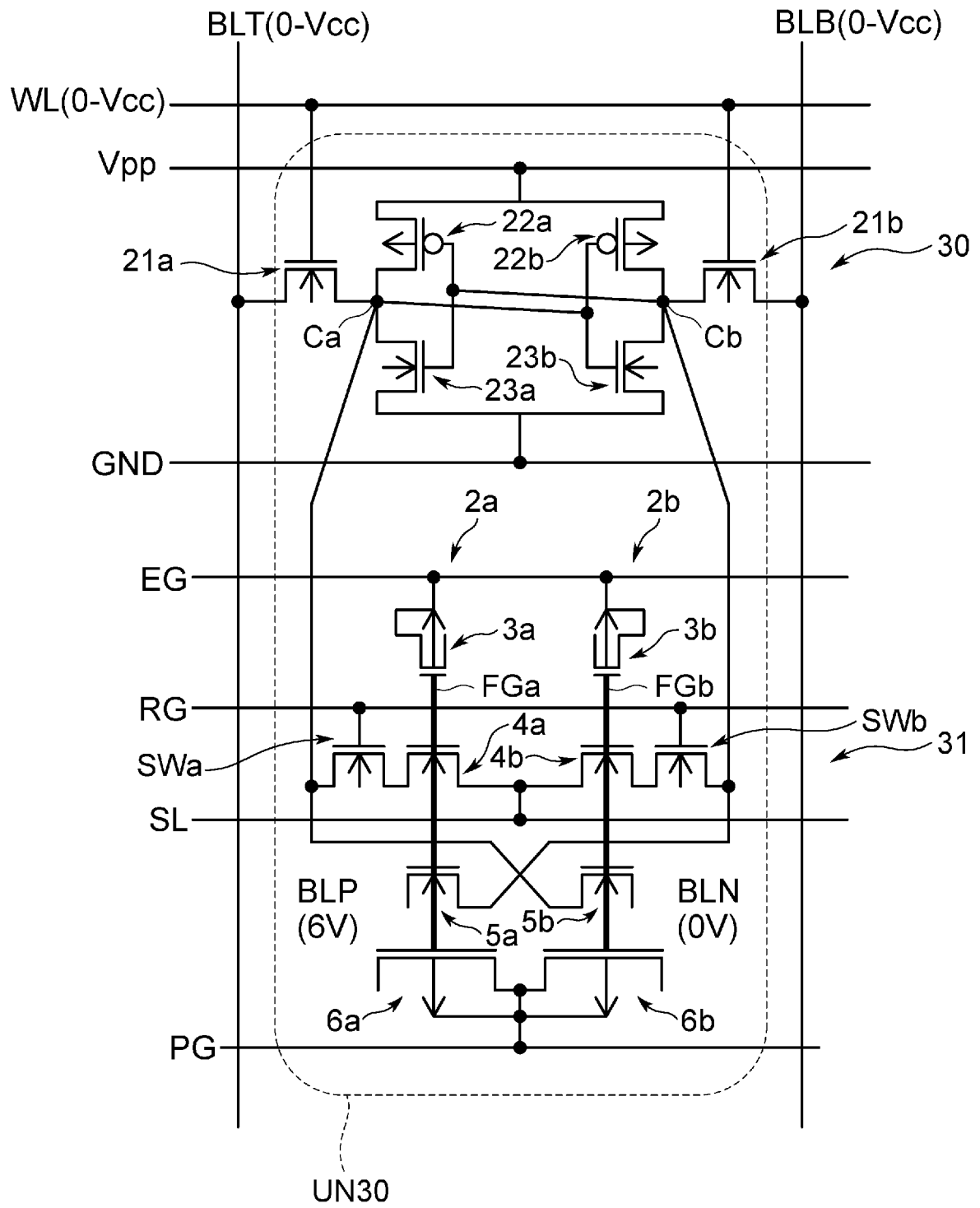
[図5]



[図6]



[図7]



[図8]

	EG	RG	SL	PG	Vpp	WL
プログラム (SRAMから不揮発メモリ部へ)	6	0	6	12	6	0
消去 (不揮発メモリ部でのデータ消去)	12	0	0	0	Vcc	0
データロード (不揮発メモリ部からSRAMへ)	0	Vcc	0	0	Vcc	0
書き込み (SRAMへの書き込み)	Don't care	0	Don't care	Don't care	Vcc	Vcc/0
読み出し (SRAMから読み出し)	Don't care	0	Don't care	Don't care	Vcc	Vcc/0