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- (71) **Applicant:** INTEL CORPORATION [US/US]; 2200 Mission College Boulevard, Santa Clara, California 95054 (US).
- (72) **Inventors:** OULD-AHMED-VALL, Elmoustapha; 5000 West Chandler Boulevard, M/s: Ch7-401, Chandler, Arizona 85226 (US). ESPASA, Roger; Muntaner 447, 4rt 1a, 08021 Barcelona (ES). GUILLEN, David F.; Avda. Catalunya 22B 2n 2a, B, 43002 Tarragona (ES). SANCHEZ, F. Jesus; C./ Sant Tomas, 87-89, 2-1, 08035 Barcelona (ES). SOLE, Guillem; Jerico 27, 3-4, 08035 Barcelona (ES).
- (74) **Agent:** NICHOLSON, David F.; Nicholson De Vos Webster & Elliott, LLP, 217 High Street, Palo Alto, California 94301 (US).

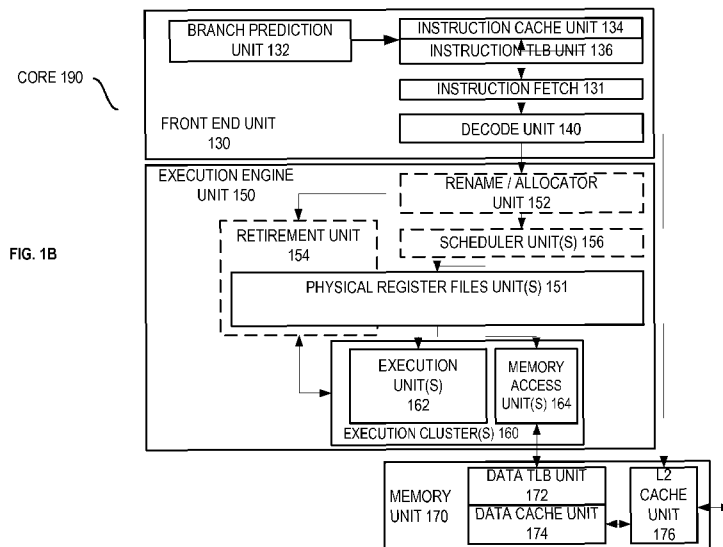
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(54) **Title:** APPARATUS AND METHOD FOR VECTOR BROADCAST AND XORAND LOGICAL INSTRUCTION



(57) **Abstract:** An apparatus and method are described for performing a vector broadcast and XORAND logical instruction. For example, one embodiment of a processor comprises: fetch logic to fetch an instruction from memory indicating a destination packed data operand, a first source packed data operand, a second source packed data operand, and an immediate operand, and execution logic to determine a bit in the second source packed data operand based a position corresponding to the immediate value, perform a bitwise AND between the first source packed data operand and the determined bit to generate an intermediate result, perform a bitwise XOR between the destination packed data operand and the intermediate result to generate a final result, and store the final result in a storage location indicated by the destination packed data operand.