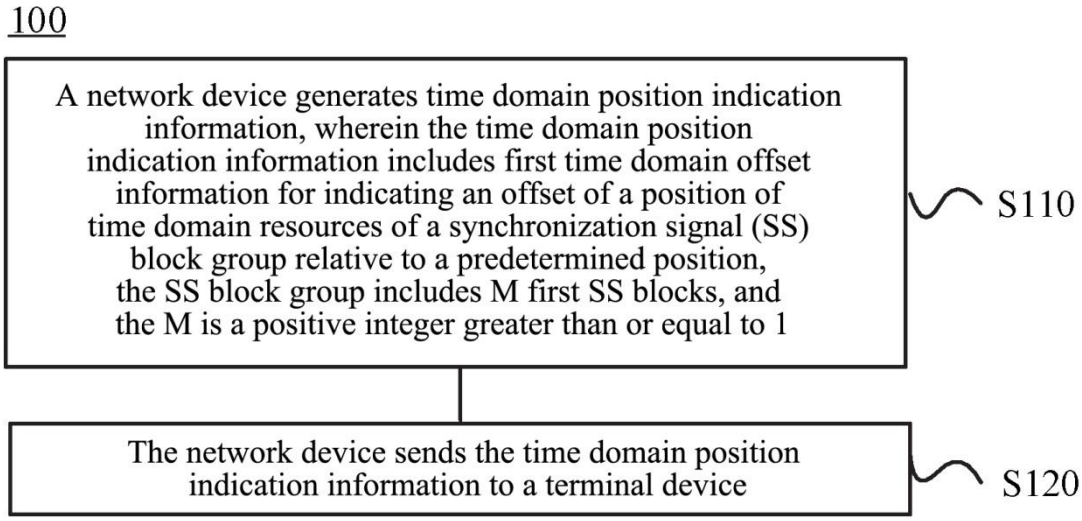

FIG. 1

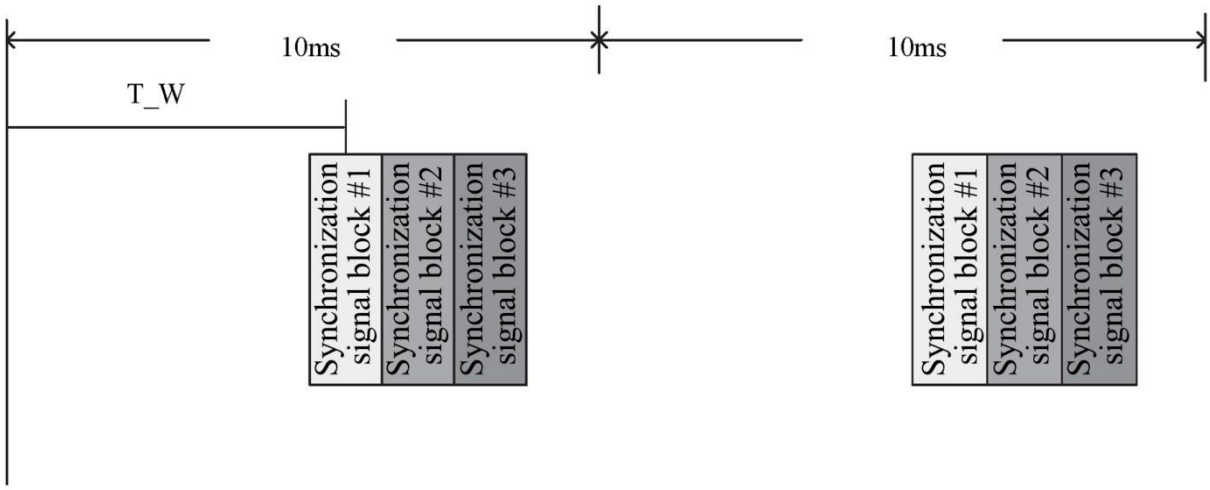

FIG. 2

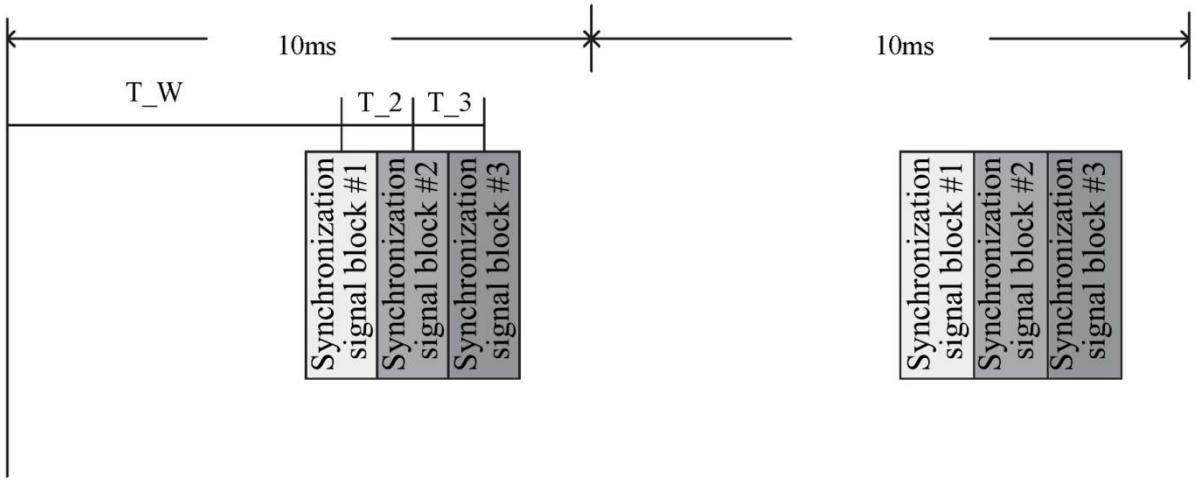

FIG. 3

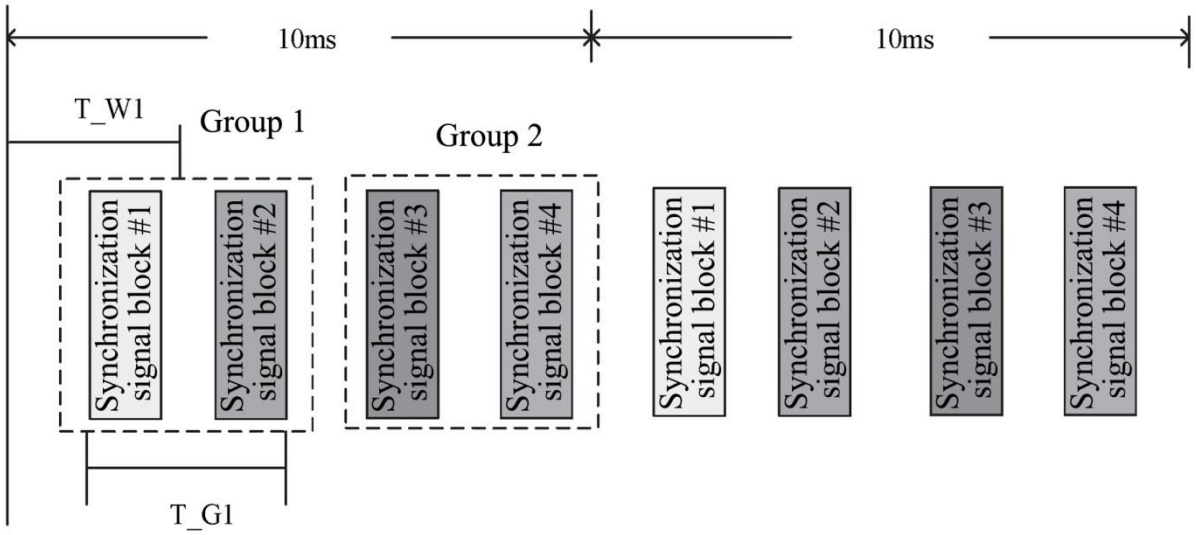
10ms
10ms
T_W1
Group 1
Group 2
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4
T_G1

FIG. 4

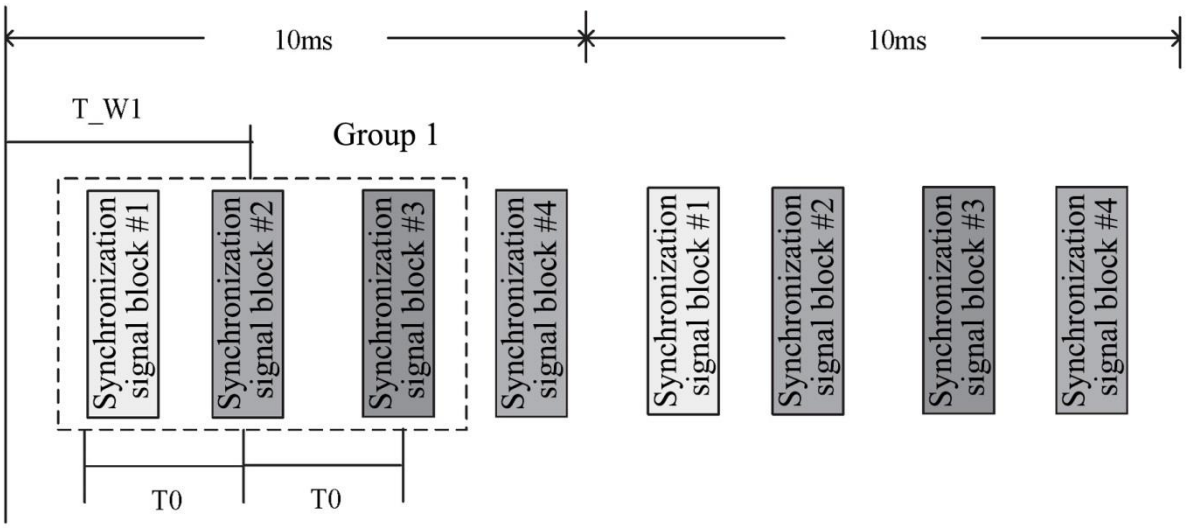
10ms
10ms
T_W1
Group 1
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4
T0
T0

FIG. 5

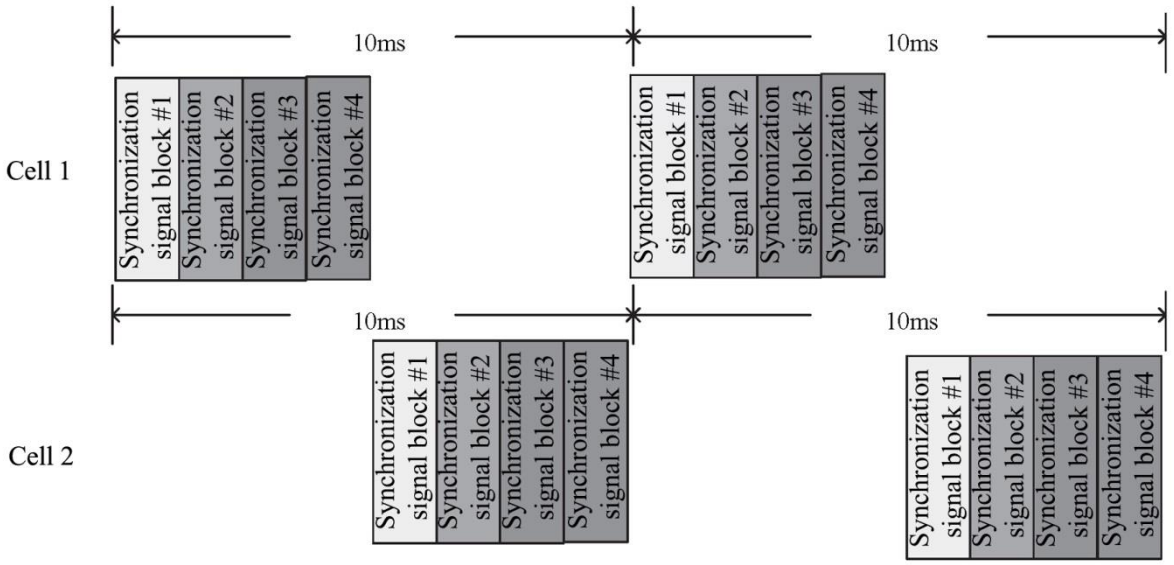
10ms
10ms
Cell 1
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4
10ms
10ms
Cell 2

FIG. 6

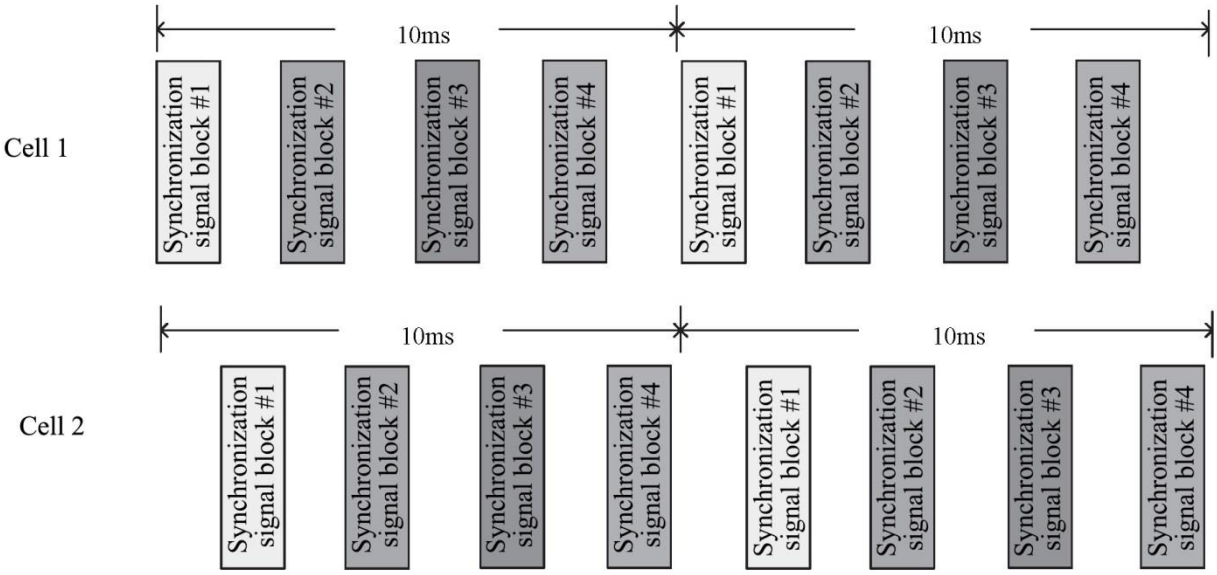
10ms
10ms
Cell 1
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4
10ms
10ms
Cell 2

FIG. 7

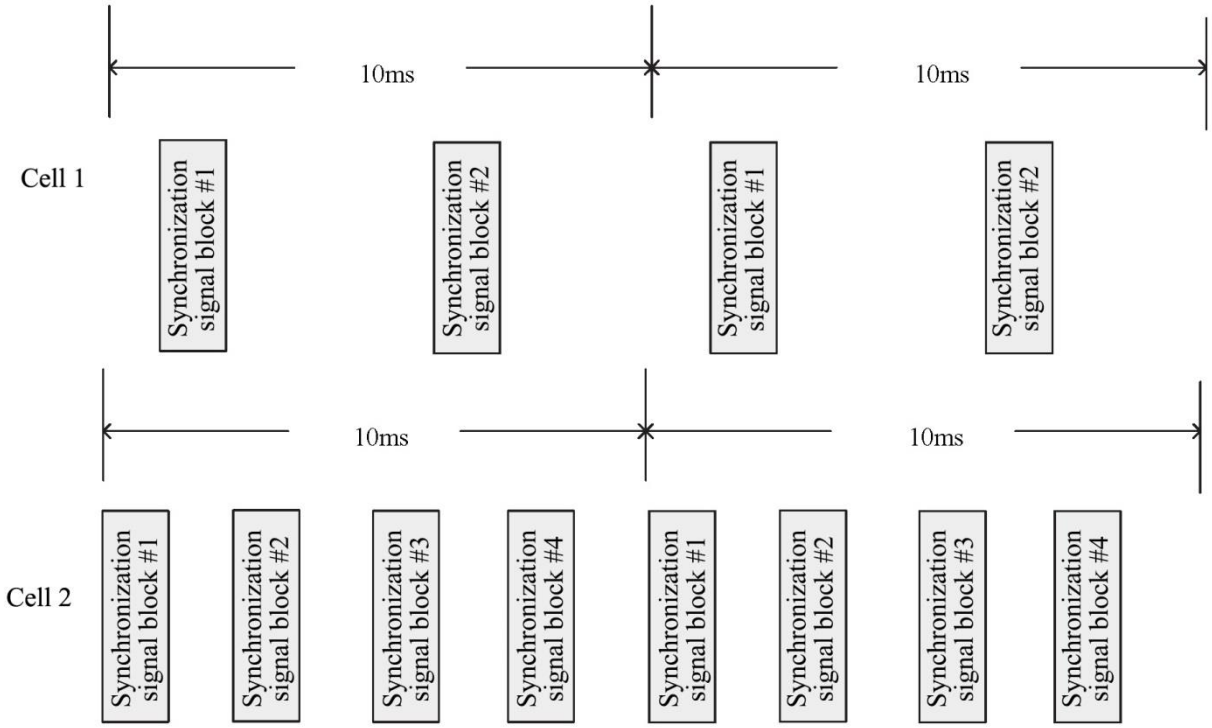
10ms
10ms
Cell 1
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #1
Synchronization signal block #2
10ms
10ms
Cell 2
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #4

FIG. 8

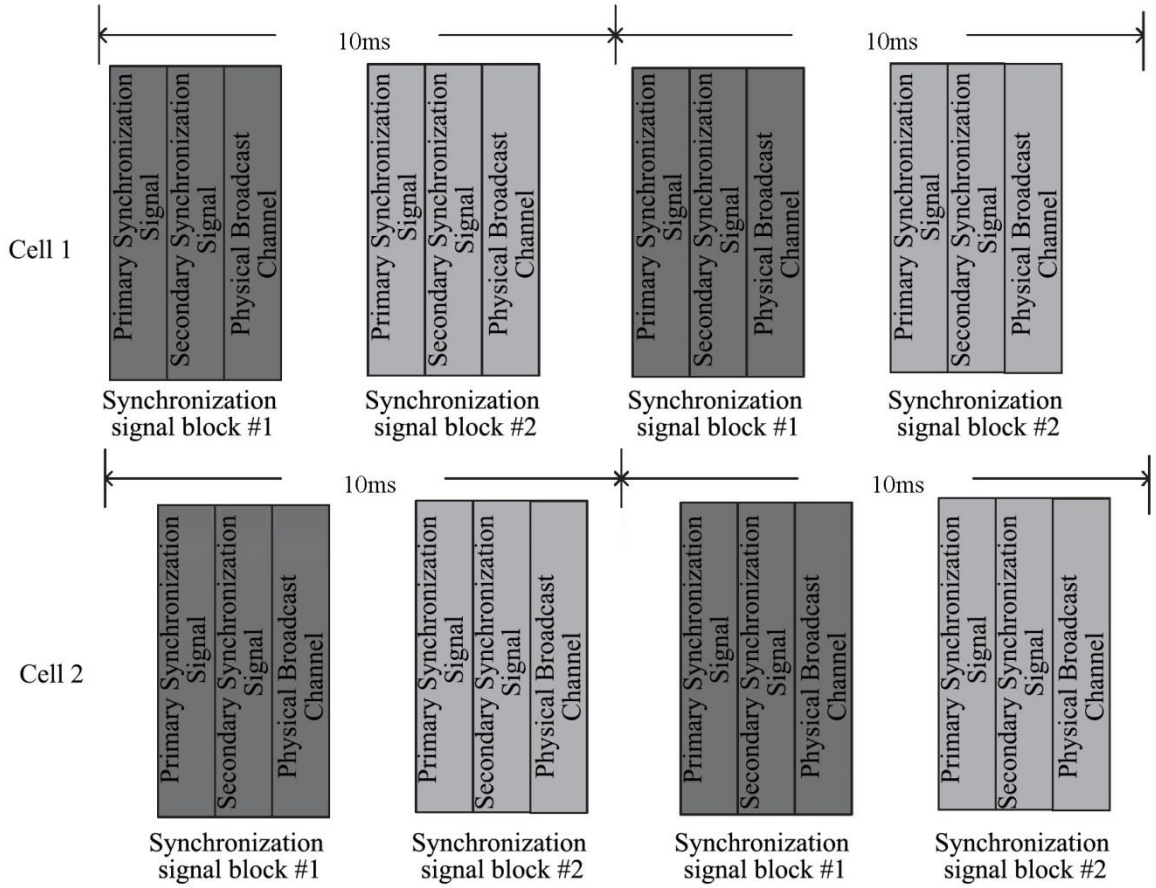
10ms
10ms
Cell 1
Primary Synchronization Signal
Secondary Synchronization Signal
Physical Broadcast Channel
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #1
Synchronization signal block #2
10ms
10ms
Cell 2
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #1
Synchronization signal block #2

FIG. 9

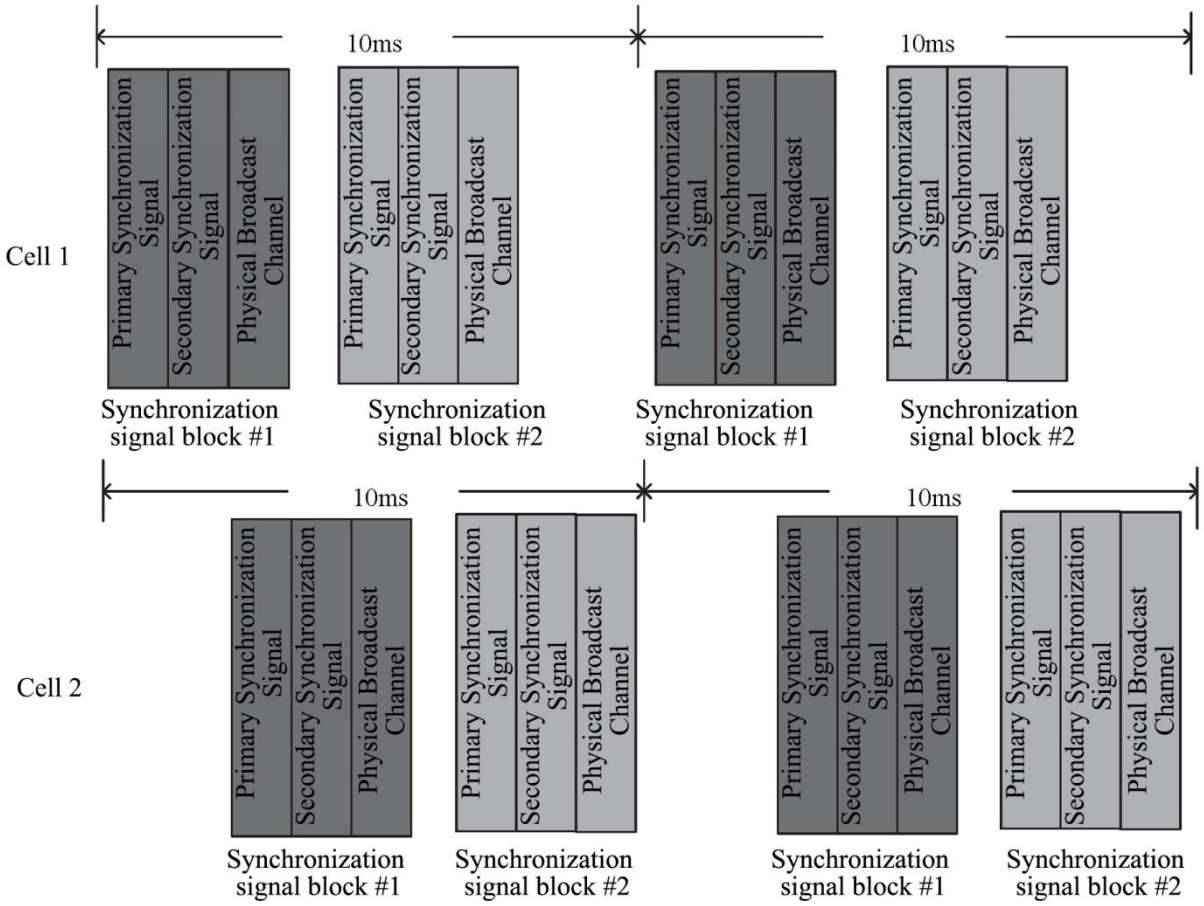
10ms
10ms
Cell 1
Primary Synchronization Signal
Secondary Synchronization Signal
Physical Broadcast Channel
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #1
Synchronization signal block #2
10ms
10ms
Cell 2
Primary Synchronization Signal
Secondary Synchronization Signal
Physical Broadcast Channel
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #1
Synchronization signal block #2

FIG. 10

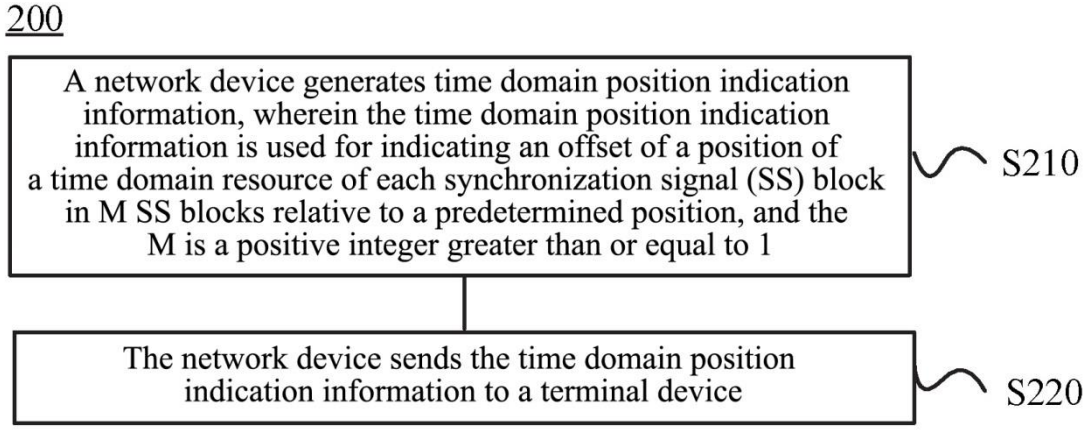
200
A network device generates time domain position indication information, wherein the time domain position indication information is used for indicating an offset of a position of a time domain resource of each synchronization signal (SS) block in M SS blocks relative to a predetermined position, and the M is a positive integer greater than or equal to 1
S210
The network device sends the time domain position indication information to a terminal device
S220

FIG. 11

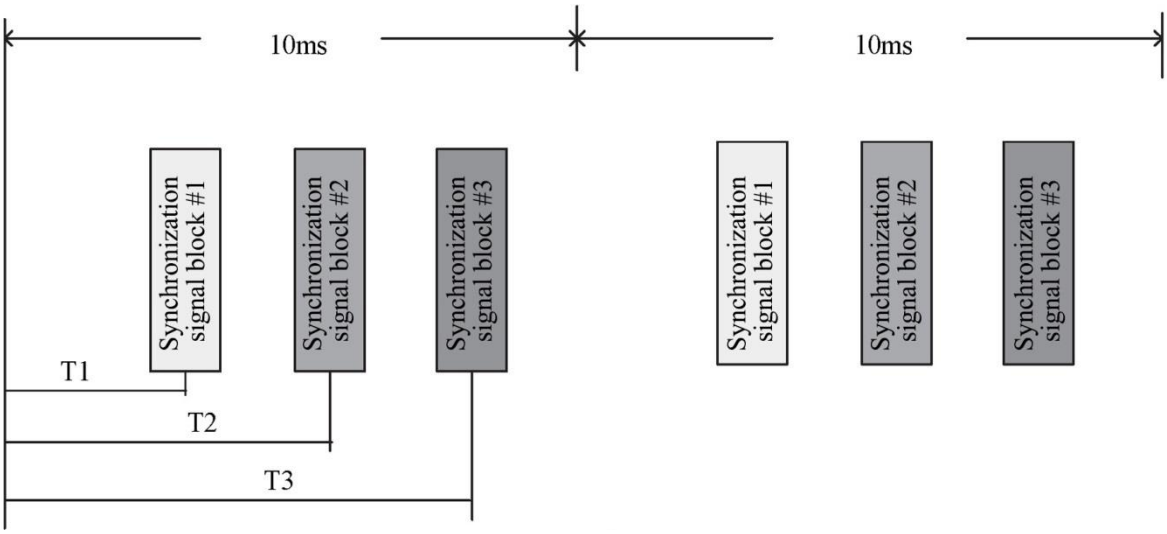
10ms
10ms
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
Synchronization signal block #1
Synchronization signal block #2
Synchronization signal block #3
T1
T2
T3

FIG. 12

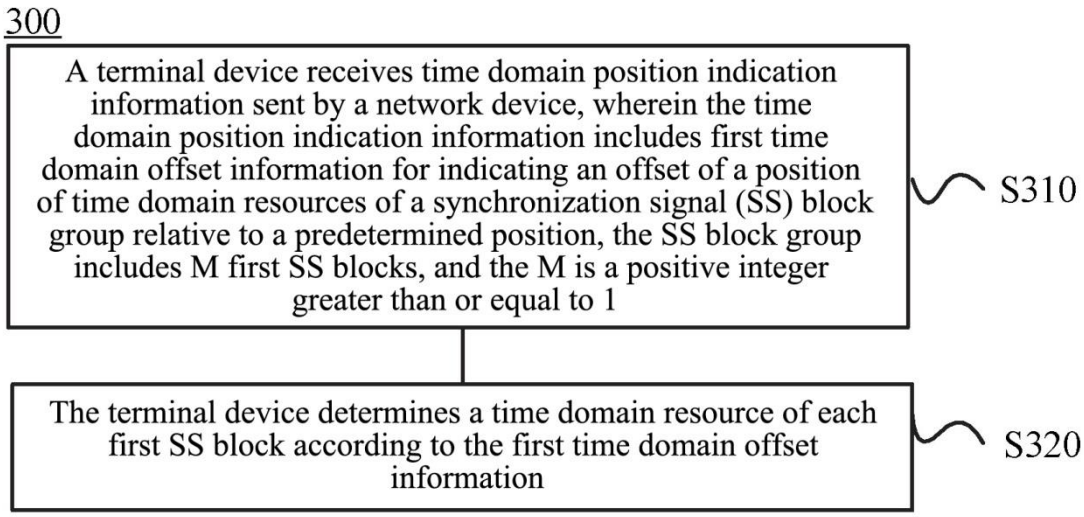
300
A terminal device receives time domain position indication information sent by a network device, wherein the time domain position indication information includes first time domain offset information for indicating an offset of a position of time domain resources of a synchronization signal (SS) block group relative to a predetermined position, the SS block group includes M first SS blocks, and the M is a positive integer greater than or equal to 1
S310
The terminal device determines a time domain resource of each first SS block according to the first time domain offset information
S320

FIG. 13

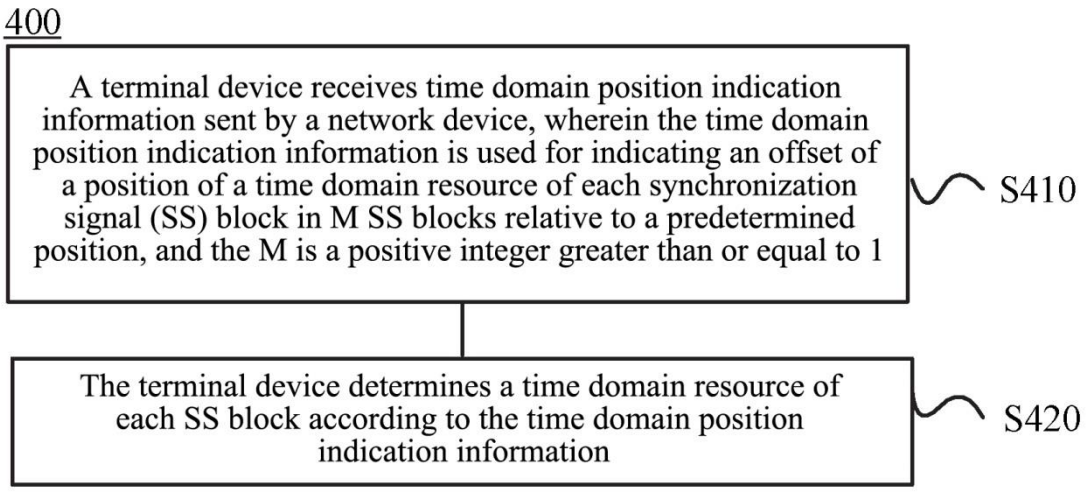

FIG. 14

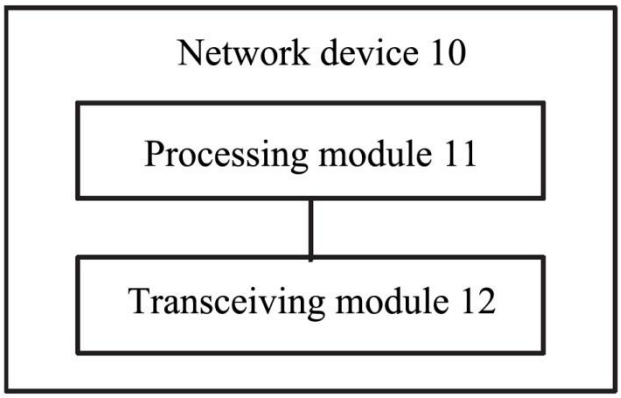

FIG. 15

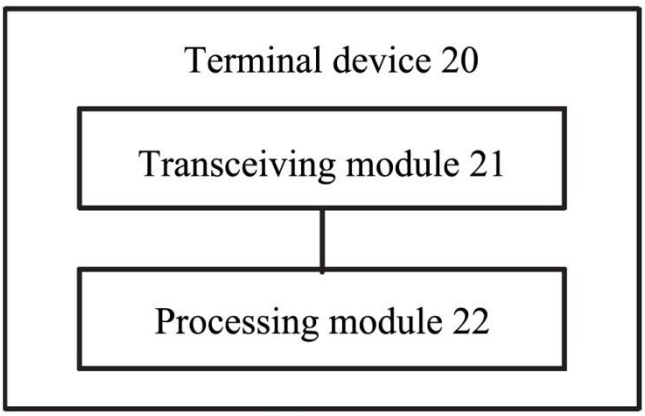

FIG. 16

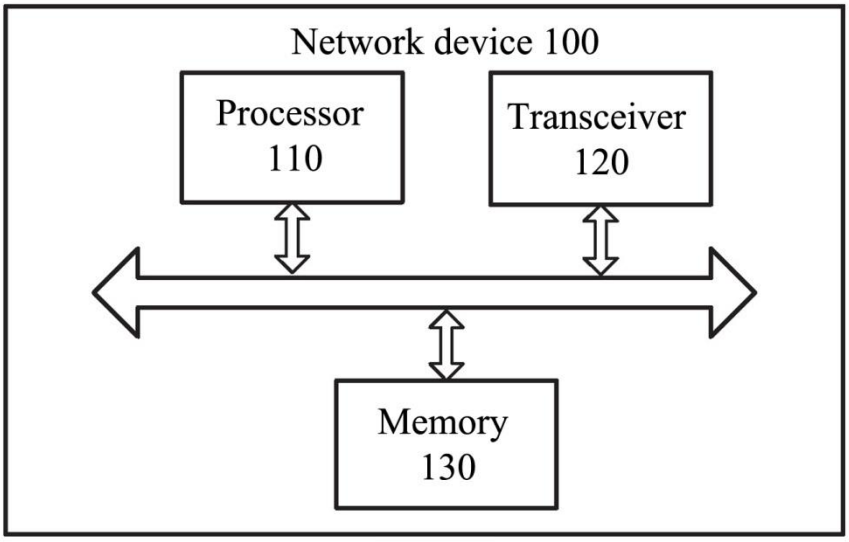
Network device 100
Processor
110
Transceiver
120
Memory
130

FIG. 17

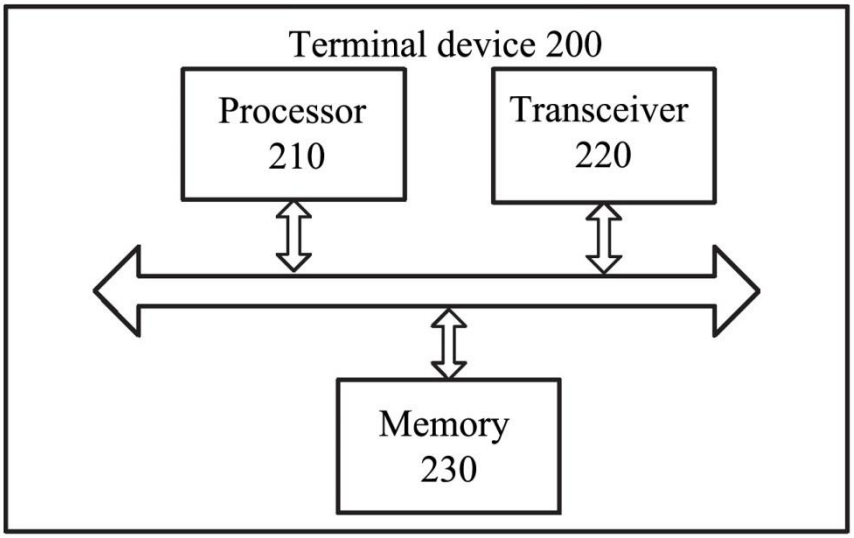
Terminal device 200
Processor
210
Transceiver
220
Memory
230

FIG. 18