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MICROCHIP FUSE

ABSTRACT OF THE DISCLOSURE

A microchip fuse is disclosed which includes a casing constructed from box-like upper and lower members. On the end surfaces of both members, electrode sections are provided which are formed by plating metal onto a sintered conductive paste on the end surfaces. Grooves are provided on the members and filled with insulating material. A fusible element extends through the insulating material and the inner surface as formed when the both members are jointed together. The end portions of the fusible element are soldered to the electrode sections. A hollow portion is provided in the casing adjacent to the inner space by way of a thin wall to damper the pressure of a gas generated during the interruption process.

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