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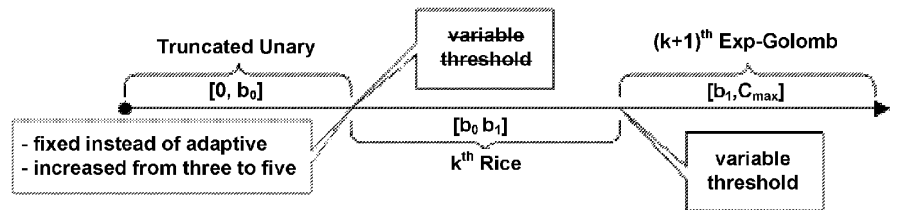
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(54) Title: REGULAR CODED BIN REDUCTION FOR COEFFICIENT CODING USING THRESHOLD AND RICE PARAMETER

FIG. 6



(57) Abstract: A video decoder can be configured to determine a threshold number of regular coded bins for a first decoding pass; for a first set of coefficients, context decode syntax elements of a coefficient group until the threshold number of regular coded bins is reached; and in response to reaching the threshold number of regular coded bins, for a second set of coefficients, bypass decode additional syntax elements, wherein to bypass decode the additional syntax elements the video decoder, for a coefficient of the second set of coefficients, derives a value for a Rice parameter based on a sum of absolute values of neighboring coefficients to the coefficient; determines values for the first set of coefficients of the transform unit based on the context decoded syntax elements; and determines values for the second set of coefficients of the transform unit based on the additional syntax elements.



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