Abstract

Arrangement with master and slave units

The invention relates to an arrangement with at least one master unit, which is connected via a bus to slave units each having a memory, the master unit executing read and/or write accesses to the said memory. Suitable means enable the master unit to initiate a data access uniformly to each of the slave units in accordance with a predetermined communications protocol. Furthermore a slave unit is proposed which is suitable for a communication with a master unit according to this protocol.

The invention is employed in automation devices.

FIG 4