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(71) Applicants: NODAL INDUSTRIES INCORPORATED [-/US]; 25151 Iroquois Court, Barrington, IL 60010 (US). EMERALD, Robert, L. [US/US]; 25151 Iroquois Court, Barrington, IL 60010 (US).

(74) Agent: COSTA, John, G.; P.O. Box 948, Highland, NY 12528 (US).

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(54) Title: COMPUTER

(57) Abstract

The invention relates to computer science, in particular, to a computer system comprising a processor, an input–output switch, an instruction loading switch, instruction memory, and a data access unit which uses the dataflow principle of computation. Performance is increased by decreasing the volume of associative memory by means of the introduction of the use of a fragment routine processor to process segments of the program which are better processed by von Neumann principles of computation.

