(PCT1187)

(Translation)

Silicon wafer and Process for producing the same

[0001] Embodiments of the present invention relate to the technical field of semiconductor wafer production, particularly a silicon wafer wherein the occurrence of both slip dislocations and warpages during a device production process can be suppressed and the process for producing the same.

[0002] A silicon wafer used as a substrate for a semiconductor device is produced by slicing a silicon single crystal ingot and subjecting the sliced ingot to a heat treatment or a mirror finishing. As process for producing such a silicon single crystal ingot, the Czochralski method (CZ method) is exemplified. The CZ method accounts for a major part of processes for producing a silicon single crystal ingot since it makes it possible to easily obtain a single crystal ingot with a large diameter or to control defects relatively easily.

[0003] A silicon single crystal pulled by the CZ method (hereinafter, "CZ-Si") includes crystal defects called "grown-in defects". The CZ-Si takes oxygen between lattices in a supersaturated state, and such supersaturated oxygen causes the induction of fine defects called Bulk Micro Defect (hereinafter, "BMD") in following heat treatments (anneal).

[0004] The formation of a semiconductor device on a silicon wafer requires no crystal defects in a semiconductor device formation region. This is because, if crystal defects are present on a surface where a circuit is formed, a circuit breakage is caused by the defects. Meanwhile, a silicon wafer is required to include a proper amount of BMDs inside, since BMDs serve to getter metal impurities, which cause semiconductor device malfunction.

[0005] To fulfill the above requirements, a process whereby an Intrinsic Gettering layer (hereinafter, "IG layer") is formed by inducing BMDs inside a silicon wafer by annealing the silicon wafer at a high temperature, while grown-in defects present on the surface of the silicon wafer are extinguished to form a Denuded Zone (hereinafter, "DZ layer") hardly including crystal defects, is employed.

[0006] As a concrete example, a process whereby grown-in defects on a surface are reduced by annealing a nitrogen-added substrate at a high temperature, while BMDs containing nitrogen as nuclei are formed inside the substrate, has been proposed (Reference 1).

[0007] However, oxygen concentrations in DZ layers formed on the front and back faces of a silicon wafer by the above-mentioned high-temperature anneal process are extremely low because of the external diffusion of oxygen during heat treatment. As a result, the restraint of dislocation defect extension on the front and back

faces of a wafer is considerably reduced, and dislocation defects (hereinafter, "slips") easily extend from fine flaws having been introduced during the anneal process into a bulk on the front and back faces of the wafer, resulting in a problem that strength of the silicon wafer is reduced because of said slip dislocation extension. For example, when a silicon wafer is annealed in a state supported by heat treatment ports, slip dislocations often extend from the supported portions around the back face of the wafer. In addition, slip dislocations sometimes extend from the edge of a silicon wafer.

[0008] When the strength of a silicon wafer is reduced, the wafer could be damaged or broken during a production process. However, DZ layers are inevitable for semiconductor device formation, and a silicon wafer having DZ layers and excelling in strength properties has been desired.

[0009] In a prior art disclosed in the above Reference 1, reduction of silicon wafer strength was not considered, and in a silicon wafer produced thereby the extension of slip dislocation could not be avoided.

[0010] Meanwhile, in order to prevent the occurrence of such slip dislocation, a process whereby BMDs are generated in a high density has been proposed.

[0011] Specifically, a silicon wafer production process wherein

a substrate cut out of a silicon single crystal ingot is subjected to a rapid temperature increasing/decreasing heat treatment in an atmosphere of nitrogen gas, inert gas or a gas mixture of ammonia gas and inert gas at a temperature of 500 °C to 1,200 °C for 1 to 600 minutes to form oxygen precipitation nuclei with sizes of 20nm or less in an density of 1×10^{10} atoms/cm^3 or more in a BMD layer, has been proposed (Reference 2). A silicon wafer wherein BMDs are generated in a high concentration (1×10^{10} /cm^3 to 1×10^{12} /cm^3) by repeating heat treatment steps several times, has also been proposed (Reference 3).

Reference 1: JP Patent Appln. Disclosure No. 10-98047

Reference 2: JP Patent Appln. Disclosure No. 2006-40980

Reference 3: JP Patent Appln. Disclosure No. 08-213403

Problem to be solved by the invention

[0012] In recent years, however, since the diameter of a silicon wafer has been enlarged and the degree of integration of a semiconductor device pattern has been increased, warpages occurring in a wafer besides slip dislocation occurrence has become problematic.

[0013]A typical example of slips and warpages introduced by a heat treatment is shown in Fig. 1. Heat treatment furnaces roughly include

two kinds of furnaces, a batch furnace and an RTA (Rapid Thermal Annealer). Since the degree of heat distortion is different between both furnaces, the way of warpage and slip occurrence in a wafer is also different. Slips are introduced from a contacting point between a wafer back face and a wafer holding portion, or a wafer edge part. The introduced slips extend in a {110} direction, sometimes cause wafer damage or breakage. A warpage is a deformation caused by heat distortion during a heat treatment. On a wafer of {100} plane, for example, hill- and valley-shaped parts appear, as shown in Fig. 1. A warpage of a silicon wafer before a heat treatment conducted for rendering desired properties to the wafer, is usually suppressed to not more than 10μm. After a heat treatment like RTA, however, the difference in the height between the hill- and the valley-shaped parts sometimes increases up to tens of μm. A large warpage retards accurate exposure of a semiconductor device pattern on a wafer surface, causing the reduction in the yield of semiconductor devices.

[0014] The problem of warpages becomes serious when a wafer diameter is 200 mm or more. This problem could not be avoided just by specifying a high density of BMDs as above.

[0015] Embodiments of the present invention seek to provide a silicon wafer wherein the occurrence of both slip dislocations and warpages during a semiconductor device production process can

be suppressed and the process for producing said silicon wafer.

Method for solving the problem

[0016] For avoiding the problem of slips and warpages occurring by the use of a batch furnace and an RTA, measures responding to the properties of both furnaces should be taken.

[0017] As a result of a dedicated study, the present inventors have found that it may be effective in avoiding warpage occurring by the use of a batch furnace, to reduce a stacking fault called BSF (Bulk Stacking Fault), which occurs in a silicon wafer, apart from BMD.

[0018] They have also found that it may be effective in avoiding warpage occurring by the use of an RTA, to reduce the density of BMDs with comparatively large sizes contained in a silicon wafer.

[0019] They have further found that it may be effective in avoiding slips occurring by the use of a batch furnace and an RTA, to adjust the density of BMDs with comparatively small sizes contained in a silicon wafer to a specific value or more, and accomplished the present invention.

[0020] Namely, embodiments of the present invention relate to a silicon wafer wherein BMDs with small sizes are formed in a high density, while the density of BMDs with large sizes is reduced, and furthermore, the density of BSFs is reduced.

[0021] In accordance with a first aspect of the invention, there is provided a silicon wafer wherein BMD has an octahedral shape, characterized in that, and of BMDs present at a depth of not less than 50 μm

from the surface of the silicon wafer, the density of BMDs having diagonal size of 10 nm to 50 nm is not less than 1 X $10^{12}/cm^3$, and the density of BSFs having sizes of not less than 1 μm is not more than 1 X $10^8/cm^3$.

[0022] The present silicon wafer should have an interstitial oxygen concentration of not more than 6 X 10^{17} atoms/cm^3, and the density of BMDs with diagonal size of not less than 200 nm should be not more than 1 X 10^7 atoms/cm^3.

[0023] Also disclosed herein is a process for producing the present silicon wafer as stated above, characterized in that a substrate is subjected to a heat treatment comprising:

A: a low temperature heat treatment step wherein the substrate is heat treated at a temperature of not less than 700 °C to not more than 800 °C for not less than 30 minutes to not more than 5 hours;

B: a temperature increasing step following the above low temperature heat treatment step, wherein the substrate is heat treated up to 1,000 °C at a rate of temperature increase of not less than 0.5 °C /minute to not more than 2 °C /minute; and

C: a high temperature heat treatment step following the temperature increasing step, wherein the substrate is heat treated at a temperature of not less than 1,000 °C to not more than 1,250 °C in such a way that the diffusion length of interstitial oxygen is not less than 20 μm.

[0024] Also disclosed herein is a process for producing the present silicon wafer above, characterized in that a substrate is subjected to a heat treatment comprising: A: a low temperature heat treatment step wherein the substrate is heat treated at a temperature of not less than 700 °C to not more than 800 °C for not less than 30 minutes to not more than 5 hours; B: a temperature increasing step following the low temperature heat treatment step, wherein the substrate is heat treated up to a temperature of not less than 900 °C to not more than 1,000 °C at a rate of temperature increase of not less than 0.5 °C /minute to not more than 2 °C /minute; and

C: a temperature decreasing and taking out step following the temperature increasing step, wherein a temperature in a furnace is decreased at a rate of temperature decrease of not less than 1 °C/minute to not more than 10 °C/minute, and when the temperature of the substrate becomes not less than 600 °C to not more than 800 °C, the substrate is taken out of the furnace and cooled down to room temperature; and

D: a high temperature heat treatment step following the temperature decreasing and taking out step, wherein a temperature in the furnace is decreased down to not less than 600 °C to not more than 800 °C and the substrate is inserted thereinto, and the temperature of the substrate is increased at a rate of temperature increase of

not less than 5 °C/minute to not more than 10 °C/minute in the range of from the substrate insertion temperature to less than 1,100 °C and at a rate of temperature increase of not less than 1 °C/minute to not more than 2 °C/minute in the range of from not less than 1,100 °C to not more than 1,250 °C, and the substrate is maintained constantly at a temperature in said range in such a way that the diffusion length of interstitial oxygen is not less than 20 μm.

[0025] In the present production process, the diffusion length of interstitial oxygen in the above step D should be not less than 20 μm. The interstitial nitrogen concentration in the substrate should be 5 X 10^{14} atoms/cm^3 to 1 X 10^{16} atoms/cm^3. And the interstitial carbon concentration in the substrate should be 2 X 10^{16} atoms/cm^3 to 2 X 10^{17} atoms/cm^3.

[0026] Also disclosed herein, BMD with an octahedral shape means BMD surrounded with plural {111} planes and other planes as shown in Figure 2. Ordinarily, there are BMDs surrounded with eight {111} planes as shown in Figure 2(1) or Figure 2(3), and those surrounded with {100} planes in addition to {111} planes as shown in Figure 2(2). BMD surrounded with planes other than {111} and {100} planes sometimes appears.

[0027] BMDs present in a wafer can have a plate shape besides an octahedral shape, and such plate-shaped BMD is surrounded with two comparatively large {100} planes and other planes as shown in Figure

2(4) and Figure 2(5). BMD sometimes has a dendrite shape inside as shown in Figure 2(5). The octahedral shape is distinguished from the plate shape as follows: of the sizes in the {100} and {010} directions viewed from the {001} direction as shown in Figure 3, the longer one is represented as A and the shorter one is represented as B. BMD with the ratio of A/B (hereinafter, "ellipticity") of not more than 1.5 is regarded octahedral BMD and BMD with an ellipticity exceeding 1.5 has a plate-shaped BMD. Since BMDs in a silicon wafer have a variations in shapes, the determination whether BMD present in a wafer has an octahedral shape or a plate shape should be made by measuring A/B of plural BMDs present at different positions in a wafer, calculating the average thereof (hereinafter "mean ellipticity") and confirming whether the obtained value exceeds 1.5 or not. If the value exceeds 1.5, the optimal BMD size distribution for suppressing the occurrence of slips and warpages becomes different from the range of embodiments of the present invention since distortions applied to crystal lattice around BMDs are different.

[0028] In the embodiments of the present invention, the diagonal size of an octahedral BMD means the longer direction A of the above {100} and {010} directions.

[0029] In embodiments of the present invention, BSF means a disk-like stacking fault occurring inside a silicon wafer during a heat treatment. Said stacking fault is formed by the agglomeration of interstitial

atoms. Oxygen precipitation nuclei, which become BSF nuclei, are present in the stacking fault. BSF is formed since interstitial atoms emitted from BMDs during a heat treatment agglomerate disk-likely around a BSF nucleus. An oxygen precipitation nucleus, which becomes a BSF nucleus, different from other BMDs, has the ability to agglomerate interstitial atoms around it. This is because such an oxygen precipitation nucleus has a form and a size different from those of other BMDs. Some percentages of oxygen precipitation nuclei become BSF nuclei and form BSFs, and the remainder are observed as BMDs. Thus, the density of BSFs is increased as the increase of the density of BMDs.

[0030] BSF is a defect different from OSF. OSF is a disk-like stacking fault occurring on the surface of a wafer when the wafer is oxygenated. In this case, interstitial oxygen forming a stacking fault is supplied from an oxygenated film on the surface of a wafer. OSF nuclei are considered to be oxygen precipitates having already been present during crystal growth. Thus, the occurrence of OSF depends on conditions for crystal growth.

Effect of the invention

[0031] The present silicon wafer as disclosed herein contains BMDs having an octahedral shape, and of BMDs present at a depth of 50 μm or more from the surface of said silicon wafer, the density of BMDs having diagonal

size of 10 nm to 50 nm is not less than 1 X 10^{12} /cm^3, and the density of BSFs is not more than 1 X 10^8 /cm^3. In virtue of said feature, the occurrence of both slips and warpages during a device production process is considerably suppressed, reduction in strength can be prevented though the silicon wafer has a DZ layer, and high quality devices with a large diameter (typically not less than 200 mm) can be produced. Compared with a mirror wafer having no BMD inside, the present silicon wafer hardly causes slip dislocations and warpages and has the high gettering ability.

[0032] According to the present production process as disclosed herein, the occurrence of both slips and warpages during a device production process can considerably be suppressed, and a high quality silicon wafer which can prevent the reduction of strength though having a DZ layer can be produced.

[0033] Compared with a mirror wafer having no BMD inside, the present silicon wafer may hardly cause slip dislocations and warpages and may have the high gettering ability.

Brief description of drawing

[0034]

[Figure 1] Figure showing slips and warpages introduced by heat treatment

[Figure 2] Schematic figure showing the difference between

octahedral BMD and plate-shaped BMD

Best embodiment for carrying out the invention

[0035] The present invention is closely explained on the basis of the embodiment for carrying out the invention in the following section.

[0036] (Silicon wafer)

The present silicon wafer is characterized in that the occurrence of both slips and warpages during a device production process can considerably be suppressed.

[0037] For carrying out the invention, the size of a wafer (diameter, thickness) is not limited. Also it is not limited whether doping should be conducted, and if doping is conducted, which element should be doped. These can properly be selected depending on the kinds of desired semiconductor silicon wafer.

[0038] In addition, a semiconductor device produced using the present silicon wafer is not particularly limited, and present silicon wafer can be applied to the production of various kinds of semiconductor silicon devices. The present silicon wafer can be applied broadly to the production, specifically of an epitaxial wafer having a surface on which an epitaxial layer is formed, a laminated SOI wafer, a SIMOX wafer having been subjected to the SIMOX (Separation By Implanted Oxygen) treatment or an SiGe wafer having a surface on which a SiGe layer is formed.

[0039] The present silicon wafer is characterized in that BMD has an octahedral shape, and of BMDs present at the depth of 50 μm or more from the surface of said silicon wafer, the density of BMDs having diagonal size of 10 nm to 50 nm is not less than $1 \times 10^{12}/cm^3$. This is based on the present inventors' knowledge as explained below.

[0040] That is, in a silicon wafer containing BMDs with an octahedral shape, BMDs present at a depth of 50 μm or more from the surface have an influence on the properties of slips or warpages, and of those BMDs, BMDs with diagonal size of 10 nm to 50 nm exhibit great effect of suppressing the occurrence of slips, and in a silicon wafer containing said BMDs in a high density of not less than $1 \times 10^{12}/cm^3$, the occurrence of slips can considerably be suppressed to a small level (typically not more than 10 mm) in a general device production process. In virtue of this, if a slip occurs from a wafer holding portion during a device production process, the exposure of the slip on the surface of the silicon wafer can be prevented, and if a slip occurs in the edge of a wafer, the arrival of the slip at a semiconductor device formation region can be prevented and adverse influence on devices can be prevented.

[0041] In the above range, longer diagonal size and higher densities are preferable since the effect of suppressing slips becomes greater. Meanwhile, the concentration of oxygen contained as solid solution under the conditions of the general CZ single crystal growth is

about 1×10^{18} atoms/cm^3, and in the state where BMDs are formed in a high density, nearly all of them are precipitated. Thus, it is generally difficult to form BMDs having diagonal size in the above range in a density exceeding $5 \times 10^{12}/cm^3$.

[0042] In addition, the present silicon wafer is characterized by the density of BSFs with sizes of not less than 1 μm is not more than $1 \times 10^8/cm^3$. This is based on the present inventors' knowledge as shown below.

[0043] That is, when a thermal stress is applied to a wafer, BSF inside the wafer unfaults to a loop dislocation, and as a result of the move and extension of the loop dislocation, the wafer causes plastic deformation resulting in the occurrence of warpages. The loop dislocation here is a closed loop-like fine dislocation fault occurring inside a wafer, which essentially differs from slips generally meaning dislocation faults introduced from a wafer holding portion or a wafer edge portion. Particularly, BSF having a size of not less than 1 μm easily changes into dislocation loop, and by suppressing the density of said BSFs to not more than 1×10^8 / cm^3, the occurrence of warpages in a batch furnace can be suppressed to an extremely small, typically, the increase of warpages from before to after heat treatment could be suppressed to not more than 10 μm. The size of BSF here means a diameter of a disk-like BSF. In view of the experiments having been conducted by the present

inventors, the above phenomenon frequently occurs particularly in a batch furnace. In an RTA, the phenomenon that BSF inside a wafer changed into dislocation is not observed, and the enlargement of warpages is not caused by BSF.

[0044] In general, BSF density had correlation with BMD density, and BSF density is known to be about 1/10 to 1/1,000 of BMD density. Thus, it is generally difficult to generate BMDs in a high density of $1 \times 10^{12}/cm^3$ while the density of BSF is suppressed to not more than $1 \times 10^{8}/cm^3$.

[0045] Further, the present silicon wafer is characterized in that, of BMDs present at a depth of 50 μm or more, the density of BMDs having diagonal size of not less than 200 nm is not more than 1×10^{7} $/cm^3$, and this is based on the present inventors' knowledge as shown below.

[0046] That is, when a thermal stress is applied to a wafer, a high density dislocation occurs inside a wafer starting from a large-sized BMD of those present at a depth of 50 μm or more from the surface, and as a result, the wafer causes plastic deformation, resulting in the occurrence of warpages. The occurrence of dislocation often starts from BMD having diagonal size of not less than 200 nm, and the occurrence of warpages during the general device production process can be suppressed to an extremely small level, typically 20 μm or less by suppressing the density of the above

BMDs to not more than $1 \times 10^7/cm^3$.

[0047] Besides the features explained above, the present silicon wafer should have a reduced average value of interstitial oxygen concentration.

[0048] Here, an interstitial oxygen concentration is usually measured in the whole part of a silicon wafer, and a region to be measured includes a DZ layer in addition to a BMD layer. However, since a DZ layer is much thinner than a BMD layer, an interstitial oxygen concentration measured in the overall silicon wafer can be assumed to be equal to an interstitial oxygen concentration in a BMD layer.

[0049] The interstitial oxygen concentration in the overall silicon wafer is defined in such a manner on the basis of the inventors' unpresumable knowledge that the optimal BMD distribution (size and density) explained above sometimes varies with conditions for a heat treatment in a device production process due to the precipitation of interstitial oxygen, and slips and warpages are sometimes caused thereby.

[0050] For this purpose, the interstitial oxygen concentration should be not more than 6×10^{17} atoms/cm^3. The lower limit of the interstitial oxygen concentration can be assumed to be about 4×10^{17} atoms/cm^3. This is because reducing the concentration to a level lower than the above lower limit requires a heat treatment at a

low temperature for extremely long time, and such a heat treatment is difficult to conduct actually.

[0051] In addition to the features explained above, the present silicon wafer should have an interstitial carbon concentration of not less than 1×10^{16} atoms/cm^3. If the carbon concentration is less than said range, a BSF density exceeds 1×10^8 /cm^3 when a BMD density is not less than 1×10^{12} /cm^3. Such carbon concentration is not preferable.

[0052] It is desirable that the above BMD size distribution and the interstitial oxygen concentration are achieved on the overall surface of a wafer, but they may be achieved on a part of the region, depending on purposes. For preventing only a typical slip introduced from the edge part of a wafer, it is sufficient that the above BMD size distribution and the interstitial oxygen concentration are achieved in the outer region distant from the centre of a wafer by 80 % or more of the radius of the wafer. This is because a slip introduced from the edge part of a wafer frequently occurs in said region. In addition, it is sufficient for just preventing typical warpage of a wafer, that the above BMD size distribution and interstitial oxygen concentration are achieved in the inside region by less than 80 % of the radius of the wafer. This is because typical high density dislocation inside a wafer, which causes a warpage, frequently occurs in said region.

[0053] The present silicon wafer is extremely excellent in respect that slips and warpages occurring in a device production process are small. More specifically, the present silicon wafer, particularly a silicon wafer wherein BMDs are controlled and an interstitial oxygen concentration is reduced as stated above, is characterized by slips with extremely short sizes occurring also in the following heat treatments (typically, slip length is not more than 10 mm and increase of a warpage of a wafer after the heat treatment is not more than 10 μm) as explained below.

[0054] The above explained shape, across corner dimension and number of BMDs are ordinarily measured by publicly known measurement methods. More specifically, they can be measured using the transmission electron microscope (hereinafter, "TEM") and the Optical Precipitate Profiler (hereinafter, "OPP"). As for BSF, the size and the number can be measured by selective etching.

[0055] Regarding the slip dislocation or the amount of a warpage of a wafer, the measurement and the evaluation methods are not particularly limited, and they can ordinarily be measured by publicly known methods. More specifically, slip dislocation can be measured by the X-ray topography, and the amount of a warpage can be evaluated by observing the warpage using FT-90A available from NIDEK Corporation.

[0056] In addition, interstitial oxygen concentration can be

measured by the Fourier transform infrared spectroscopy (FTIR).

[0057] (Process for producing silicon wafer)

The present silicon wafer has the above explained features. Thus, any processes for producing a silicon wafer having said features can be applied without limitation. More specifically, a silicon wafer having the above features can be produced by controlling conditions for a single crystal growth (crystal pulling-up rate, crystal cooling rate, crucible rotation, gas stream and the like) or conditions for heat treatment (heat treatment temperature, time, increasing/decreasing temperature and the like).

[0058] In the present invention, it is particularly preferable that a substrate is heat treated step by step.

[0059] The substrate here means a pre-heat treatment silicon wafer, which is cut out of a single crystal ingot and is properly subjected to steps like chamfer and the like, in addition to heat treatment.

[0060] The size of the substrate (diameter, thickness) is not particularly limited. And it is not particularly limited whether doping should be conducted and if doing is conducted, which element should be doped. These can properly be selected depending on the kind and the performance of a desired silicon wafer.

[0061] The interstitial oxygen contained in the substrate should have an oxygen concentration during the silicon single crystal growth by the CZ method under ordinary conditions, and during the production

by the heat treatment explained below, the concentration should preferably be 6.5×10^{17} to 8.5×10^{17} atoms/cm^3. If the oxygen concentration is less than the above range, BMDs are not formed in a high density, and if the oxygen concentration exceeds said range, BSFs or large-sized BMDs are formed in a high density. Both concentrations are, therefore, unpreferable.

[0062] In the present invention, the above heat treatment preferably comprises (A): a first heat treatment wherein temperature is increased from 700 ° C to 800 ° C for 30 minutes to 5 hours; (B): a second heat treatment wherein the temperature is further increased up to 1,000 ° C at a rate of 0.5 to 2 ° C / minute for 2.5 to 10 hours; and (C): a third heat treatment wherein the temperature is maintained within a range of from 1,000 ° C to 1,250 ° C in such a way that the diffusion length of the interstitial oxygen is not less than 20 μm.

[0063] In the step (A), a heat treatment temperature less than 700 ° C is unpreferable since the diffusion of oxygen insufficiently occurs, resulting in the insufficient formation of BMDs. A temperature exceeding 700 ° C is also unpreferable since such temperature hardly has an influence on the BMD optimization, involving great ineffectiveness. The time of the heat treatment less than 30 minutes is insufficient for the formation of BMD nuclei, and the time exceeding 5 hours incredibly reduces productivity and thus unpreferable.

[0064] In the step (B), the rate of low rate-temperature increase less than 0.5 °C/ minute is unpreferable since a stable rate of temperature increase cannot be ensured, and the rate exceeding 2 °C/ minute is also unpreferable since precipitated BMDs could disappear. The interstitial oxygen concentration of not more than 6×10^{17} atoms/cm^3 is preferable for keeping the density of BMDs having sizes of not less than 200 nm at a level of $1 \times 10^7/cm^3$ or less, since the interstitial oxygen concentration in a silicon wafer after the completion of the following high temperature heat treatment easily becomes not more than 6×10^{17} atoms/cm^3. It can be presumed that this occurs since interstitial oxygen atoms inside a silicon wafer gather around BMDs while interstitial oxygen is externally diffused during the high temperature heat treatment (C).

[0065] The step (C) has a purpose of forming a DZ layer by externally diffusing interstitial oxygen. In this step, a temperature less than 1,000 °C is unpreferable from the viewpoint of productivity reduction since the external diffusion of interstitial oxygen takes long time, and a temperature exceeding 1,250 °C is also unpreferable since the degradation of the members constituting an anneal furnace becomes serious. The diffusion length of interstitial oxygen is a numerical value calculated on the basis of temperatures and time in the step (C), and concretely, it can be obtained by integrating the following formula (i) in accordance with the temperature pattern

in the step (C):

[0066]

Diffusion length of interstitial oxygen (μm) =

$2 \times 10^4 \times (\int D_{oxygen} dt)^{0.5}$ (i)

[0067] Wherein,

D_{oxygen} (cm^2/second) = $0.17 \times \exp(-2.53 \div 8.62 \times 10^{-5} \div$ temperature (K)).

A heat treatment resulting in a diffusion length of interstitial oxygen of not less than 20 μm is preferable for forming a broad DZ layer having a size of not less than 5 μm.

[0068] Temperature increase from the steps (B) to (C) is not particularly limited, and an ordinary rate of temperature increase may preferably be used. The most general rate of temperature increase is not less than 5 °C/minute to not more than 10 °C/minute in a temperature range of from not less than 1,000 °C to less than 1,100 °C, and not less than 1 °C/minute to not more than 2 °C/minute in a temperature range of from not less than 1,100 °C to not more than 1,250 °C.

[0069] In the present invention, the above temperature treatment more preferably comprises:

(A): a low temperature heat treatment step wherein the substrate is heat treated at a temperature of not less than 700 °C to not more than 800 °C for not less than 30 minutes to not more than 5

hours;

(B): a temperature increasing step following the low temperature heat treatment step, wherein the substrate is heat treated up to a temperature of not less than 900 °C to not more than 1,000 °C at a rate of temperature increase of not less than 0.5 °C /minute to not more than 2 °C /minute; and

(C): a temperature decreasing and taking out step following the temperature increasing step, wherein a temperature in a furnace is decreased at a rate of temperature decrease of not less than 1 °C/minute to not more than 10 °C/minute, and when the temperature of the substrate becomes not less than 600 °C to not more than 800 °C, the substrate is taken out of the furnace and cooled down to room temperature; and

(D): a high temperature heat treatment step, wherein a temperature in the furnace is decreased down to not less than 600 °C to not more than 800 °C and the substrate is inserted thereinto, and the temperature of the substrate is increased at a rate of temperature increase of not less than 5 °C/minute to not more than 10 °C/minute in the range from the substrate insertion temperature to less than 1,100 °C and at a rate of temperature increase of not less than 1 °C/minute to not more than 2 °C/minute in the range from not less than 1,100 °C to not more than 1,250 °C, and the substrate is maintained constantly at a temperature in said range in such a way that the

diffusion length of interstitial oxygen is not less than 20 μm. The heat decreasing and taking out step (C) is conducted in the case where two heat treatment furnaces are used and the heat treatments (A) to (C) are conducted separately from the heat treatment (D) in different furnaces. In the case where each of the heat treatments is advantageously conducted in separate heat treatment furnaces for improving productivity, it is preferable to add the step (C) and divide heat treatment into (A) to (C) and (D).

[0070] In this case, the temperature at the completion of the temperature increasing step at a rate of not less than 0.5 °C/minute to not more than 2 °C/minute in the step (B) should preferably be not less than 900 °C for obtaining the BMD density in an annealed wafer of not less than $1 \times 10^{12}/cm^3$. The temperature less than 900 °C is unpreferable since BMDs having been formed during temperature increase disappear in the following step (D). In the step (B), the temperature at the completion of the temperature increasing step at a rate of not less than 0.5 °C/minute to not more than 2 °C/minute should preferably be not more than 1,000 °C. This is because temperature increasing in a temperature range exceeding 1,000 °C does not have an influence on the density of BMDs. In the divided heat treatment, it can be assumed that BMDs formed at the completion of the step (C) have changed into BMDs neither shrinking nor disappearing in the following high temperature heat treatment step

(D), since the substrate has once been cooled down to room temperature in the step (C). Because of this, the divided heat treatment does not require increasing a temperature up to 1,000 °C at a rate of not less than 0.5 °C/minute to not more than 2 °C/minute like the continuous heat treatment. Moreover, in the case where a temperature is increased up to 1,000 °C after the completion of the steps (B) and (C), the interstitial oxygen concentration is not more than 6×10^{17} atoms/cm^3, and the above heat treatment is preferable also for keeping the density of BMDs with sizes of 200 nm or more, at a level not exceeding 1×10^7/cm^3. In the step (B), the rate of temperature increase of less than 0.5 °C/minute is unpreferable since stable rate of temperature increase cannot be ensured and the rate exceeding 2 °C/minute is also unpreferable since precipitated BMDs could disappear.

The rate of temperature decrease in the step (C) should be not less than 1 °C/minute to not more than 10 °C/minute, which can be achieved by using a general furnace. As for the temperature in the furnace at which the substrate is taken out thereof, a temperature less than 600 °C is unpreferable since said temperature causes the life of the heater of the furnace to be shortened, and a temperature exceeding 800 °C is also unpreferable since said temperature causes the deterioration of the members of a furnace.

For the same reason as the above (C), the temperature in the furnace

at which a substrate is inserted in the step (D) should be neither less than 600 °C nor exceeding 800 °C. The preferable rate of temperature increase should be a rate which can be achieved by the use of a general furnace and can shorten the overall time for the heat treatment, specifically, not less than 5 °C/minute to not more than 10 °C/minute within a range of from a temperature at which the substrate is inserted to a temperature of less than 1,100 °C, and not less than 1 °C/minute to not more than 2 °C/minute within a range of from not less than 1,100 °C to not more than 1,250 °C. The ranges of the temperature and the diffusion length of oxygen in the heat treatment at a temperature of not less than 1,000 °C are as stated above. The rate of temperature decrease and the temperature at which the wafer is taken out are not particularly limited.

[0071] The apparatus used for carrying out the series of the above heat treatments is not particularly limited, and conventionally and publicly known apparatus can preferably be used. Specifically, an ordinary batch vertical furnace or a batch vertical furnace with oxygen purging function may be used.

[0072] In the present production process, the above substrate should contain nitrogen. This is because the increase of warpage can further be suppressed (typically, to not more than 5 μm) when the substrate contains nitrogen. The production of devices with higher performance

becomes possible by suppressing the increase of warpage in such a way.

[0073] The concentration of nitrogen to be added for the above purpose should be 5×10^{14} atoms/cm^3 to 1×10^{16} atoms/cm^3. The nitrogen concentration less than the above range is unpreferable since it becomes difficult to suppress the increase of warpage to not more than 5 μm though it is possible to suppress the increase of warpage to not more than 10 μm. The concentration of nitrogen exceeding the above range is also unpreferable since polycrystallization occurs and the yield of wafers could be reduced.

[0074] In the present production process, the substrate should contain carbon. This is because a silicon wafer with a carbon concentration of not less than 1×10^{16} atoms/cm^3 can be produced when the substrate contains carbon.

[0075] The concentration of carbon to be added for the above purpose should be not less than 2×10^{16} atoms/cm^3 to not more than 2×10^{17} atoms/cm^3. The carbon concentration less than the above range is unpreferable since the carbon concentration in a silicon wafer becomes less than 1×10^{16} atoms/cm^3. The concentration exceeding the above range is also unpreferable since polycrystallization occurs and the yields of wafers could be reduced.

[0076] The processes for adding nitrogen or carbon to a substrate are not particularly limited, and conventionally and publicly known

processes can preferably be used. More concretely, a process for adding nitrogen wherein substrates with nitride films are added to a melt solution for pulling up a single crystal in order to control the concentration of nitrogen in a substrate to be obtained, and a process for adding carbon wherein carbon powder is added to a melt solution for pulling up a single crystal in order to control the carbon concentration in a substrate to be obtained can be employed.

[0077] The processes for measuring nitrogen, carbon and oxygen concentrations in a substrate are not particularly limited, and said concentrations can preferably be measured by conventionally and publicly known processes. More specifically, nitrogen concentration can be measured using a secondary ion mass spectrometry apparatus (SIMS). The oxygen and carbon concentrations can be measured by the infrared absorption spectrometry, and calculated using values defined by JEITA (Japan Electronics and Information Technology Industries Association) as conversion factors.

Examples

[0078] The present invention is closely explained on the basis of examples in the following section, but it is not particularly limited to those examples.

[0079] (Process for producing annealed wafer)

Single crystal ingots were produced under various conditions (wafer

diameter, conduction type, oxygen-, nitrogen- and carbon concentrations), the same portions of the straight trunks of the obtained single crystal ingots were cut out using a wire saw and subjected to a mirror treatment to form substrates with a thickness of 725 to 750 μm. From these substrates, annealed wafers were produced by the following processes (1) and (2):

(1) Heat treatment 1

The obtained substrates were inserted into a batch vertical heat treatment furnace, and subjected to the first heat treatment (A, B and C) and the second heat treatment (D) in an argon atmosphere in the same furnace. The diffusion lengths of the interstitial oxygen in the second heat treatment were obtained by integrating the above formula (i) on the basis of temperature and time in accordance with the temperature pattern in the second heat treatment.

[0080] The first heat treatment: After maintained at a temperature of 700 ° C for four hours, the temperature was increased at a rate of 1 ° C/minute from 700 ° C to 1,000 ° C;

The second heat treatment: the temperature was increased at a rate of 5 ° C/minute from 1,000 ° C to 1,100 ° C, at a rate of 1 ° C/minute from 1,100 ° C to 1,200 ° C, and after maintained at 1,200 ° C for 1 hour, the temperature was decreased at a rate of 1 ° C/minute from 1,200 ° C to 1,100 ° C, at a rate of 5 ° C/minute from 1,100 ° C to 1,000 ° C and at a rate of 2 ° C/minute from 1,000 ° C to 700 ° C,

and the substrates were taken out at a temperature of 700 ° C.

(2) Heat treatment 2

The obtained substrates were inserted into a first batch vertical heat treatment furnace and after the completion of the first heat treatment (A, B and C), the substrates were inserted into the second batch vertical heat treatment furnace and subjected to the second heat treatment (D) in an argon atmosphere. The diffusion lengths of the interstitial oxygen in the second heat treatment were obtained by the same way as in the Heat treatment 1.

The first heat treatment: After maintained at 700 ° C for four hours, the temperature was increased up to 900 ° C at a rate of 1 ° C/minute, and the temperature was thereafter decreased down to 700 ° C at a rate of 3 ° C/minute. The substrates were taken out at a temperature of 700° C from the furnace and cooled down to room temperature;

The second heat treatment: The substrates were inserted at a temperature of 700 ° C, and the temperature was increased from 700 ° C to 1,100 ° C at a rate of 5 ° C/minute, from 1,100 ° C to 1,200 ° C at a rate of 1 ° C/minute, and after maintained at 1,200 ° C for 1 hour, the temperature was decreased from 1,200 ° C to 1,100 ° C at a rate of 1 ° C/minute, from 1,100 ° C to 1,000 ° C at a rate of 5 ° C/minute and from 1,000 ° C to 700 ° C at a rate of 2 ° C/minute, and the substrates were taken out at a temperature of 700 ° C.

The conditions for the production of the silicon wafers (wafer diameter, conduction type, concentrations (of carbon, nitrogen and oxygen) in the substrates and diffusion lengths of interstitial oxygen in the heat treatments) were summarized in Table 1. In Table 1, p-type means boron dope. And the concentrations of contained substances (such as oxygen) were controlled and measured by ordinarily and publicly known processes.

[0081] [Table 1]

[0082] (Measurement and evaluation of annealed wafers)

Table 1

No.	Wafer diameter (mm)	Con-duction type	Nitrogen (atoms/cm3)	Carbon (atoms/cm3)	Oxygen (atoms/cm3)	Heat treatment condition	Interstitial oxygen diffusion length in second heat treatment (μm)
Example 1	200	p	4.7E+15	2.1E+16	8.3E+17	heat treatment 1	40
Example 2	200	n	3.5E+15	5.4E+16	7.9E+17	heat treatment 1	40
Example 3	300	p	5.4E+14	9.5E+16	8.3E+17	heat treatment 1	40

No.	Wafer diameter (mm)	Con-duction type	Nitrogen (atoms/cm3)	Carbon (atoms/cm3)	Oxygen (atoms/cm3)	Heat treatment condition	Interstitial oxygen diffusion length in second heat treatment (μm)
Example 4	200	p	4.7E+15	2.1E+16	8.3E+17	heat treatment 2	40
Example 5	200	n	3.5E+15	5.4E+16	7.9E+17	heat treatment 2	40
Example 6	300	p	5.4E+14	9.5E+16	8.3E+17	heat treatment 2	40

No.	Wafer diameter (mm)	Con-duction type	Nitrogen (atoms/cm3)	Carbon (atoms/cm3)	Oxygen (atoms/cm3)	Heat treatment condition	Interstitial oxygen diffusion length in second heat treatment (μm)
Comparative Example 1	200	p	2.4E+15	——	8.7E+17	heat treatment 1	40
Comparative Example 2	200	p	1.4E+15	1.2E+16	8.5E+17	heat treatment 1	40
Comparative Example 3	200	p	2.4E+15	——	8.7E+17	heat treatment 2	40
Comparative Example 4	200	p	1.4E+15	1.2E+16	8.5E+17	heat treatment 2	40

The annealed wafers obtained under the above production conditions were measured and evaluated in respect of the following (1), (2), (3), (4) and (6). The nitrogen-added wafers were measured also in respect of (5) nitrogen concentration. Of the samples used for the measurement of (1) and (2), TEM samples were obtained by grinding the wafers to the predetermined depths (50 μm, 100 μm and 300 μm) with a precision grinder, and taken out from the two positions, the centre position and the position 10 mm distant from the edge of the wafer. Measurements were conducted by using OPP by setting focus to the predetermined depths (50 μm, 100 μm and 300 μm) and the predetermined positions (the centre position, the position 10 mm distant from the edge) in the wafers.

(1) Determination of BMD shape: BMD ellipticity was determined on the basis of the ratio of signal intensities obtained by measuring the same measurement sample twice, while a scanning direction of OPP was changed between {110} and {100} directions. That means, the relation between the ratio of signal intensity and the BMD ellipticity was examined in advance and the ellipticity was obtained from the ratio of signal intensities. Measurement was also conducted by using TEM, and the ellipticity was measured on the basis of a microscope image viewed from {001}direction. On the basis of the results obtained above, BMD shape was determined. The determination was conducted on the basis of the mean ellipticity obtained by

averaging all of the ellipticities obtained by measuring at least 10 BMDs contained in each of the samples, by judging whether said value exceeded 1.5 or not.

BMD size and density: The BMD size and density of the samples were measured using OPP and TEM. In view of the observation results obtained by the following processes 1) and 2), the density of BMDs with predetermined sizes was obtained. The density of BMDs with predetermined sizes was obtained by averaging the densities at predetermined three depths (50 μm, 100 μm and 300 μm).

1) Measurement by OPP: Using OPP produced by Axent Technology Inc, signal intensity obtained by electrically signal-processing phase difference of transmission laser ascribed to BMD was measured. BMDs with known sizes were measured using OPP in advance, and a calibration curve showing the relation between the signal intensities and the BMD sizes were made. The calibration curve was as shown below.

[0084] Diagonal size of octahedral BMD (nm) = $153 \times (\text{OPP signal})^{0.43}$

When the sizes were obtained, the Ghost Signal Removal Treatment (K. Nakai Review of Scientific Instruments, vol. 69 (1988), pp. 3283) was conducted. The detection sensitivity was set to the degree that BMD with diagonal size of not less than 80 nm could be measured.

[0085] 2) Measurement using TEM: The density of BMDs with predetermined sizes was obtained on the basis of the microscopic image obtained by measurement. The density was obtained from the

number of BMDs observed within a field of view and the volume of a sample corresponding to the observed region.

(3) BSF size and density

After the completion of oblique polishing, the polished surface of each sample was subjected to selective etching to a depth of 2 μm in a light etching solution, and the pits of BSFs were observed using an optical microscope. BSF was observed here as a linear or an ellipsoidal pit. The length of a linear pit or the length of the long axial direction of an ellipsoidal pit was defined as the size of BSF. The area density of BSF pits was obtained from the number of BSF pits with sizes of not less than 1 μm and the area of the observed region, and BSF density (/cm^3) was calculated by dividing the area density by the etching amount to a depth of 2 μm. BSF density was obtained by averaging the measured values at predetermined three depths (50 μm, 100 μm and 300 μm) of the wafers.

(4) Interstitial oxygen- and carbon concentration in annealed wafer

The interstitial oxygen concentration in annealed wafers was measured by the infrared absorption spectrometry and the values defined by JEITA (Japan Electronics and Information Technology Industries Association) as conversion factors. The conversion factor for interstitial oxygen concentration was 3.03×10^{17} /cm^2 and for interstitial carbon concentration was 8.1×10^{16}/cm^2.

(5) Nitrogen concentration in annealed wafer

Samples were collected from annealed wafers. After the completion of polishing up to 20 μm for the removal of a nitrogen external diffusion layer on the surfaces thereof, interstitial nitrogen concentration was measured using SIMS.

(6) Evaluation of slip length and warpage resistance of annealed wafer

Annealed wafers were subjected to the following heat treatments (6)-A and (6)-B (hereinafter, "pseudo-device process heat treatment"). The warpages of annealed wafers before and after the pseudo-device process heat treatment were measured using FT-90A produced by NIDEK Corporation, and the increase of warpage was obtained according to the following equation:

Increase of warpage = warpage after the heat treatment - warpage before the heat treatment

The annealed wafers after the completion of the pseudo-device heat treatment were observed by the X-ray topography, and the length of the longest slip of those observed was defined as a representative value.

(6)-A: Heat treatment using batch heat treatment furnace

(I): A wafer was inserted into the furnace while a temperature in the furnace was maintained at 900 ° C;

(II): After maintained in an oxygen atmosphere at a temperature of 900 ° C for 30 minutes, the wafer was taken out at a temperature

of 900 ° C.

(6)-B: Heat treatment using RTA

A heat treatment under the following conditions was repeated ten times:

Insertion: room temperature;

Rate of temperature increase: 50 ° C/minute;

Maintenance: at 1,100 ° C for 1 minute;

Rate of temperature decrease: 30 ° C/minute;

Taking out: room temperature; and

Atmosphere: argon

(Measurement and evaluation results of annealed wafers)

Regarding the annealed wafers produced under the conditions as shown in Table 1, measured density of BMDs with a predetermined size and interstitial oxygen concentration, and slips and amounts of warpages caused by the pseudo-device process heat treatment were summarized in Table 2, as Examples and Comparative Examples. The mean ellipticity of BMD was not more than 1.5 in the wafers produced under any conditions.

[0087] [Table 2]

[0088] In Table 2, BMD densities (1) and (2) mean a density of BMDs

Table 2

No.	Wafer center part					10 mm from wafer edge					Batch furnace test		RTA test	
	BMD (/cm3)		BSF(/cm3)	Interstitial oxygen Atoms/cm3	Carbon Atoms/cm3	BMD (/cm3)		BSF(/cm3)	Interstitial oxygen Atoms/cm3	Carbon Atoms/cm3	slip mm	Increase of warpage um	slip mm	Increase of warpage um
	(1)	(2)				(1)	(2)							
Example 1	1.2E+12	9.0E+06	8.7E+07	5.6E+17	1.2E+16	1.3E+12	8.1E+06	8.1E+07	5.7E+17	2.1E+16	8	4	6	5
Example 2	1.3E+12	9.6E+06	9.5E+07	5.0E+17	2.1E+16	1.3E+12	9.5E+06	8.3E+07	5.1E+17	3.1E+16	8	4	6	5
Example 3	1.2E+12	9.0E+06	1.0E+08	5.0E+17	4.2E+16	1.3E+12	8.3E+06	9.4E+07	5.2E+17	3.8E+16	8	4	6	5

No.	Wafer center part					10 mm from wafer edge					Batch furnace test		RTA test	
	BMD (/cm3)		BSF(/cm3)	Interstitial oxygen Atoms/cm3	Carbon Atoms/cm3	BMD (/cm3)		BSF(/cm3)	Interstitial oxygen Atoms/cm3	Carbon Atoms/cm3	slip mm	Increase of warpage um	slip mm	Increase of warpage um
	(1)	(2)				(1)	(2)							
Example 4	1.4E+12	8.5E+06	9.2E+07	4.8E+17	1.1E+16	1.5E+12	8.1E+06	8.7E+07	4.8E+17	1.5E+16	8	4	5	5
Example 5	1.2E+12	9.3E+06	7.6E+07	5.2E+17	1.5E+16	1.3E+12	8.8E+06	7.2E+07	4.9E+17	2.7E+16	7	3	5	5
Example 6	1.3E+12	8.9E+06	9.8E+07	5.0E+17	3.2E+16	1.4E+12	8.5E+06	9.3E+07	4.8E+17	4.1E+16	8	4	6	5

No.	Wafer center part					10 mm from wafer edge					Batch furnace test		RTA test	
	BMD (/cm3)		BSF(/cm3)	Interstitial oxygen Atoms/cm3	Carbon Atoms/cm3	BMD (/cm3)		BSF(/cm3)	Interstitial oxygen Atoms/cm3	Carbon Atoms/cm3	slip mm	Increase of warpage um	slip mm	Increase of warpage um
	(1)	(2)				(1)	(2)							
Comparative Example 1	7.6E+11	2.3E+07	3.4E+08	7.2E+17	—	7.6E+11	2.0E+07	3.1E+08	7.6E+17	—	21	129	17	56
Comparative Example 2	1.8E+12	7.6E+06	9.8E+08	4.5E+17	5.1E+15	1.5E+12	8.7E+06	9.5E+08	4.2E+17	4.2E+15	5	154	6	7
Comparative Example 3	8.1E+07	1.3E+07	4.5E+08	6.8E+17	—	7.5E+11	3.4E+07	3.1E+08	6.5E+17	—	20	115	16	63
Comparative Example 4	1.7E+12	7.8E+06	9.7E+08	4.2E+17	7.8E+15	1.6E+12	8.8E+06	9.6E+08	4.4E+17	8.9E+15	4	150	7	8

with diagonal size of 10 to 50 nm, and a density of BMDs with diagonal size of not less than 200 nm, respectively.

[0089] The nitrogen concentration in the nitrogen-added annealed wafer did not differ from the nitrogen concentration measured asgrown.

[0090] These results show that in the case where the BMD density (1) is not less than $1 \times 10^{12}/cm^3$ and the BMD density (2) is not more than $1 \times 10^{7}/cm^3$ and the BSF density is not more than $1 \times 10^{8}/cm^3$, the lengths of slips after the completion of a batch furnace heat treatment and an RTA heat treatment are not more than 10 mm and the increases of warpages are suppressed to not more than 10 μm.

[0091] In addition, Comparative Examples show that in the case where the BMD density (1) was less than $1 \times 10^{12}/cm^3$, the lengths of slips after the completion of a batch furnace heat treatment and an RTA heat treatment exceeded 10 mm.

[0092] In the case where the BMD density (2) exceeded $1 \times 10^{7}/cm^3$, the increases of warpages after the completion of an RTA heat treatment exceeded 10 μm.

[0093] In the case where the BSF density exceeded $1 \times 10^{8}/cm^3$, the increases of warpages after the completion of a batch furnace heat treatment exceeded 10 μm.

[0094] In the present Examples, since tests for evaluating slips accompanied by the precipitation of interstitial oxygen and warpage

resistance were conducted, the present silicon wafer does not exhibit marked effect of suppressing the occurrence of slips and warpages in the case where interstitial oxygen concentration is not reduced. In the case where a heat treatment hardly accompanied by the precipitation of interstitial oxygen (for example, heat treatments at a comparatively low temperature or for short time) is carried out, however, the present silicon wafer wherein interstitial oxygen concentration is not reduced can incredibly suppress the occurrence of slips and warpages.

Claims

1. A silicon wafer wherein BMD has an octahedral shape, characterized in that, of BMDs present at a depth of not less than 50 μm from the surface of the silicon wafer, the density of BMDs having diagonal size of 10 nm to 50 nm is not less than $1 \times 10^{12}/cm^3$, and the density of BSFs having sizes of not less than 1 μm is not more than $1 \times 10^8/cm^3$.

2. The silicon wafer according to Claim 1, wherein an interstitial oxygen concentration is not less than 4×10^{17} atoms/cm^3 to not more than 6×10^{17} atoms/cm^3, and an interstitial carbon concentration is not less than 1×10^{16} atoms/cm^3.

3. The silicon wafer according to Claim 1 or 2, wherein the density of BMDs having diagonal size of not less than 200 nm is not more than 1×10^7 /cm^3.

4. A process for producing the silicon wafer according to any one of Claims 1 to 3, wherein a substrate is subjected to a heat treatment comprising:

A: a low temperature heat treatment step wherein the substrate is heat treated at a temperature of not less than 700 °C to not more than 800 °C for not less than 30 minutes to not more than 5 hours;

B: a temperature increasing step following the low temperature heat treatment step, wherein the substrate is heat treated up to 1,000 °C at a rate of temperature increase of not less than 0.5 °C /minute to not more than 2 °C /minute; and

C: a high temperature heat treatment step following the temperature increasing step, wherein the substrate is heat treated at a temperature of not less than 1,000 °C to not more than 1,250 °C in such a way that the diffusion length of interstitial oxygen is not less than 20 μm.

5. Process for producing the silicon wafer according to any one of Claims 1 to 3, wherein a substrate is subjected to a heat treatment comprising:

A: a low temperature heat treatment step wherein the substrate is heat treated at a temperature of not less than 700 °C to not more than 800 °C for not less than 30 minutes to not more than 5 hours;

B: a temperature increasing step following the low temperature heat treatment step, wherein the substrate is heat treated up to a temperature of not less than 900 °C to not more than 1,000 °C at a rate of temperature increase of not less than 0.5 °C /minute to not more than 2 °C /minute; and

C: a temperature decreasing and taking out step following the temperature increasing step, wherein a temperature in a furnace

is decreased at a rate of temperature decrease of not less than 1 °C/minute to not more than 10 °C/minute, and when the temperature of the substrate becomes not less than 600 °C to not more than 800 °C, the substrate is taken out of the furnace and cooled down to room temperature; and

D: a high temperature heat treatment step following the temperature decreasing and taking out step, wherein a temperature in the furnace is decreased down to not less than 600 °C to not more than 800 °C and the substrate is inserted thereinto, and the temperature of the substrate is increased at a rate of temperature increase of not less than 5 °C/minute to not more than 10 °C/minute in the range from the substrate insertion temperature to less than 1,100 °C and at a rate of temperature increase of not less than 1 °C/minute to not more than 2 °C/minute in the range from not less than 1,100 °C to not more than 1,250 °C, and the substrate is maintained constantly at a temperature in said range in such a way that the diffusion length of interstitial oxygen is not less than 20 μm.

6. The process for producing a silicon wafer according to any one of Claims 4 and 5, wherein the interstitial nitrogen concentration in the substrate is not less than 5×10^{14} atoms/cm^3 to not more than 1×10^{16} atoms/cm^3.

7. The process for producing a silicon wafer according to any one of Claims 4 to 6, wherein the interstitial carbon concentration in the substrate is not less than 2×10^{16} atoms/cm^3 to not more than 2×10^{17} atoms/cm^3.

Abstract

The present invention provides a silicon wafer wherein the occurrence of both slip dislocations and warpages during a device production process is suppressed, and a process for producing the same. The present silicon wafer contains BMDs with an octahedral shape, and of the BMDs present at a depth of not less than 50 μm from the surface of the silicon wafer, the density of BMDs with diagonal size of 10 nm to 50 nm is not less than $1 \times 10^{12}/cm^3$ and the density of BSFs is not more than $1 \times 10^{8}/cm^3$. The present silicon wafer should have an interstitial oxygen concentration of not less than 4×10^{17} atoms/cm^3 to not more than 6×10^{17} atoms/cm^3, and the density of BMDs with diagonal size of not less than 200 nm is not more than $1 \times 10^{7}/cm^3$.