CLAIMS

1. A computing system comprising:

a circuit configured to operate at a plurality of operation frequencies;

a graphics output configured to output image data for display;

a rate determiner configured to determine a frame rate of the graphics output; and

a frequency setter configured to set an operation frequency of the circuit based on the determined frame rate.

2. The computing system of claim 1,

wherein the frequency setter is further configured to increase the operation frequency of the circuit if the determined frame rate is below a pre-determined first threshold.

3. The computing system of claim 1,

wherein the frequency setter is further configured to decrease the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold.

4. The computing system of claims 2,

wherein the frequency setter is further configured to decrease the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold; and

wherein the first threshold is lower than the second threshold.

5. A computing system comprising:

a circuit configured to operate at a plurality of operation frequencies;

a storage configured to store a pre-determined frequency value for a pre-determined application; and

a frequency setter configured to set the pre-determined frequency as an operation frequency of the circuit if the pre-determined application is executed by the computing system.

6. The computing system of claim 5,

wherein the storage is further configured to store an application specific frequency value for each of a plurality of applications.

7. The computing system of claim 6,

wherein the frequency setter is further configured to set, if more than one application is executed, a frequency based on the more than one application specific frequency values of the more than one application as an operation frequency of the circuit.

8. The computing system of claim 6,

wherein the frequency setter is further configured to set, if more than one application is executed, a sum of the more than one application specific frequency values of the more than one application as an operation frequency of the circuit.

9. The computing system of claim 6,

wherein the frequency setter is further configured to set, if more than one application is executed, a maximum value of the more than one application specific frequency values of the more than one application as an operation frequency of the circuit.

10. A computing system, comprising:

a processor configured to operate at a plurality of operation frequencies;

a power source configured to provide power to the processor;

a power measurement circuit configured to measure the power provided by the power source; and

a frequency setter configured to set an operation frequency of the processor based on the measured power.

11. A method for controlling a computing system, the method comprising:
controlling a circuit configured to operate at a plurality of operation frequencies;
controlling a graphics output configured to output image data for display;
determining a frame rate of the graphics output; and
setting an operation frequency of the circuit based on the determined frame rate.

12. The method of claim 11, further comprising:
increasing the operation frequency of the circuit if the determined frame rate is below a pre-determined first threshold.

13. The method of claim 11, further comprising:
decreasing the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold.

14. The method of claim 12, further comprising:
decreasing the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold;
wherein the first threshold is lower than the second threshold.

15. A method for controlling a computing system, the method comprising:
controlling a circuit configured to operate at a plurality of operation frequencies;
storing a pre-determined frequency value for a pre-determined application; and
setting the pre-determined frequency as an operation frequency of the circuit if the pre-determined application is executed by the computing system.

16. The method of claim 15, further comprising:

storing an application specific frequency value for each of a plurality of applications.

17. The method of claim 16,

wherein, if more than one application is executed, a frequency based on the more than one application specific frequency values of the more than one application is set as an operation frequency of the circuit.

18. The method of claim 16,

wherein, if more than one application is executed, a sum of the more than one application specific frequency values of the more than one application is set as an operation frequency of the circuit.

19. The method of claim 16,

wherein, if more than one application is executed, a maximum value of the more than one application specific frequency values of the more than one application is set as an operation frequency of the circuit.

20. A method for controlling a computing system, the method comprising:

controlling a processor configured to operate at a plurality of operation frequencies;

controlling a power source configured to provide power to the processor;

measuring the power provided by the power source; and

setting an operation frequency of the processor based on the measured power.

CLAIMS

1. A computing system comprising:

a circuit configured to operate at a plurality of operation frequencies;

a graphics output configured to output image data for display;

a rate determiner configured to determine a frame rate of the graphics output; and

a frequency setter configured to set an operation frequency of the circuit based on the determined frame rate;

wherein the frequency setter is further configured to increase the operation frequency of the circuit if the determined frame rate is below a pre-determined first threshold.

2. A computing system comprising:

a circuit configured to operate at a plurality of operation frequencies;

a graphics output configured to output image data for display;

a rate determiner configured to determine a frame rate of the graphics output; and

a frequency setter configured to set an operation frequency of the circuit based on the determined frame rate;

wherein the frequency setter is further configured to decrease the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold.

3. The computing system of claim 1,

wherein the frequency setter is further configured to decrease the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold; and

wherein the first threshold is lower than the second threshold.

4. A computing system comprising:

a circuit configured to operate at a plurality of operation frequencies;

a storage configured to store a pre-determined frequency value for a pre-determined application; and

a frequency setter configured to set the pre-determined frequency as an operation frequency of the circuit if the pre-determined application is executed by the computing system.

5. The computing system of claim 4,

wherein the storage is further configured to store an application specific frequency value for each of a plurality of applications.

6. The computing system of claim 5,

wherein the frequency setter is further configured to set, if more than one application is executed, a frequency based on the more than one application specific frequency values of the more than one application as an operation frequency of the circuit.

7. The computing system of claim 5,

wherein the frequency setter is further configured to set, if more than one application is executed, a sum of the more than one application specific frequency values of the more than one application as an operation frequency of the circuit.

8. The computing system of claim 5,

wherein the frequency setter is further configured to set, if more than one application is executed, a maximum value of the more than one application specific frequency values of the more than one application as an operation frequency of the circuit.

9. A method for controlling a computing system, the method comprising:

controlling a circuit configured to operate at a plurality of operation frequencies;

controlling a graphics output configured to output image data for display;

determining a frame rate of the graphics output;

setting an operation frequency of the circuit based on the determined frame rate; and

increasing the operation frequency of the circuit if the determined frame rate is below a pre-determined first threshold.

10. A method for controlling a computing system, the method comprising:

controlling a circuit configured to operate at a plurality of operation frequencies;

controlling a graphics output configured to output image data for display;

determining a frame rate of the graphics output;

setting an operation frequency of the circuit based on the determined frame rate; and

decreasing the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold.

11. The method of claim 9, further comprising:

decreasing the operation frequency of the circuit if the determined frame rate is above a pre-determined second threshold;

wherein the first threshold is lower than the second threshold.

12. A method for controlling a computing system, the method comprising:

controlling a circuit configured to operate at a plurality of operation frequencies;

storing a pre-determined frequency value for a pre-determined application; and

setting the pre-determined frequency as an operation frequency of the circuit if the pre-determined application is executed by the computing system.

13. The method of claim 12, further comprising:

storing an application specific frequency value for each of a plurality of applications.

14. The method of claim 13,

wherein, if more than one application is executed, a frequency based on the more than one application specific frequency values of the more than one application is set as an operation frequency of the circuit.

15. The method of claim 13,

wherein, if more than one application is executed, a sum of the more than one application specific frequency values of the more than one application is set as an operation frequency of the circuit.

16. The method of claim 13,

wherein, if more than one application is executed, a maximum value of the more than one application specific frequency values of the more than one application is set as an operation frequency of the circuit.