LIQUID CRYSTAL DISPLAY CONTROLLER

Abstract of the Invention

A DMA controller 33 retrieves pixel intensity data 60 from memory 16 and provides the pixel intensity data 60 to the frame rate controller 37. The frame rate controller 37 also receives a frame synchronising signal 32 and provides switching data 42 which is stored in a display data buffer 41, 41A, 41B and 41C in accordance with the frame synchronising signal 32 to switch pixels 45 in corresponding display frames 50, 50A, 50B and 50C to have the intensity indicated by the pixel intensity data 60.

(FIG. 2 accompanies this abstract)