

1/25

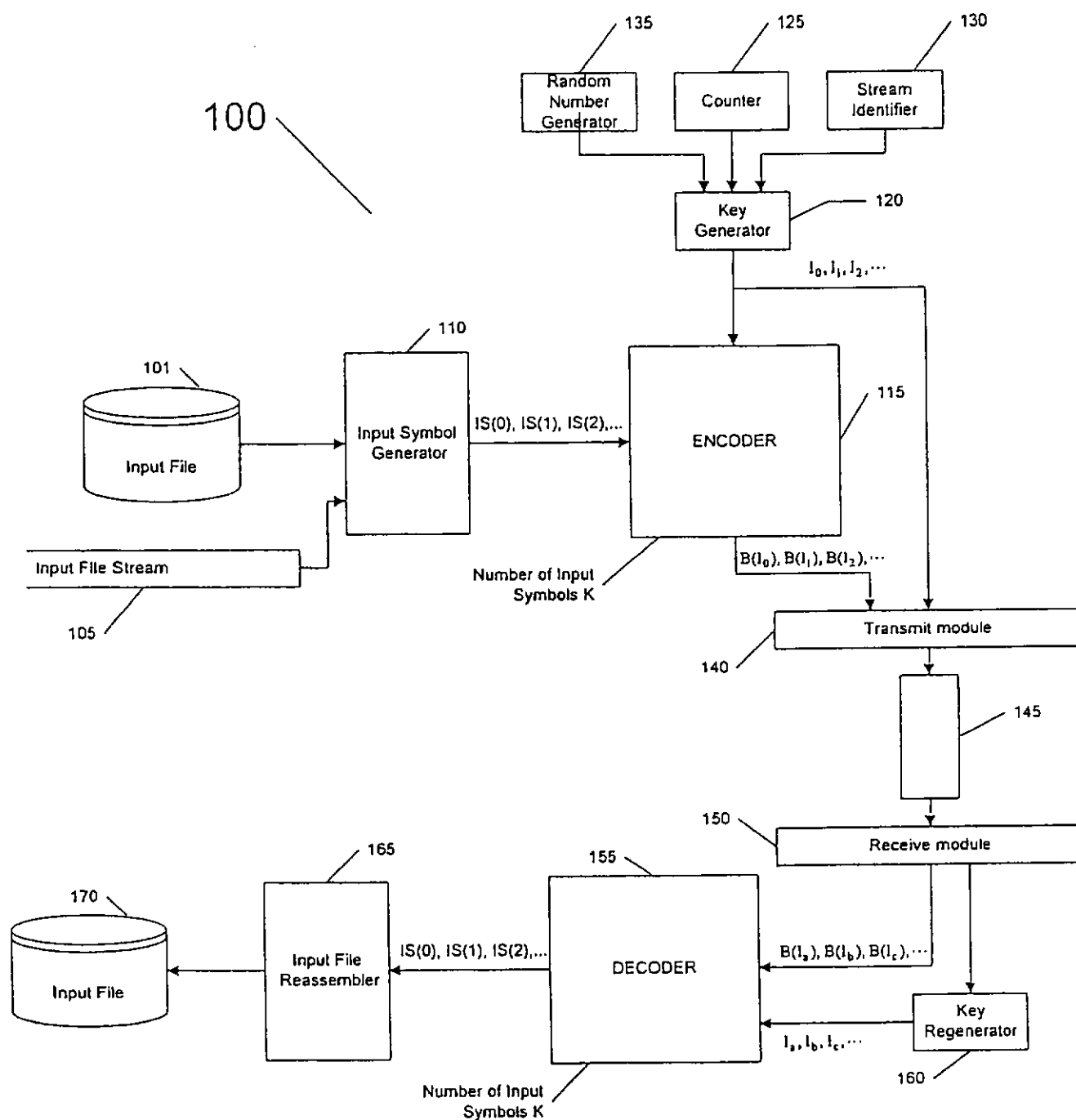


Figure 1

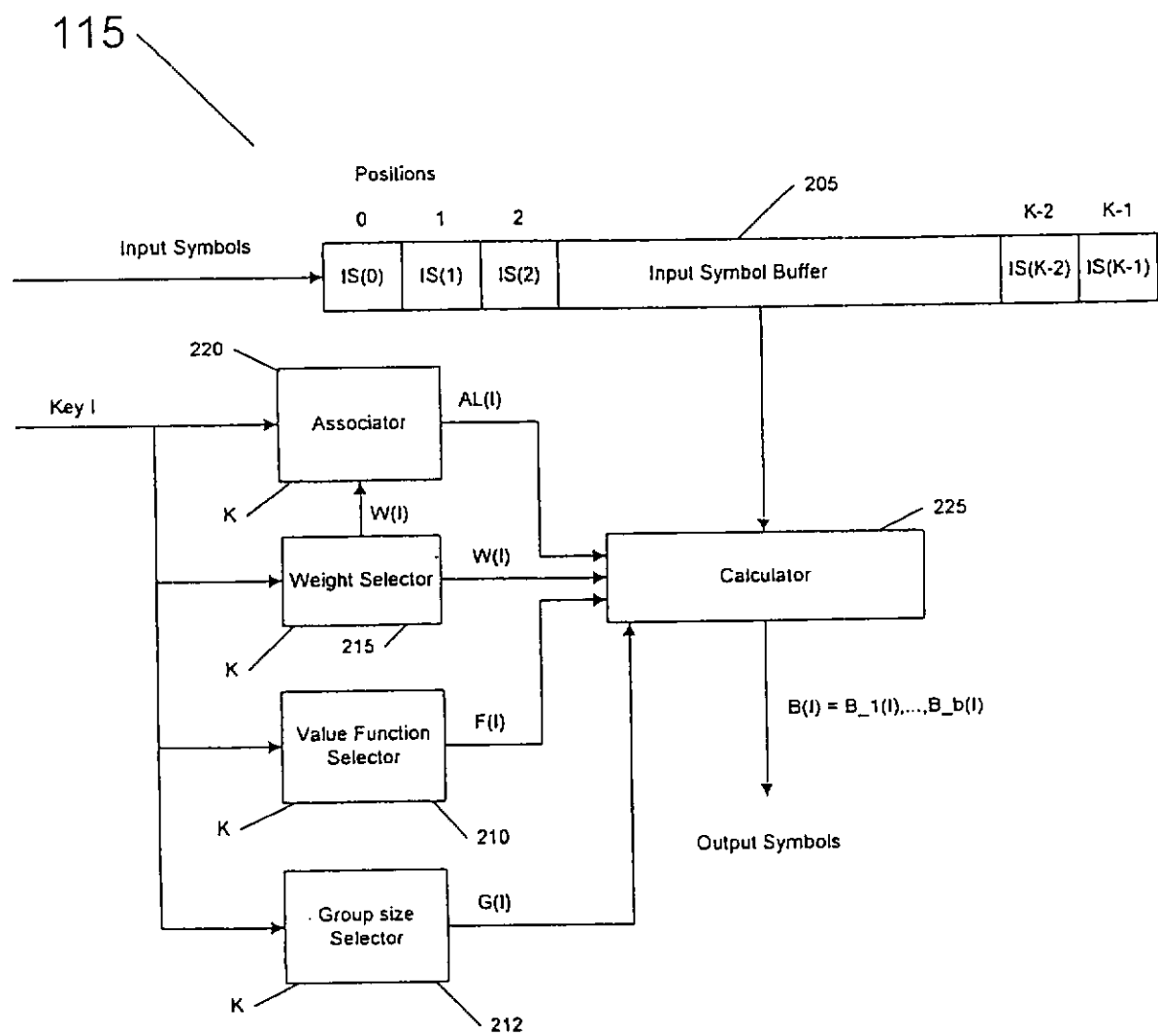


Figure 2

3/25

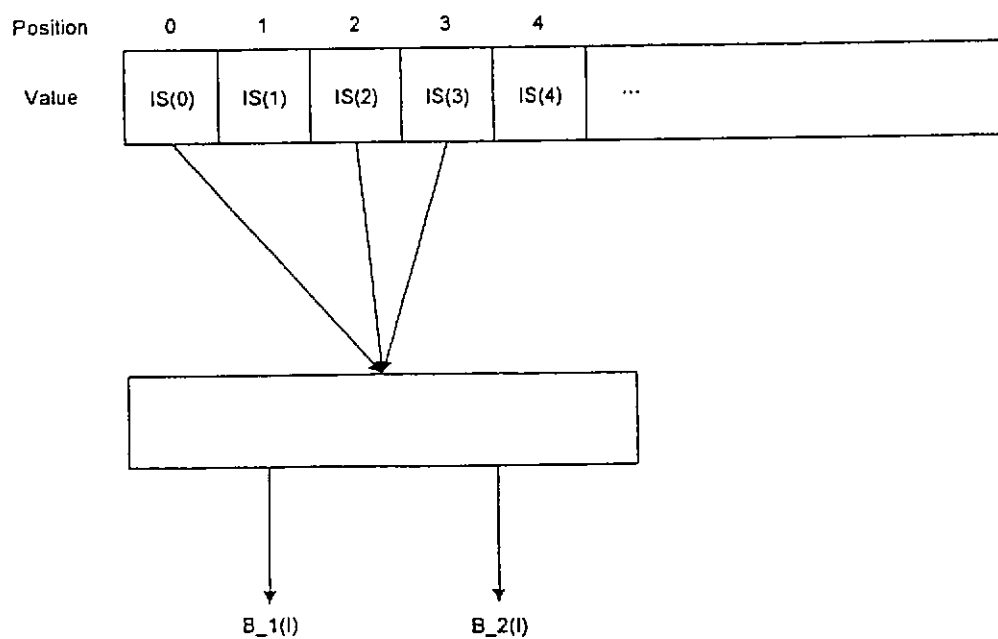


Figure 3

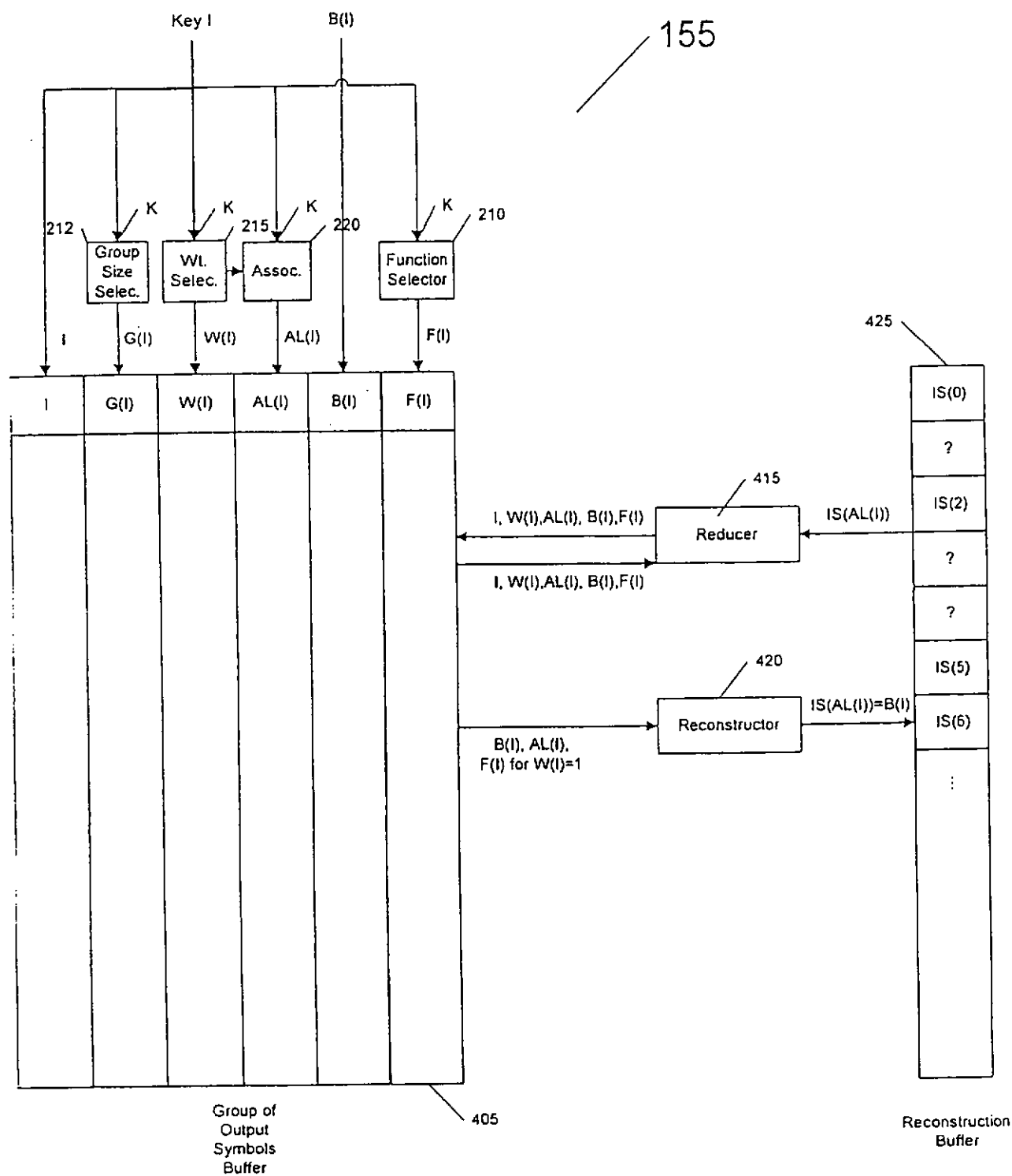


Figure 4

5/25

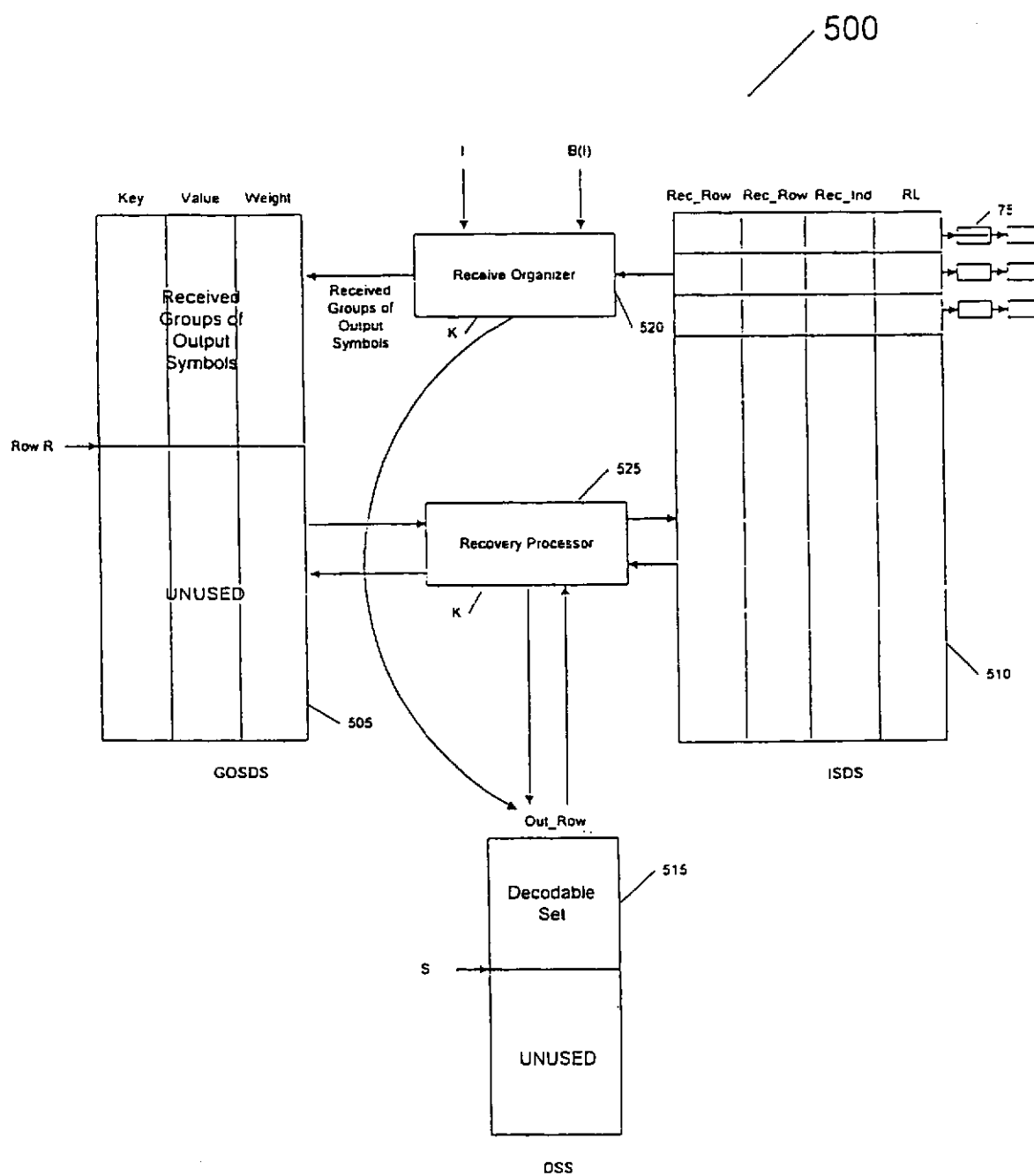


Figure 5

6/25

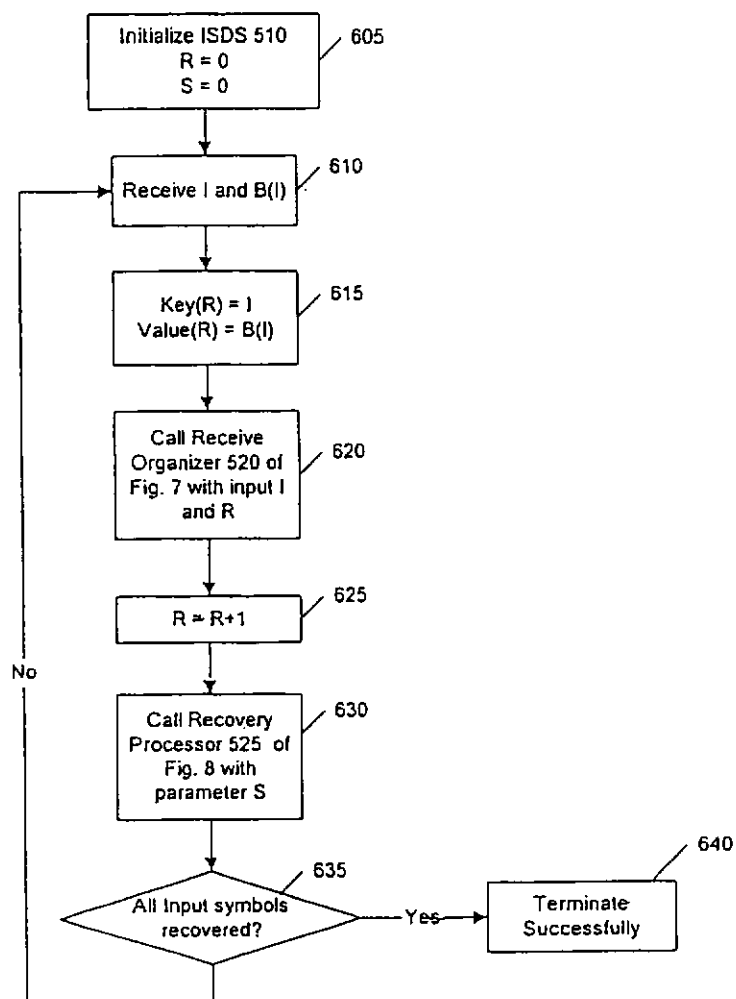
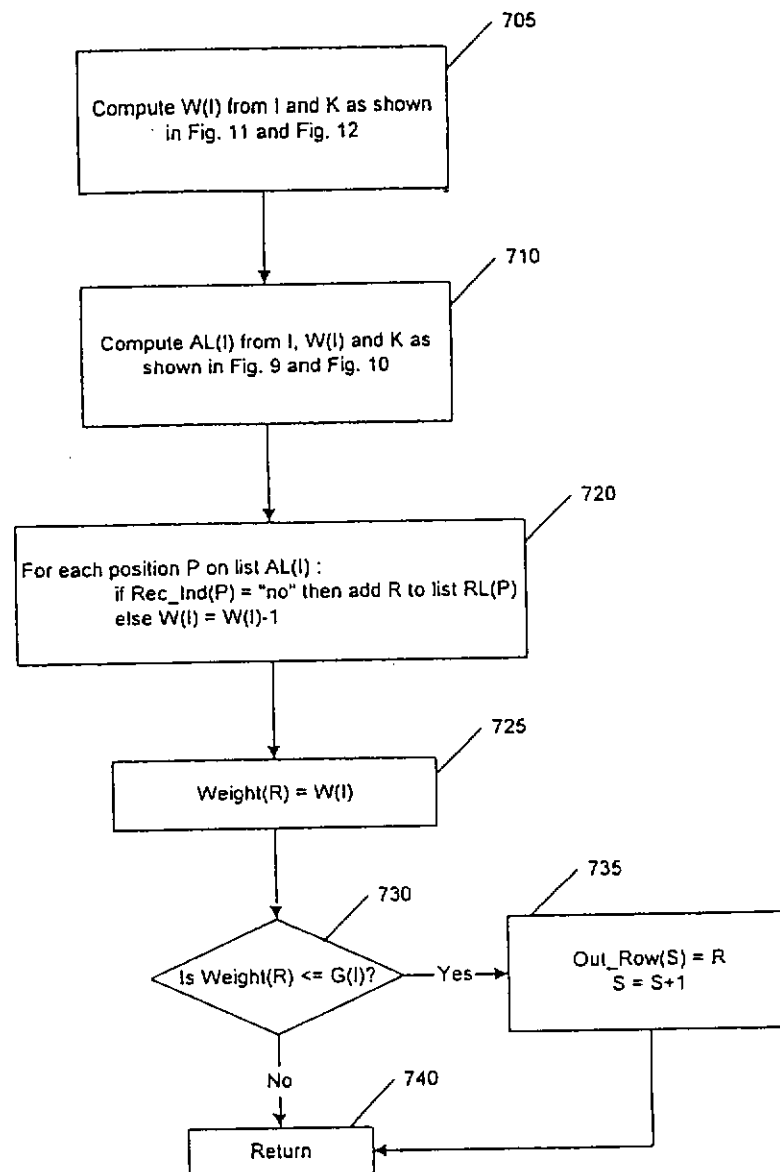


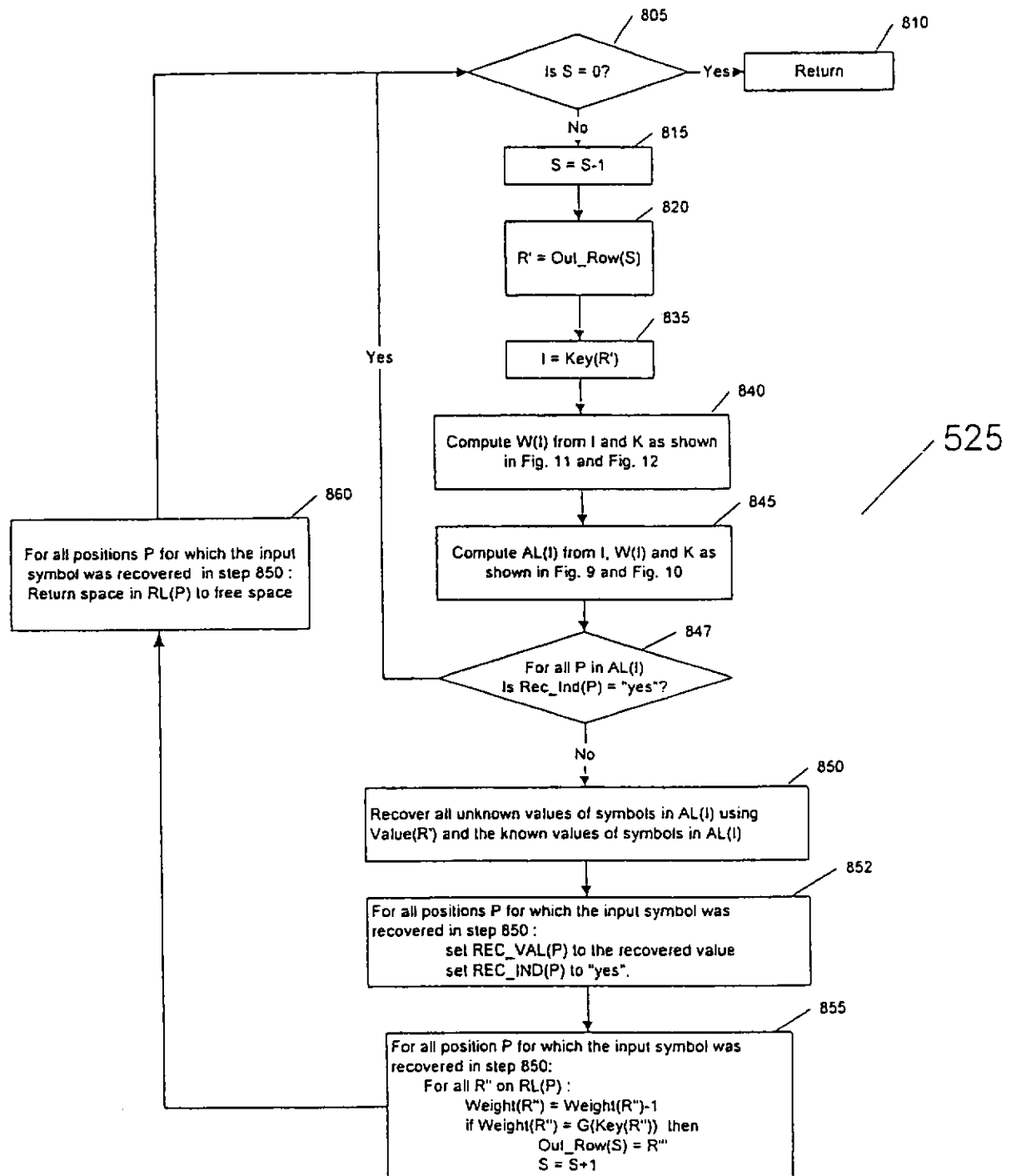
Figure 6

7/25



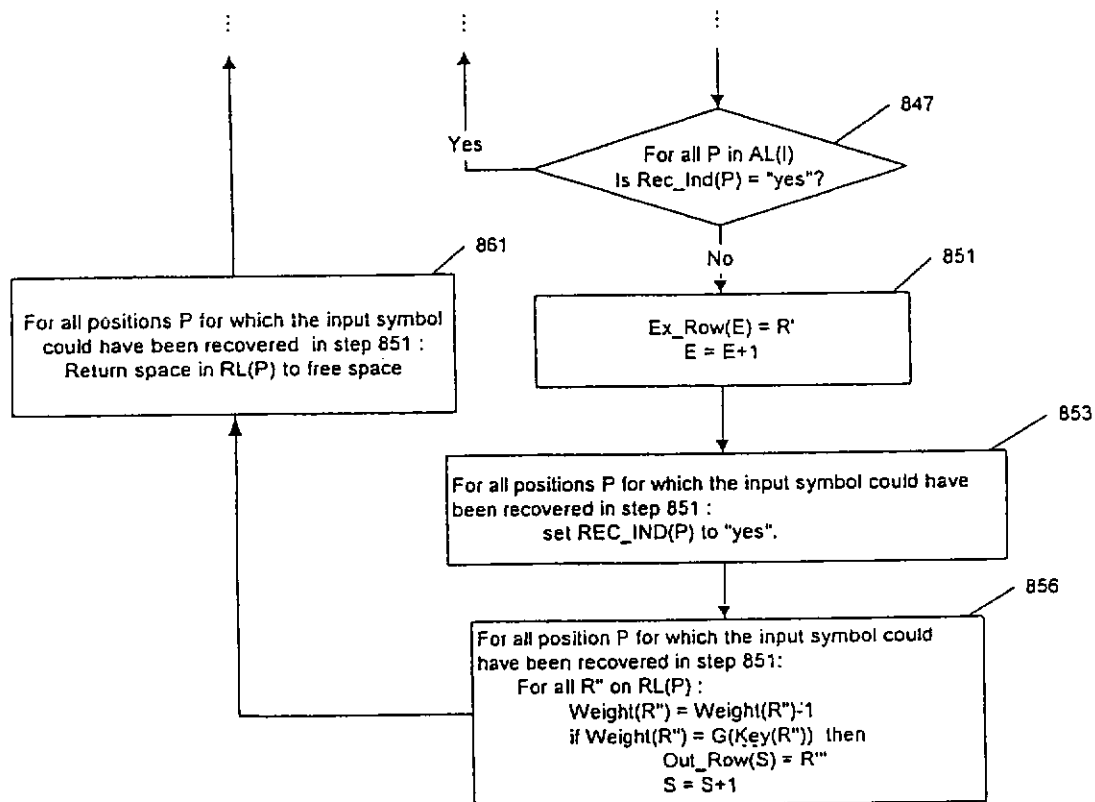
Receive Organizer 520 Flow Chart
Figure 7

8/25



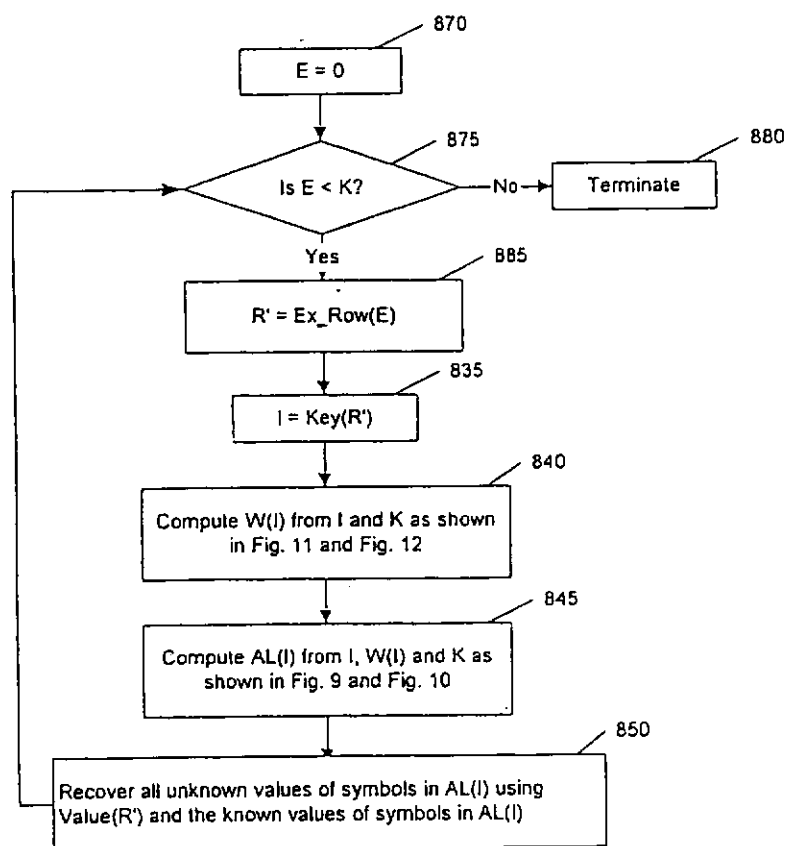
Recovery Processor Flow Chart
Figure 8(a)

9/25



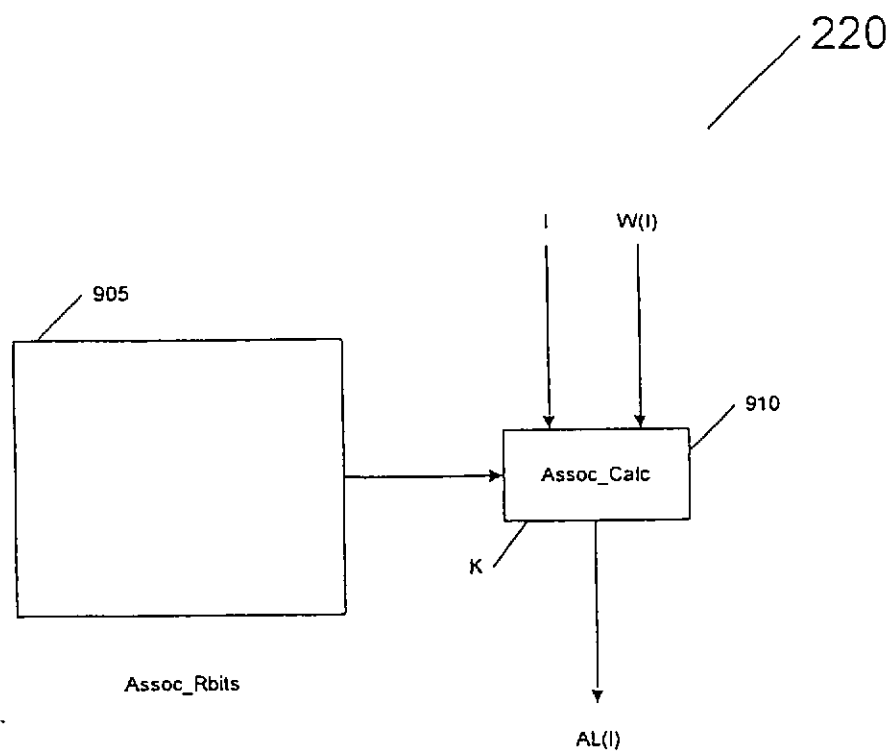
Prepare Execution Schedule
Figure 8(b)

10/25



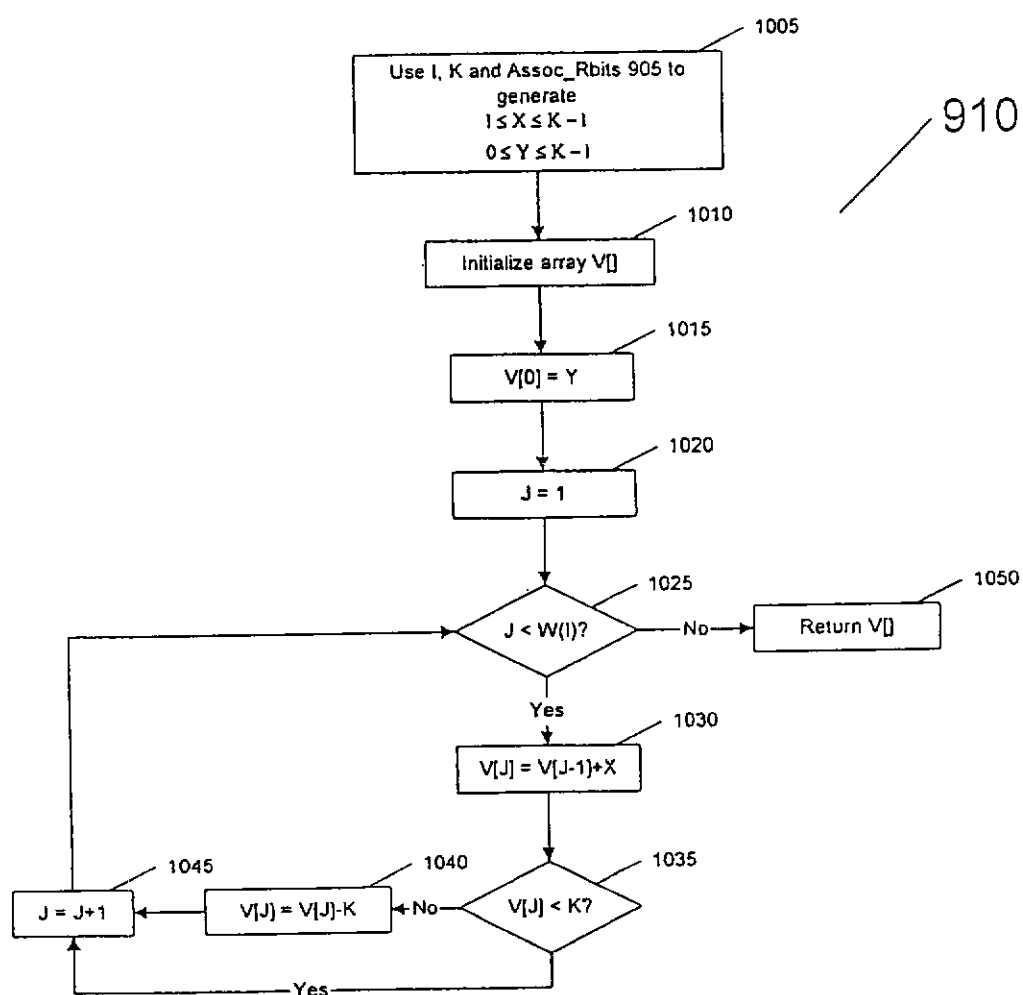
Process Execution Schedule
Figure 8(c)

11/25



Associator
Figure 9

12/25



Assoc_Calc
Figure 10

13/25

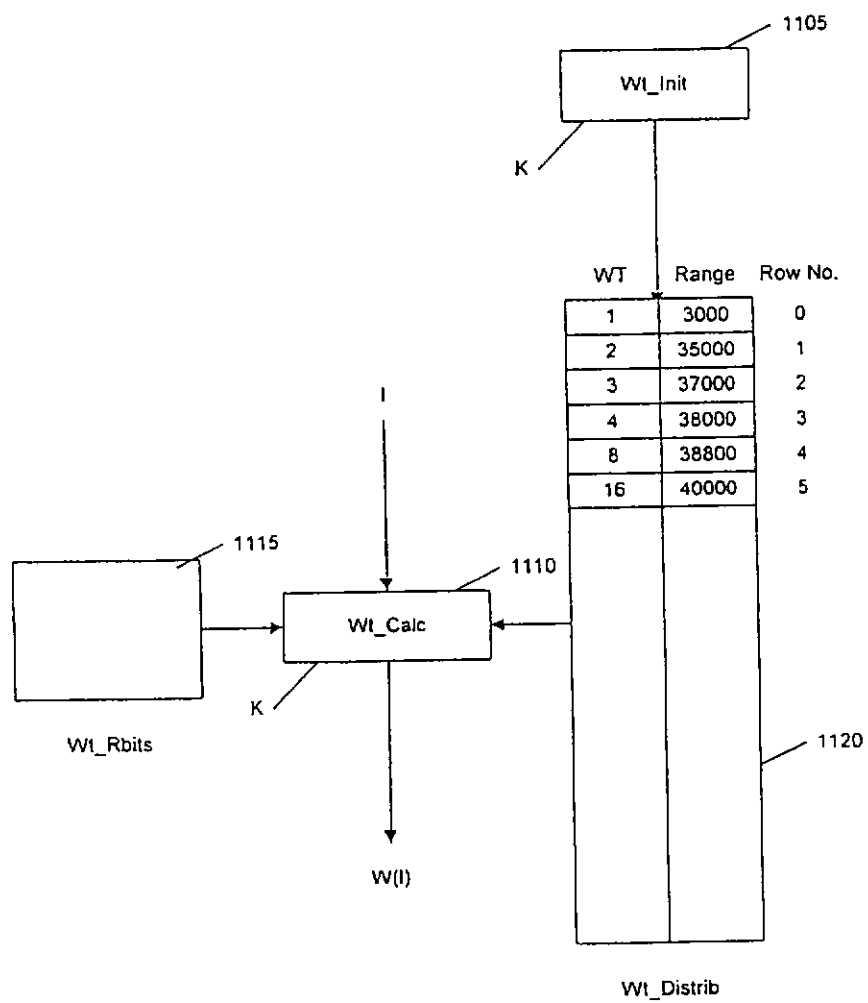
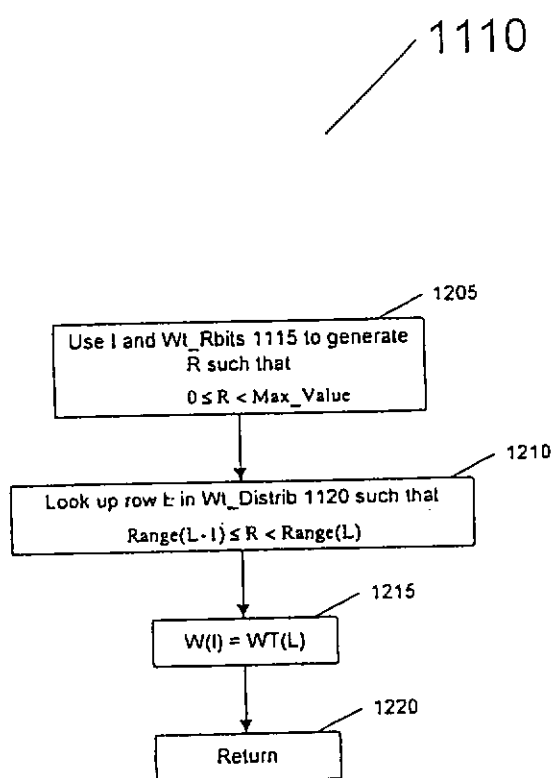


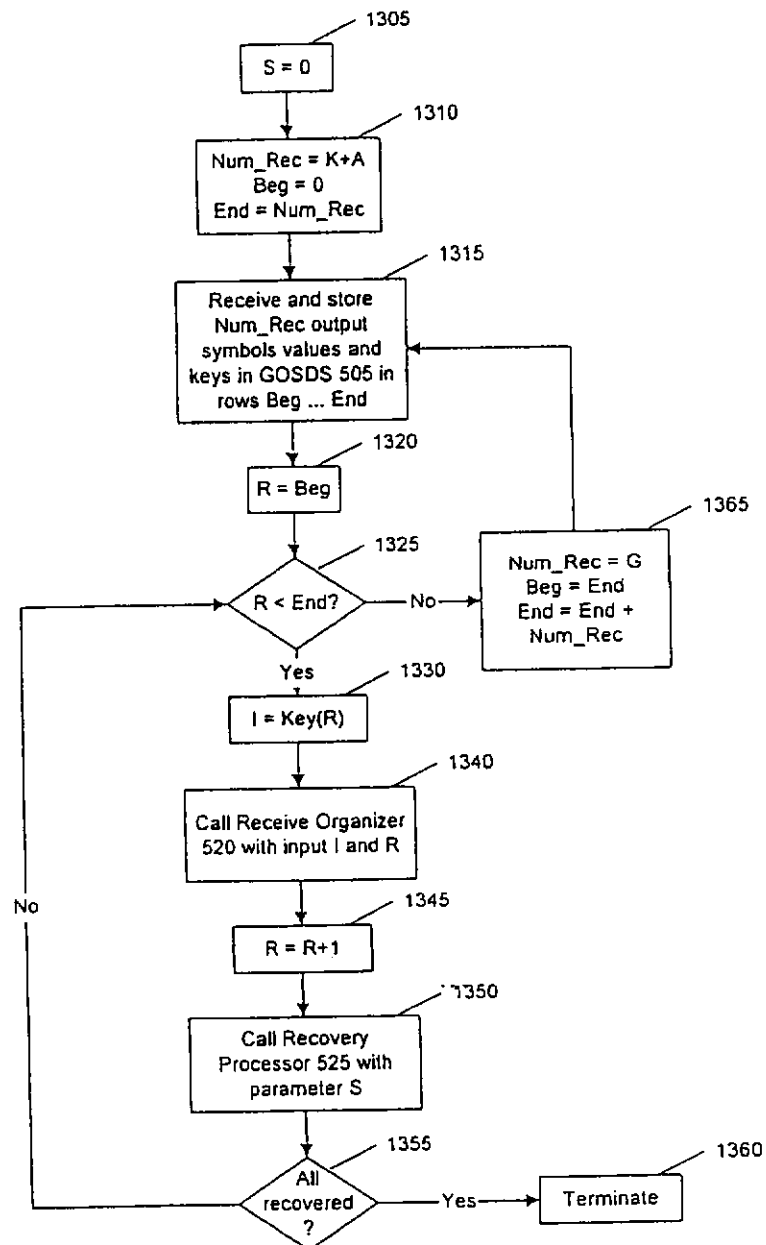
Figure 11

14/25



Wt_Calc
Figure 12

15/25



Lazy Decoder
Figure 13

16/25

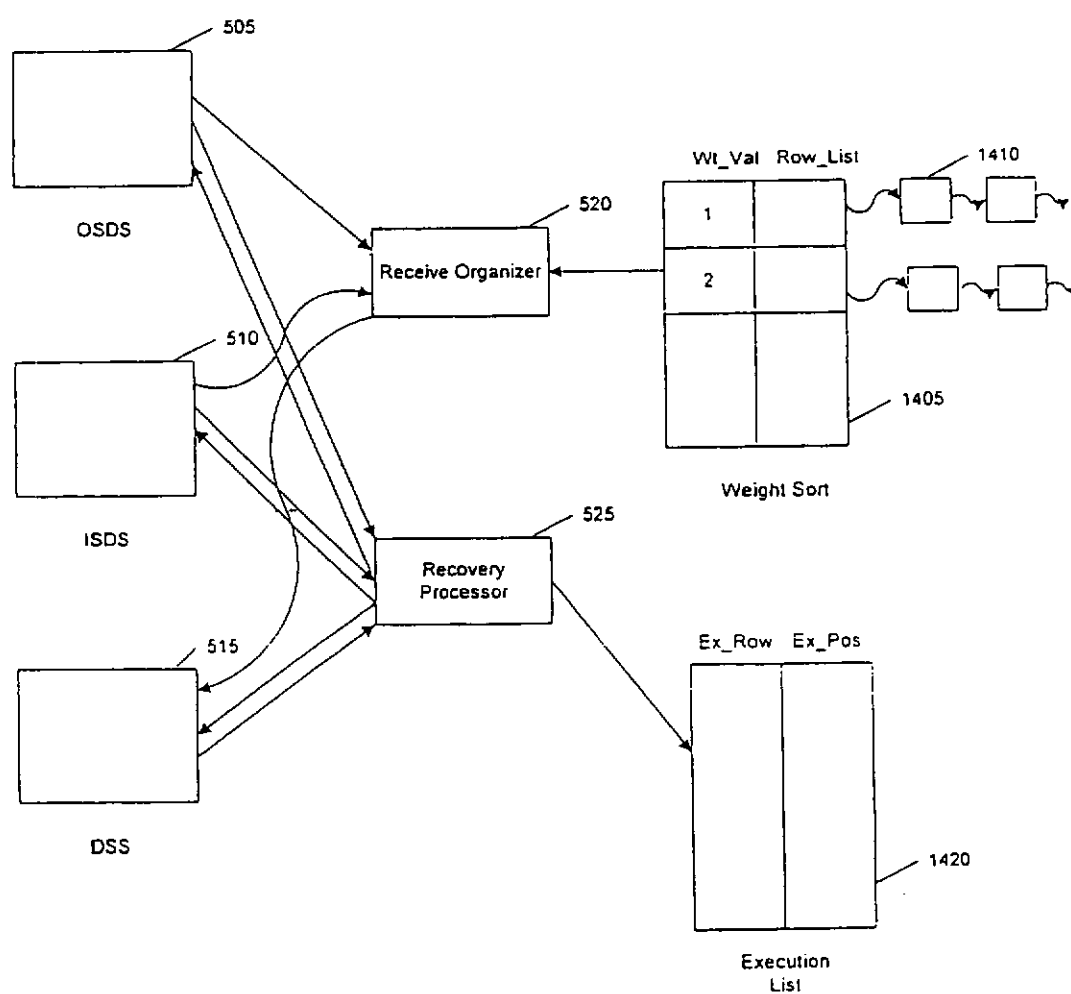


Figure 14

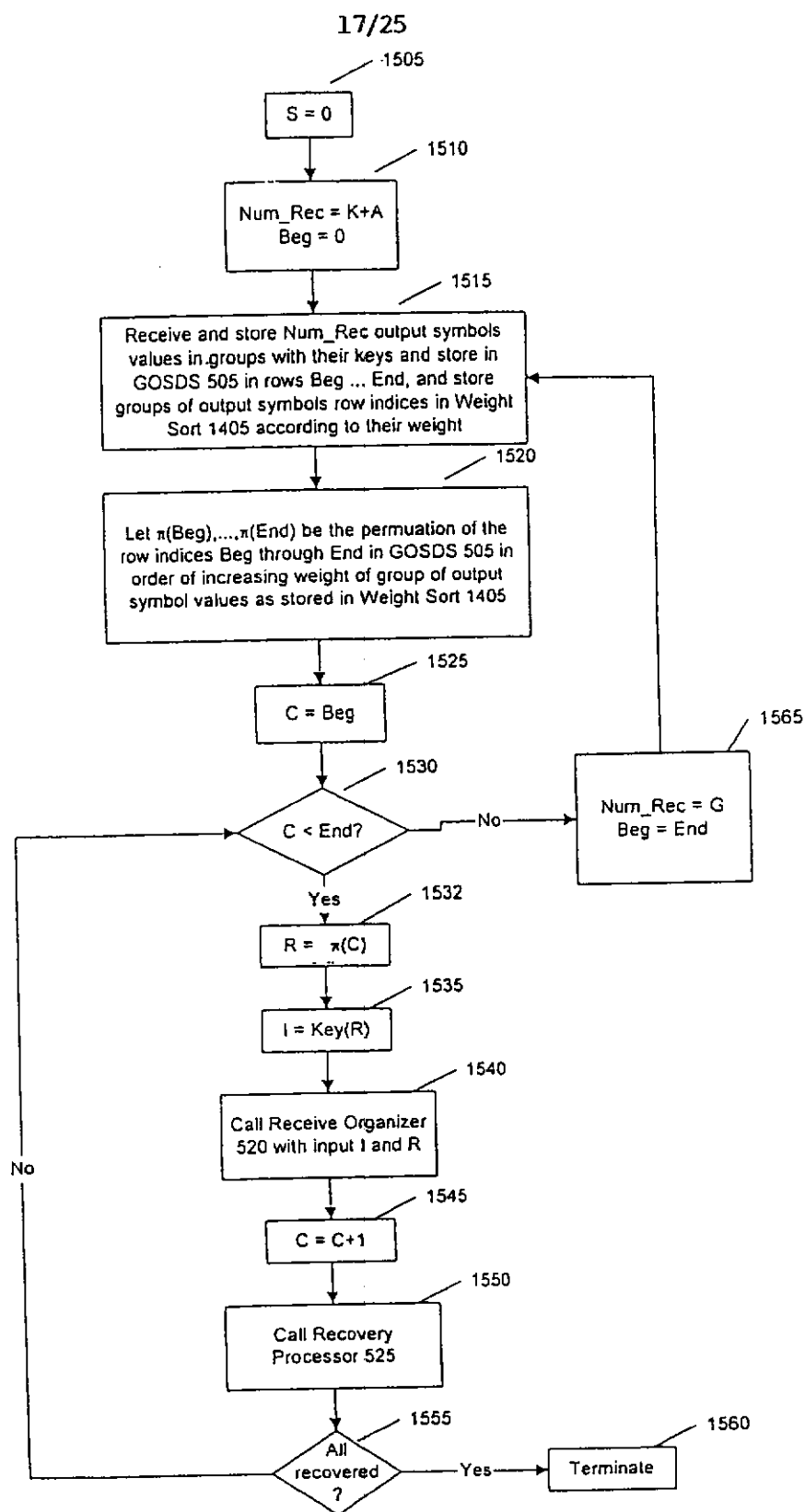


Figure 15

18/25

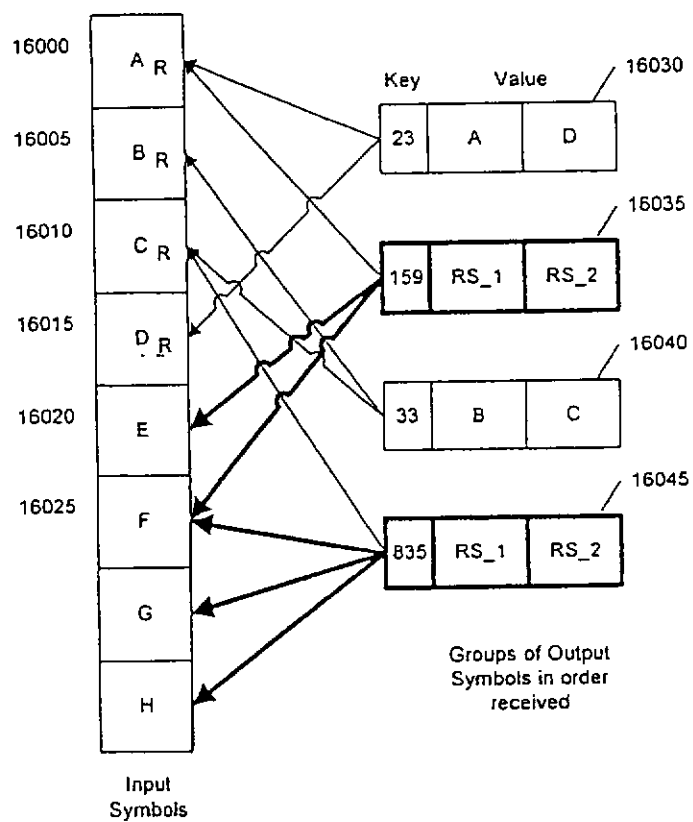


Figure 16

19/25

Rec_Val	Rec_Ind	RL
A	Y	-
B	Y	-
C	Y	-
D	Y	-
-	N	-
-	N	-
-	N	-
-	N	-

ISDS

510

1

1

3

3

3

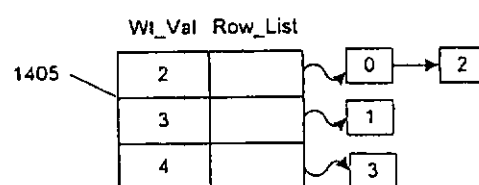
Key	Value	Weight
23	(A,D)	2
159	(RS_1(A,E,F), RS_1(A,E,F))	3
33	(B,C)	2
835	(RS_1(C,F,G,H), RS_1(C,F,G,H))	4

GOSDS

505

Figure 17

20/25

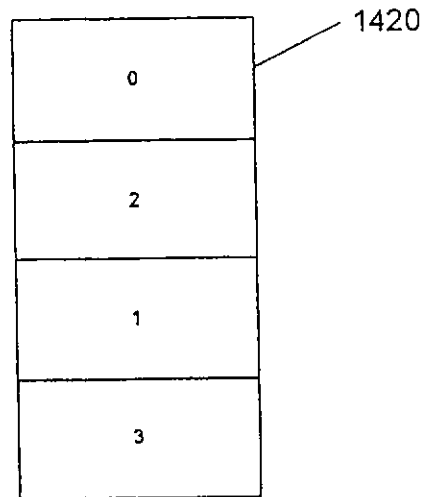


Weight Sort

Figure 18

21/25

Ex_Row



0
2
1
3

Execution List

Figure 19

22/25

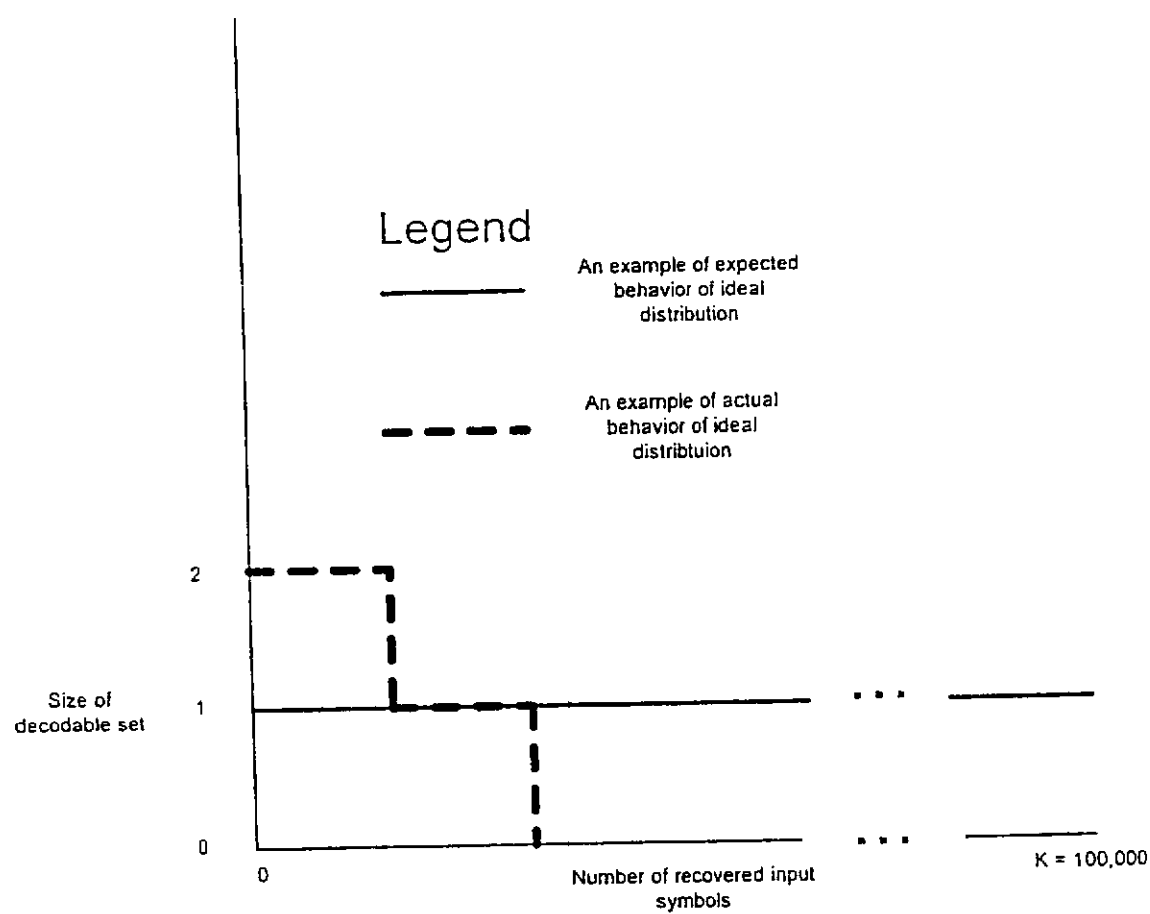


Figure 20

23/25

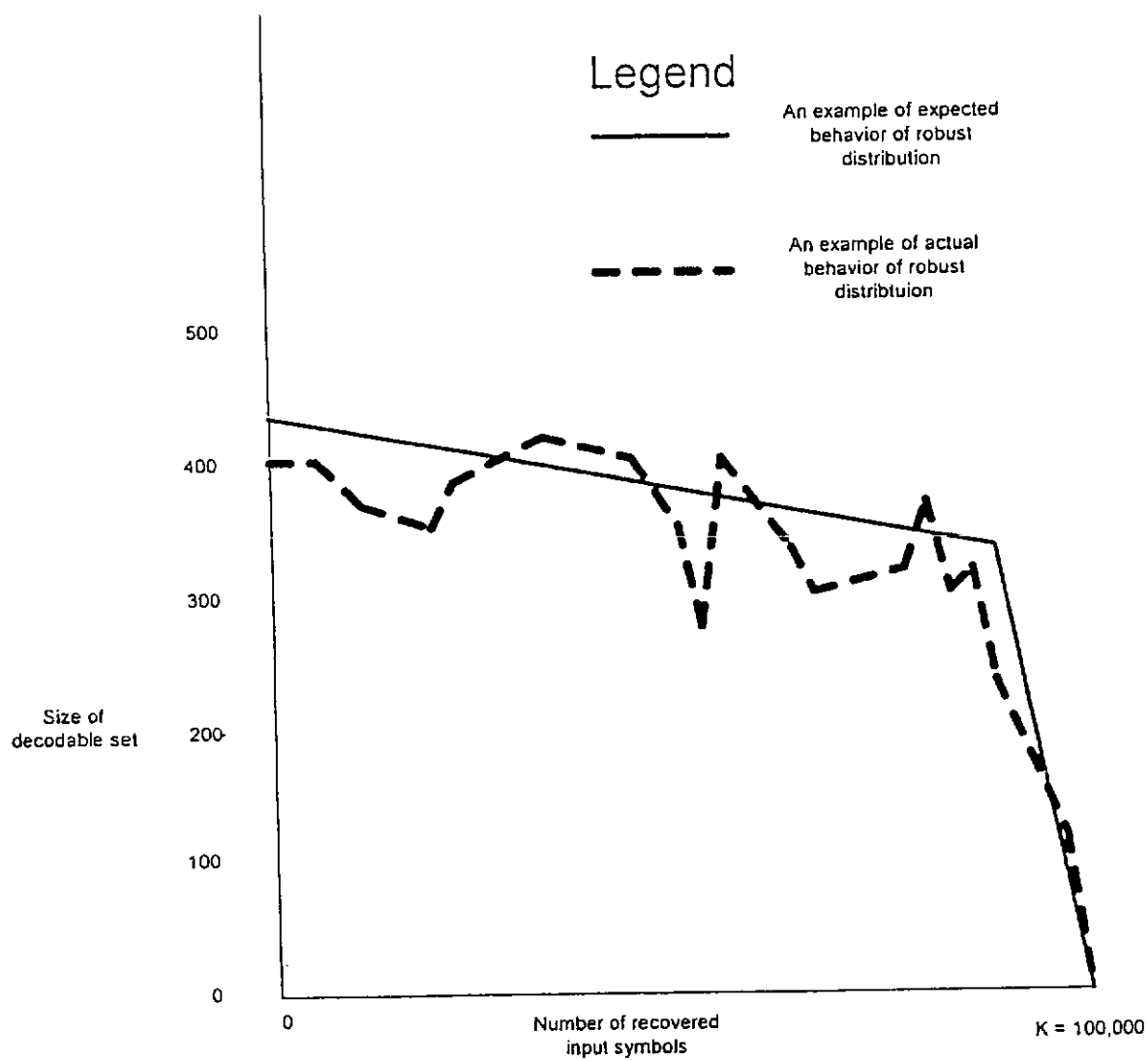


Figure 21

24/25

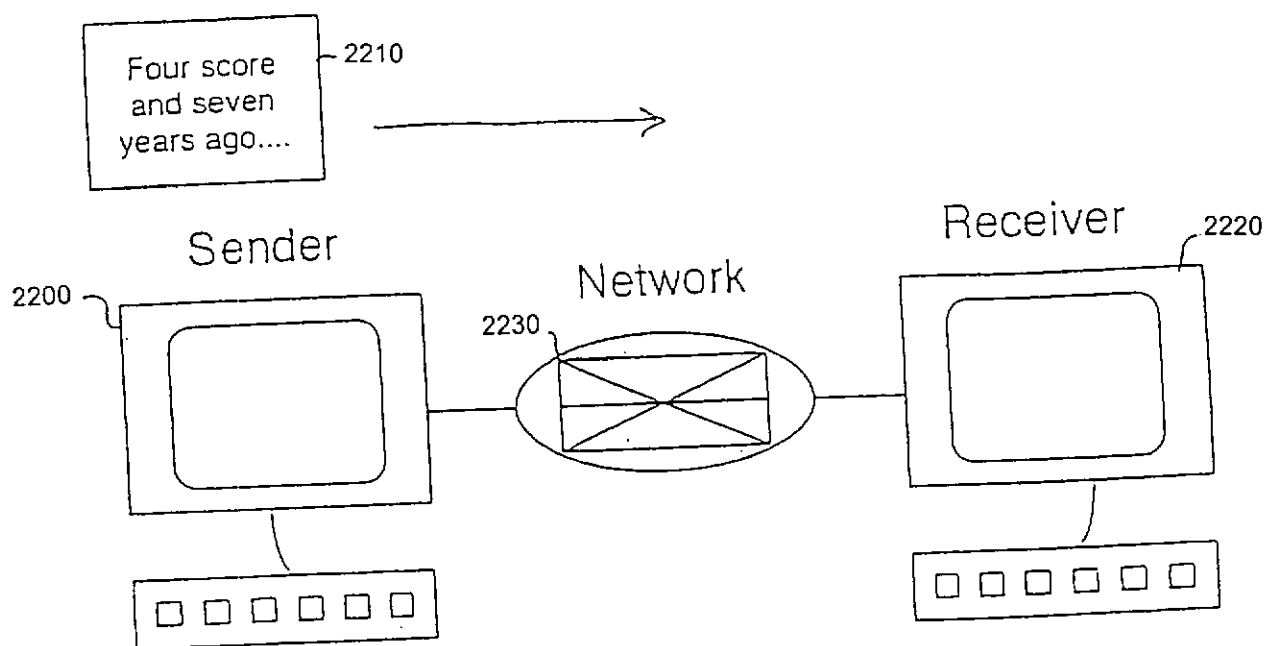


Fig. 22

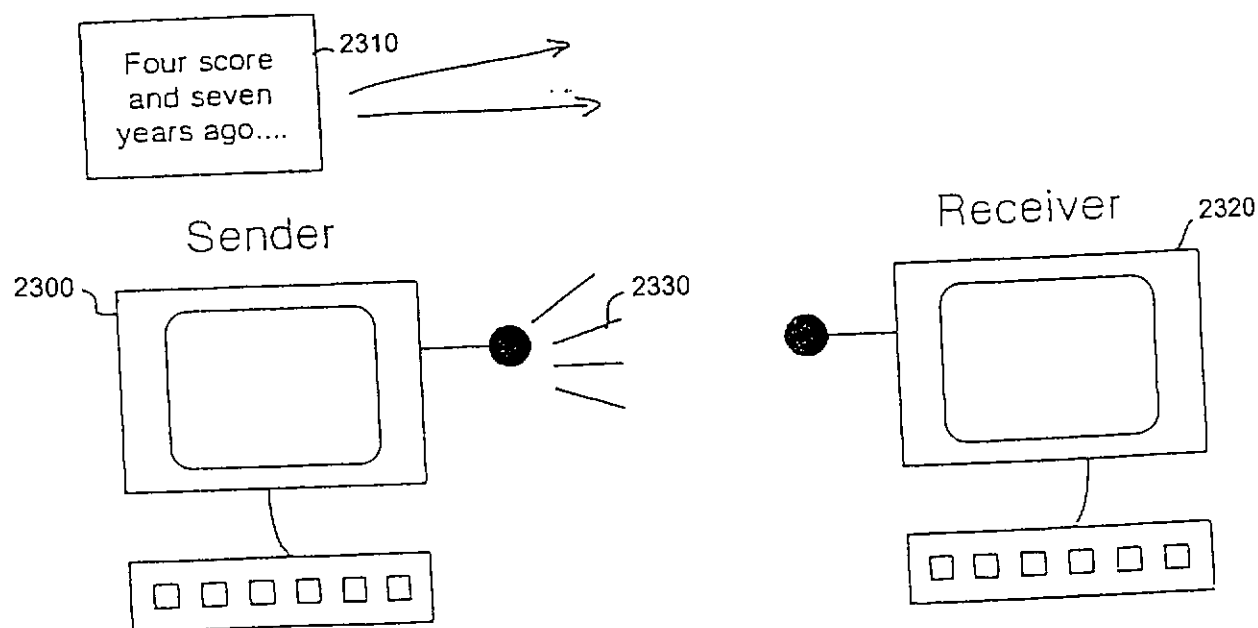


Fig. 23

25/25

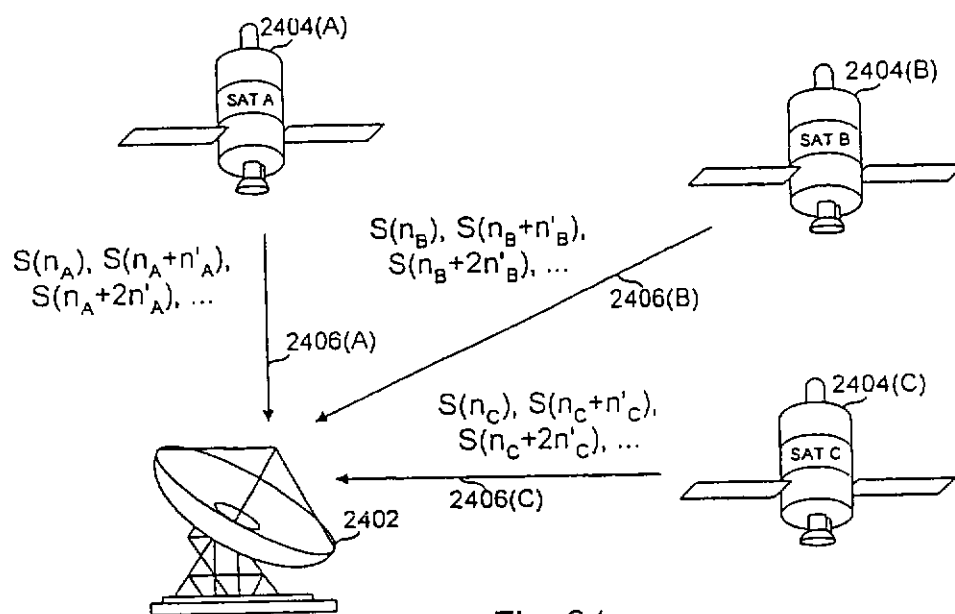


Fig. 24

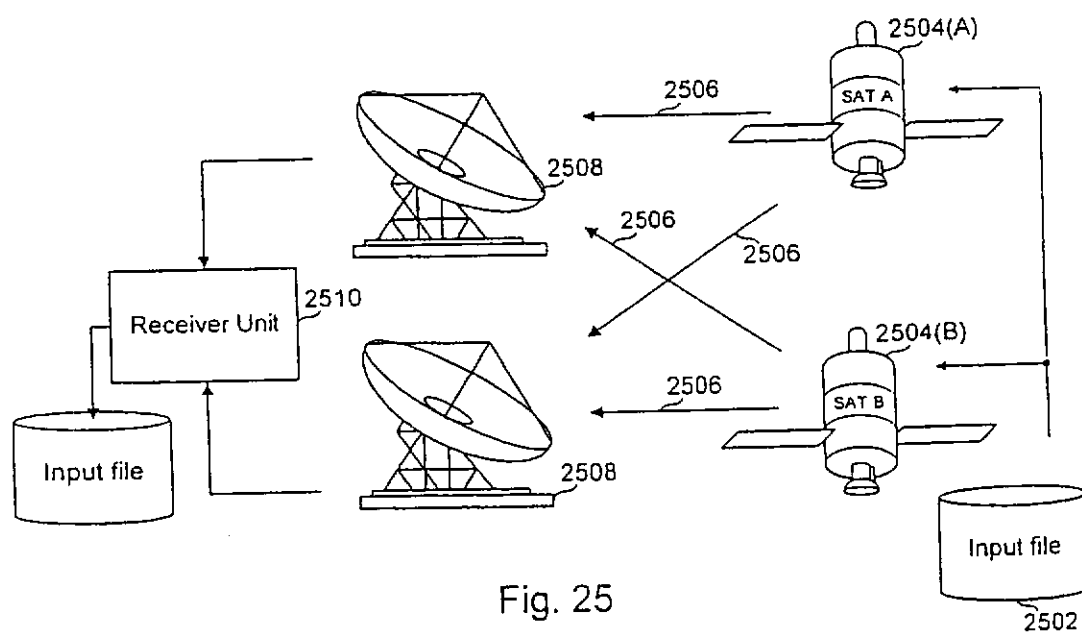


Fig. 25