

ABSTRACT

An integrated circuit device including a first cell block on a substrate that includes a first cell area, a first dummy cell area surrounding the first cell area in a plan view, the first dummy cell area including first active regions and second active regions in an outer periphery of the first dummy cell area, the first active regions each having a first size and the second active regions each having a second size larger than the first size, first and second word lines extending in a first direction and alternating with each other in a second direction, each of the first word lines including a first landing area extending between a second active region and a first active region, the first active region being near and apart from the second active region in the second direction, and first word line contacts on the first landing area may be provided.

FIG. 3