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(54) Title: VARIABLE STAGE CHARGE PUMP

(57) Abstract

A variable state charge pump (100) for a flash memory device is described. The variable state charge pump (100) includes a first charge pump (110) and a second charge pump (120). A first switch (130) couples an output of the first charge pump to the input of the second charge pump. A second switch (131) couples an input of the first charge pump to the input of the second charge pump. These first and second charge pumps are series-coupled to a common output node (150) when a first switch is in a first position and the second switch is in a second position, and wherein the first and second charge pumps are parallel-coupled to the common output node (150) when the first switch is in the second position and the second switch is in the first position.

