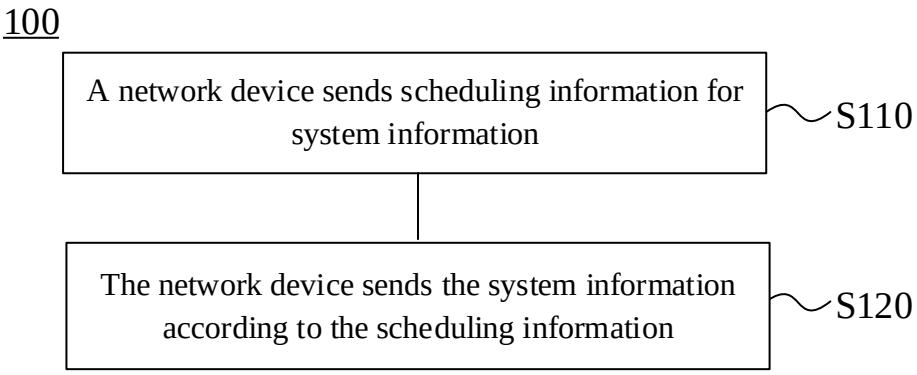

FIG. 1

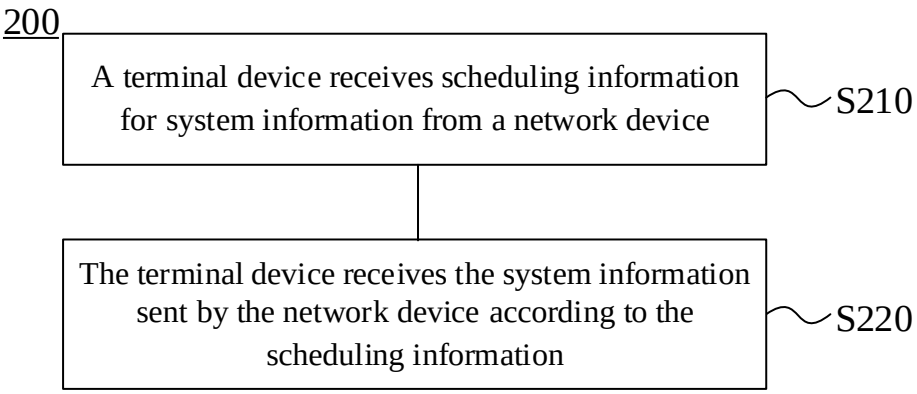

FIG. 2

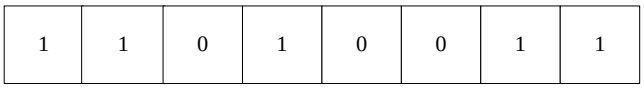

FIG. 3

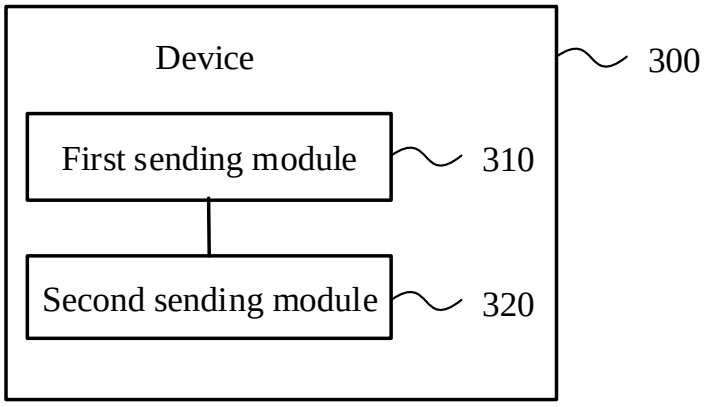

FIG. 4

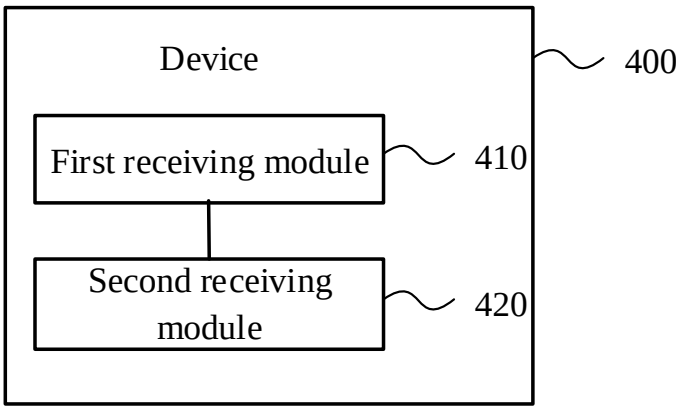

FIG. 5

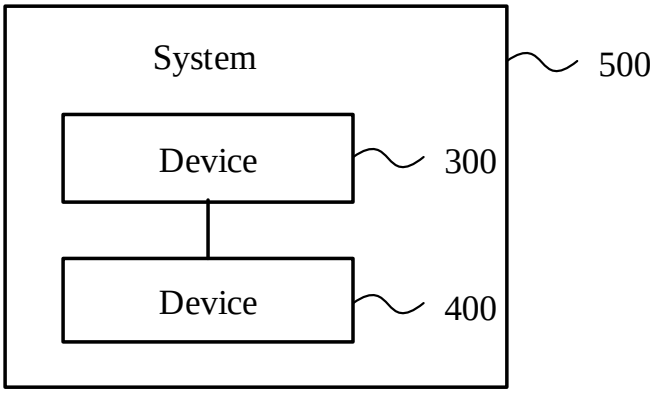

FIG. 6

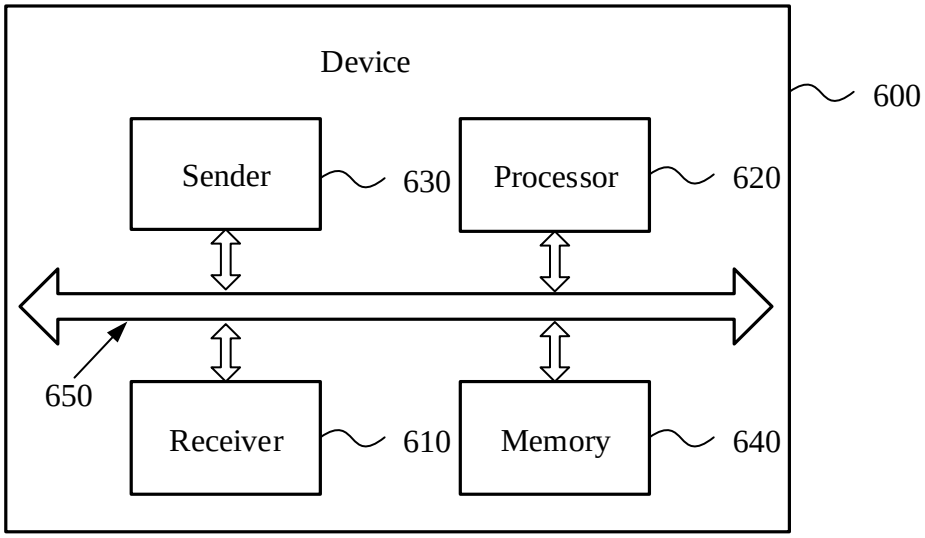

FIG. 7

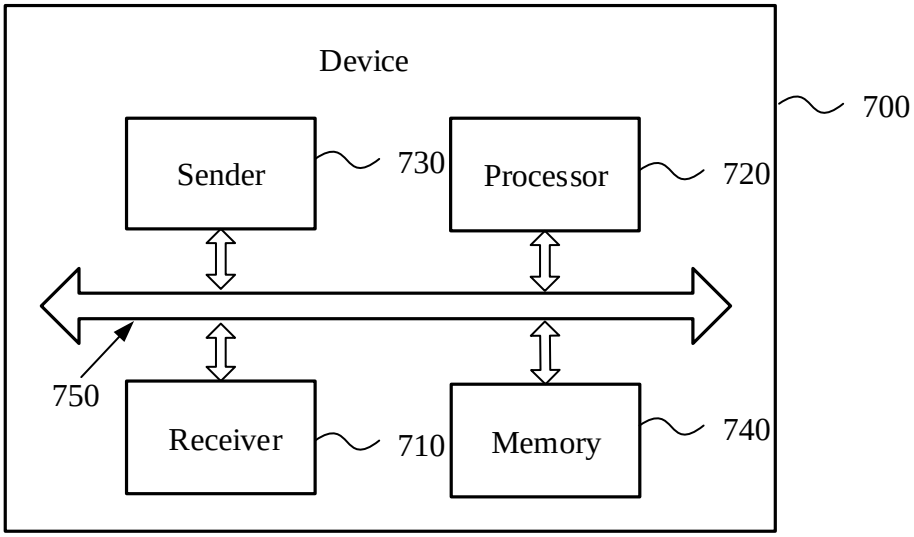
Device
700
Sender
730
Processor
720
750
Receiver
710
Memory
740

FIG. 8