~~**WHAT IS CLAIMED IS:**~~

CLAIMS

1.~~——~~ A method of coding video data, the method comprising:

determining a first reference block in a first reference picture for a first block of a current picture;

determining whether the first reference picture is a same size as the current picture;

~~———~~ based on the first reference picture being a different size than the current picture, coding the first block ~~with a first inter-coding tool of a first set of inter-coding tools~~, wherein a first particular ~~tool of a plurality of~~ inter-coding ~~tools~~tool is disabled~~;~~

for the first block; determining a second reference block in a second reference picture for a second block of the current picture;

determining whether the second reference picture is a same size as the current picture; ~~and~~

~~———~~based on the second reference picture being the same size as the current picture, coding the second block ~~with a second inter-coding tool of a second set of inter-coding tools~~, wherein the first particular inter-coding tool is enabled~~.~~

~~2. The method of claim 1, further comprising:~~

for the second block; determining a third reference block in a third reference picture for a third block of the current picture~~;~~

~~determining a fourth reference block in a fourth reference picture for~~ , wherein the third block ~~of the current picture;~~

is a bi-predicted block; determining whether the third reference picture is a same size as the current picture;

and based on the third reference picture being a different size than the current picture, bi-predictive coding the third block ~~with a third inter-coding tool of a third set of inter-coding tools~~, wherein a second particular ~~tool of the plurality of~~ inter-coding ~~tools~~tool is disabled for the third ~~reference~~ block~~;~~

~~determining whether the fourth reference picture is a same size as the current picture; and~~

based on the fourth reference picture being the same size as the current picture, coding the third block with a fourth inter-coding tool of a fourth set of inter-coding tools, wherein the second particular tool is enabled for the fourth reference block.

3. 2. The method of claim 1, further comprising:

determining a third reference block in a third reference picture for a third block;

determining a fourth reference block in a fourth reference picture for the third block;

determining whether the third reference picture is a same size as the fourth reference picture;

based on third reference picture being the same size as the fourth reference picture, coding the third block with a third inter-coding tool of a third set of inter-coding tools, wherein the first particular tool is enabled.

4. The method of claim 1, further comprising:

determining whether the disabled first particular inter-coding tool uses a result of interpolation of the first reference block and does not directly use the first reference block;

based on the disabled first particular inter-coding tool using a result of interpolation of the first reference block and not directly using the first reference block, enabling the disabled first particular tool; and

selecting the first particular tool as the first inter-coding tool, wherein coding for the first block with the first inter-coding tool comprises; and coding the first block with the first particular inter-coding tool.

5. 3. The method of claim 1, wherein the first particular inter-coding tool includes one of temporal motion vector prediction (TMVP), decode side motion vector refinement (DMVR), andor bi-directional optical flow (BDOF) and wherein the second particular inter-coding tool includes one of TMVP, DMVR, or bi-directional optical flow (BDOF).

6. 4. The method of claim 1, wherein the method is performed based on a determination that reference picture resampling (RPR) is enabled.

7. 5. The method of claim 13, wherein size comprises a resolution.

8. 6. A device for coding video data, the device comprising:

a memory configured to store video data; and

one or more processors implemented in circuitry and coupled to the memory, the one or more processors being configured to:

determine a first reference block in a first reference picture for a first block of a current picture;

determine whether the first reference picture is a same size as the current picture;

based on the first reference picture being a different size than the current picture, code the first block with a first inter-coding tool of a first set of inter-coding tools, wherein a first particular tool of a plurality of inter-coding toolstool is disabled;

for the first block; determine a second reference block in a second reference picture for a second block of the current picture;

determine whether the second reference picture is a same size as the current picture; and

based on the second reference picture being the same size as the current picture, code the second block with a second inter-coding tool of a second set of inter-coding tools, wherein the first particular inter-coding tool is enabled.

9. The device of claim 8, wherein for the one or more processors are further configured to:

second block; determine a third reference block in a third reference picture for a third block of the current picture;

determine a fourth reference block in a fourth reference picture for , wherein the third block of the current picture;

is a bi-predicted block; determine whether the third reference picture is a same size as the current picture;

and based on the third reference picture being a different size than the current picture, code bi-predictive coding the third block with a third inter-coding tool of a third set of inter-coding tools, wherein wherein a second particular tool of the plurality of inter-coding toolstool is disabled for the third reference block;

determine whether the fourth reference picture is a same size as the current picture; and

based on the fourth reference picture being the same size as the current picture, code the third block with a fourth inter-coding tool of a fourth set of inter-coding tools, wherein the second particular tool is enabled for the fourth reference block.

10. 7. The device of claim 86, wherein the one or more processors are further configured to:

determine a third reference block in a third reference picture for a third block;

determine a fourth reference block in a fourth reference picture for the third block;

determine whether the third reference picture is a same size as the fourth reference picture;

based on third reference picture being the same size as the fourth reference picture, code the third block with a third inter-coding tool of a third set of inter-coding tools, wherein the first particular tool is enabled.

11. The device of claim 8, wherein the one or more processors are further configured to:

determine whether the disabled first particular inter-coding tool uses a result of interpolation of the first reference block and does not directly use the first reference block;

based on the disabled first particular inter-coding tool using a result of interpolation of the first reference block and not directly using the first reference block, enable the disabled first particular toolinter-coding tool for the first block; and

select the first particular as the first inter-coding tool, wherein the one or more processors code the first block by coding the first block with the first particular inter-coding tool.

12. 8. The device of claim 8, 6, wherein the first particular inter-coding tool includes one of temporal motion vector prediction (TMVP), decode side motion vector refinement (DMVR), or bi-directional optical flow (BDOF) and wherein the firstsecond particular tool includes one of TMVP, DMVR, andor BDOF.

13. 9. The device of claim 86, wherein the one or more processors are further configured to determine that reference picture resampling (RPR) is enabled.

~~14.~~ 10. The device of claim 8, wherein size comprises a resolution.

~~15.~~ 11. The device of claim ~~8~~6, comprising a wireless phone.

~~16.~~ 12. A non-transitory computer-readable medium having stored thereon instructions that, when executed, cause one or more processors to:

determine a first reference block in a first reference picture for a first block of a current picture;

determine whether the first reference picture is a same size as the current picture;

based on the first reference picture being a different size than the current picture, code the first block ~~with a first inter-coding tool of a first set of inter-coding tools~~, wherein a first particular ~~tool of a plurality of~~ inter-coding ~~tools~~tool is disabled~~;~~

for the first block; determine a second reference block in a second reference picture for a second block of the current picture;

determine whether the second reference picture is a same size as the current picture; ~~and~~

based on the second reference picture being the same size as the current picture, code the second block ~~with a second inter-coding tool of a second set of inter-coding tools~~, wherein the first particular inter-coding tool is enabled~~.~~

~~17. The non-transitory computer-readable medium of claim 16, wherein the instructions, when executed, further cause~~ for the ~~one or more processors to:~~

second block; determine a third reference block in a third reference picture for a third block of the current picture~~;~~

~~determine a fourth reference block in a fourth reference picture for~~ , wherein the third block ~~of the current picture;~~

is a bi-predicted block; determine whether the third reference picture is a same size as the current picture;

and based on the third reference picture being a different size than the current picture, ~~code~~ bi-predictive coding the third block ~~with a third inter-coding tool of a third set of inter-coding tools, wherein~~ wherein a second particular ~~tool of the plurality of~~ inter-coding ~~tools~~tool is disabled for the third ~~reference~~ block~~;~~

~~determine whether the fourth reference picture is a same size as the current picture; and~~

~~based on the fourth reference picture being the same size as the current picture, code the third block with a fourth inter-coding tool of a fourth set of inter-coding tools, wherein the second particular tool is enabled for the fourth reference block~~.

~~18.~~ 13. The non-transitory computer-readable medium of claim ~~16~~12, wherein the instructions, when executed, further cause the one or more processors to:

~~determine a third reference block in a third reference picture for a third block;~~

~~determine a fourth reference block in a fourth reference picture for the third block;~~

~~determine whether the third reference picture is a same size as the fourth reference picture;~~

~~based on third reference picture being the same size as the fourth reference picture, code the third block with a third inter-coding tool of a third set of inter-coding tools, wherein the first particular tool is enabled.~~

~~19. The non-transitory computer-readable medium of claim 16, wherein the instructions, when executed, further cause the one or more processors to:~~

determine whether the disabled first particular inter-coding tool uses a result of interpolation of the first reference block and does not directly use the first reference block;

based on the disabled first particular inter-coding tool using a result of interpolation of the first reference block and not directly using the first reference block, enable the disabled first particular ~~tool~~inter-coding tool for the first block; and

~~select the first particular tool as the first inter-coding tool, wherein the instructions cause the one or more processors to~~ code the first block with the first particular inter-coding tool.

~~20.~~ 14. The non-transitory computer-readable medium of claim ~~16,~~ 12, wherein the first particular inter-coding tool includes one of temporal motion vector prediction (TMVP), decode side motion vector refinement (DMVR), or bi-directional optical flow (BDOF) and wherein the ~~first~~second particular tool includes one of TMVP, DMVR, ~~and~~or BDOF.

~~21.~~ 15. The non-transitory computer-readable medium of claim ~~16~~12, wherein the instructions, when executed, further cause the one or more processors to determine that reference picture resampling (RPR) is enabled.

~~22.~~ 16. The non-transitory computer-readable medium of claim ~~16~~14, wherein size comprises a resolution.

~~23.~~ 17. A device for coding video data, the device comprising:

means for determining a first reference block in a first reference picture for a first block of a current picture;

means for determining whether the first reference picture is a same size as the current picture;

~~———~~ means for coding the first block ~~with a first inter-coding tool of a first set of inter-coding tools~~ based on the first reference picture being a different size than the current picture, wherein a first particular ~~tool of a plurality of~~ inter-coding ~~tools~~tool is disabled~~;~~

for the first block; means for determining a second reference block in a second reference picture for a second block of the current picture;

means for determining whether the second reference picture is a same size as the current picture; ~~and~~

~~———~~means for coding the second block ~~with a second inter-coding tool of a second set of inter-coding tools~~ based on the second reference picture being the same size as the current picture, wherein the first particular inter-coding tool is enabled~~.~~

~~24. The device of claim 23, further comprising:~~

for the second block; means for determining a third reference block in a third reference picture for a third block of the current picture~~;~~

~~means for determining a fourth reference block in a fourth reference picture for~~ , wherein the third block ~~of the current picture;~~

is a bi-predicted block; means for determining whether the third reference picture is a same size as the current picture;

and means for bi-predictive coding the third block ~~with~~wherein a ~~third~~second particular inter-coding tool ~~of a~~ is disabled for the third ~~set of inter-coding tools~~block

based on the third reference picture being a different size than the current picture~~, wherein a second particular tool of the plurality of inter-coding tools is disabled for the third reference block;~~

~~means for determining whether the fourth reference picture is a same size as the current picture; and~~

~~means for coding the third block with a fourth inter-coding tool of a fourth set of inter-coding tools based on the fourth reference picture being the same size as the current picture, wherein the second particular tool is enabled for the fourth reference block~~.

~~25.~~ 18. The device of claim ~~23~~17, further comprising:

~~means for determining a third reference block in a third reference picture for a third block~~

~~means for determining a fourth reference block in a fourth reference picture for the third block;~~

~~means for determining whether the third reference picture is a same size as the fourth reference picture;~~

~~means for coding the third block with a third inter-coding tool of a third set of inter-coding tools based on third reference picture being the same size as the fourth reference picture, wherein the first particular tool is enabled.~~

~~26. The device of claim 23, further comprising:~~

means for determining whether the disabled first particular inter-coding tool uses a result of interpolation of the first reference block and does not directly use the first reference block;

means for enabling the disabled first particular inter-coding tool for the first block based on the disabled first particular inter-coding tool using a result of interpolation of the first reference block and not directly using the first reference block; and

~~selecting the first particular tool as the first inter-coding tool, wherein the~~ means for coding the first block with the first particular inter-coding tool~~ comprises means for coding the first block with the first particular tool~~.

~~27.~~ 19. The device of claim ~~23~~17, wherein the first particular inter-coding tool

includes one of temporal motion vector prediction (TMVP), decode side motion vector refinement (DMVR), and bi-directional optical flow (BDOF) and wherein the second particular inter-coding tool includes one of TMVP, DMVR, or BDOF.

~~28. The device of claim 23~~20. The device of claim 17, further comprising means for determining that reference picture resampling (RPR) is enabled.

~~29.~~ 21. The device of claim ~~23~~19, wherein size comprises a resolution.

22. The method of claim 3, wherein the same size comprises a same height and a same width.

23. The device of claim 8, wherein the same size comprises a same height and a same width.

24. The non-transitory computer-readable medium of claim 14, wherein the same size comprises a same height and a same width.

25. The device of claim 19, wherein the same size comprises a same height and a same width.