

FIG. 1

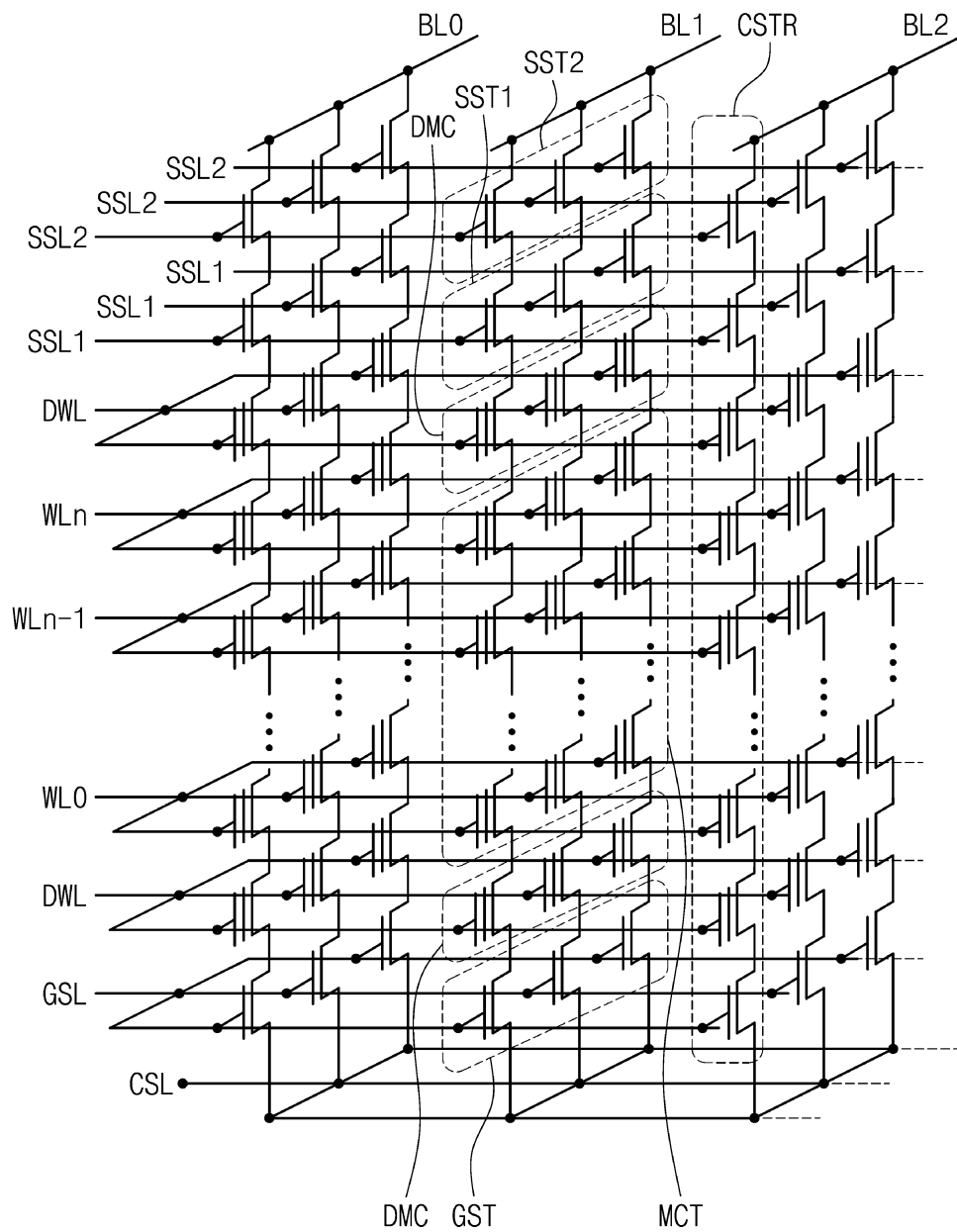


FIG. 2

FIG. 2 is a schematic diagram of a display panel 40, showing a top view of a pixel array. The panel is divided into a common region (CNR) and a common area region (CAR). The CNR contains a grid of pixels with subpixels (EL1, EL1p, EL2, EL2p) and a common layer (CPLG). The CAR contains a grid of pixels with subpixels (EL1, EL2) and a common layer (CPLG). The panel is bounded by a frame (ST) and a display area (D1, D2).

FIG. 3

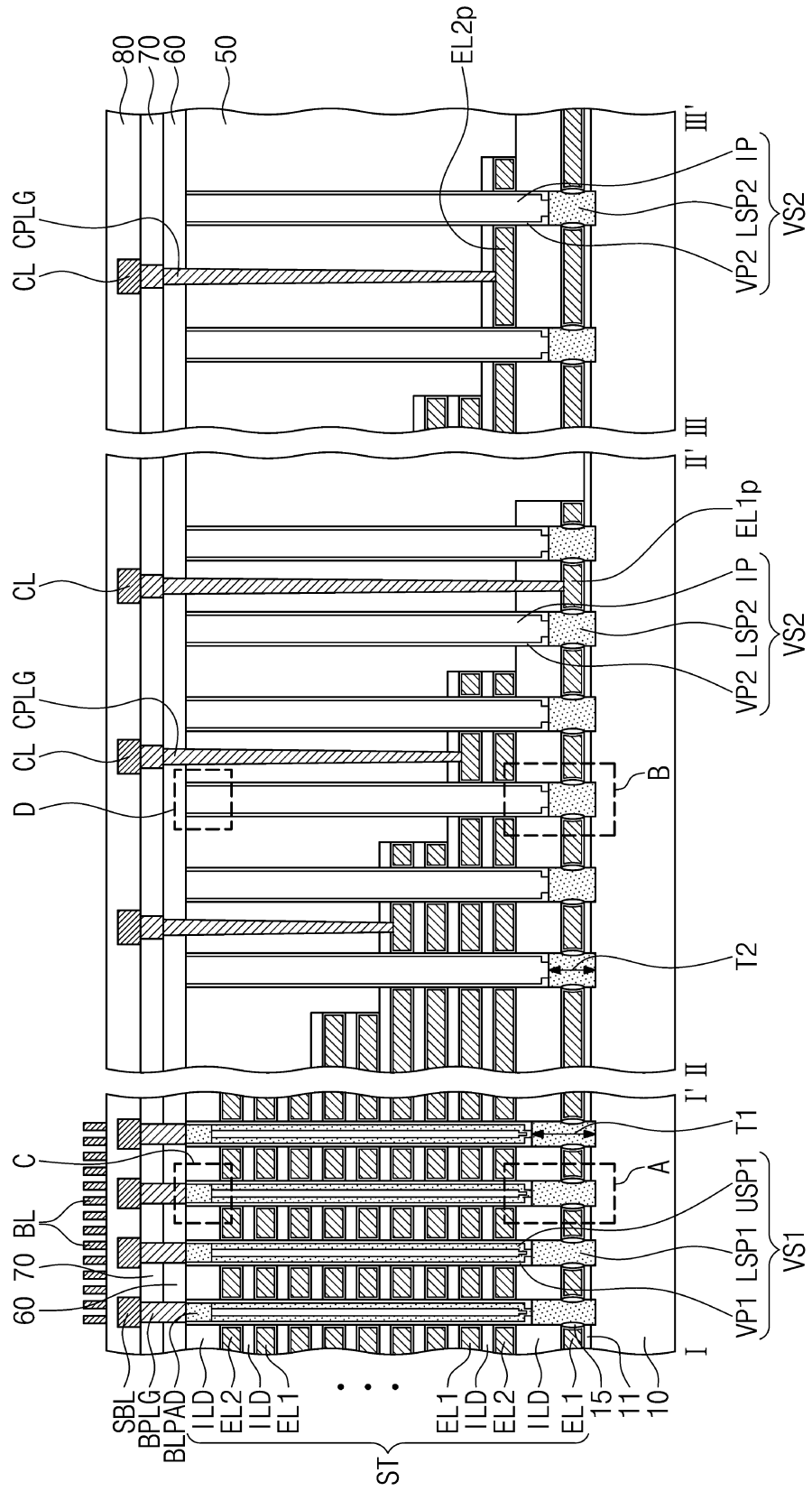


FIG. 4

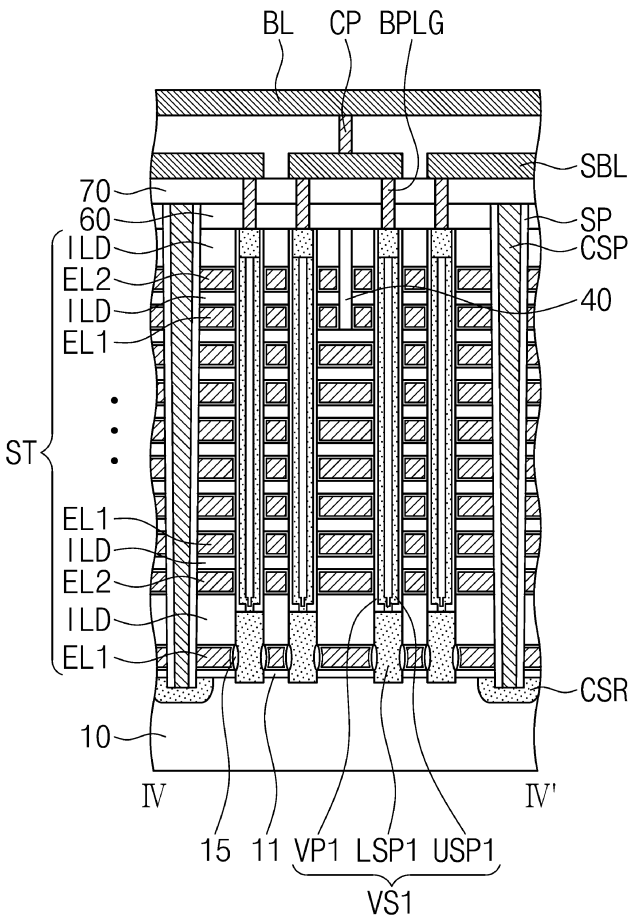


FIG. 5A

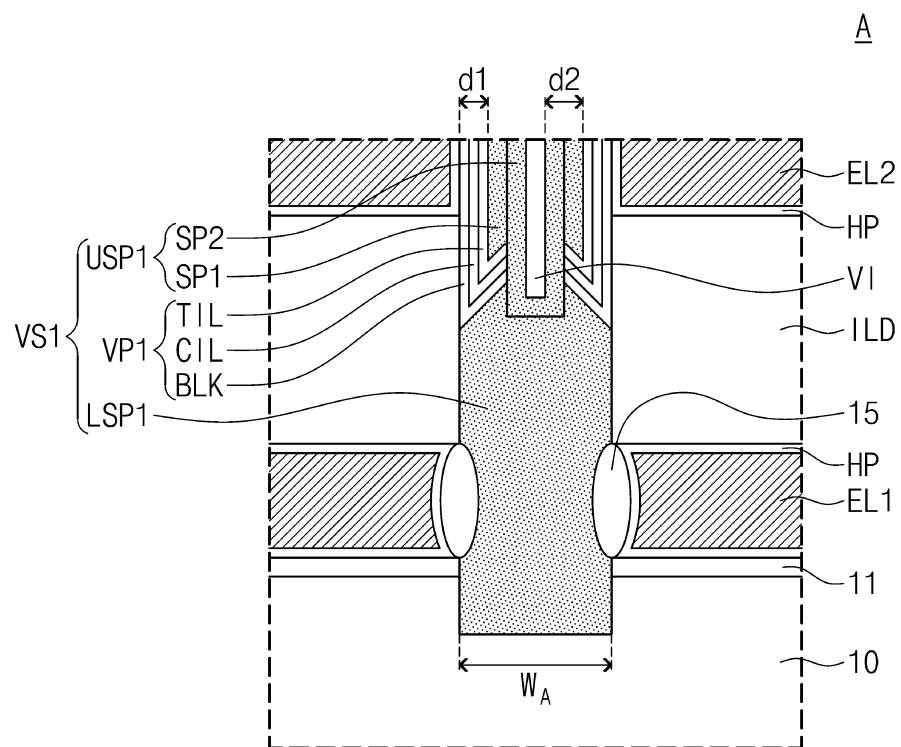


FIG. 5B

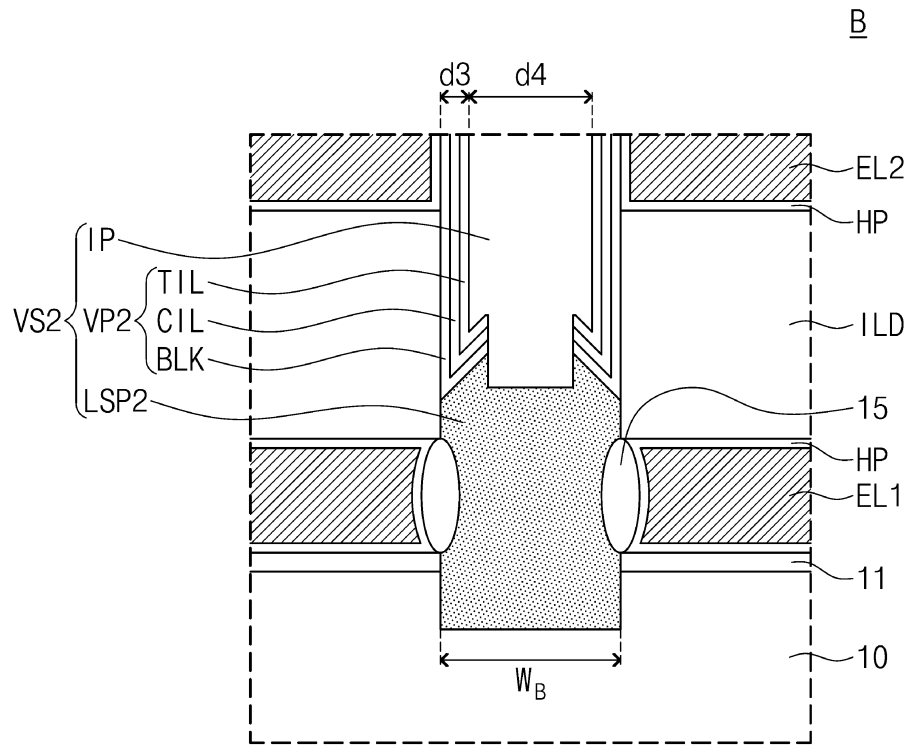


FIG. 5C

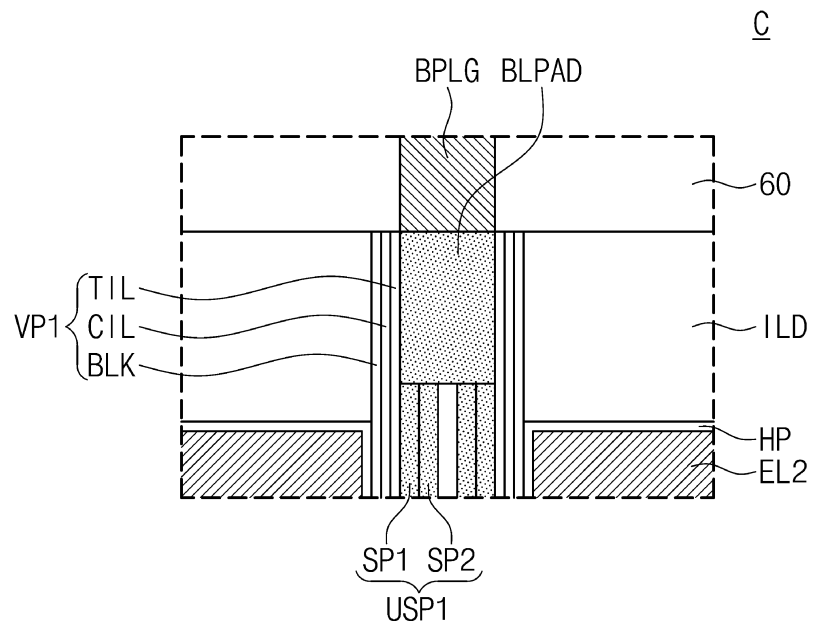


FIG. 5D

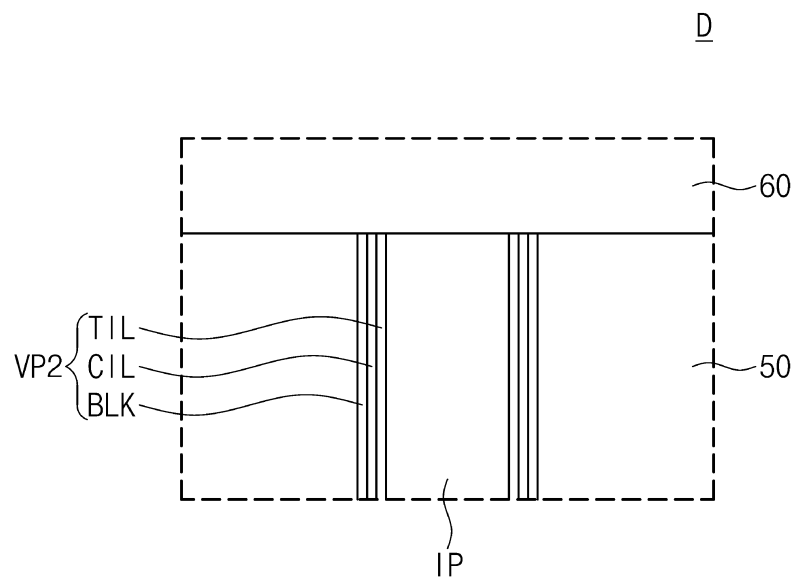


FIG. 5E

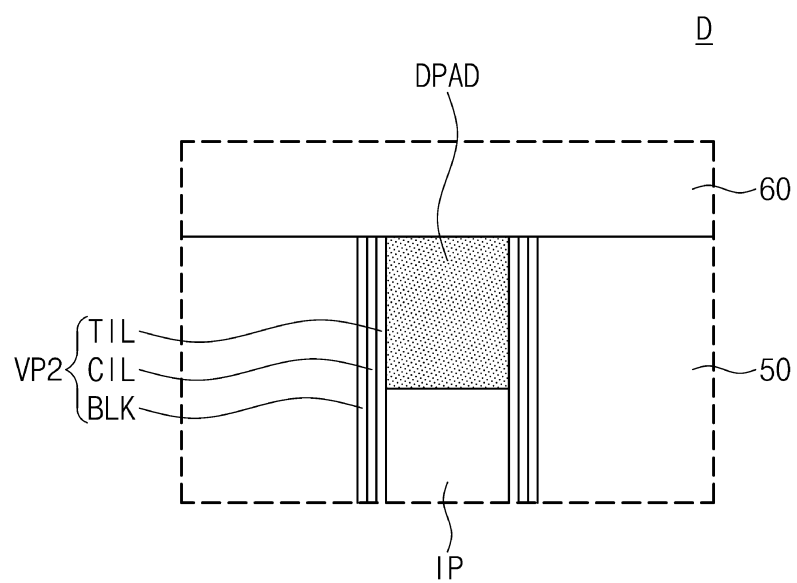


FIG. 6

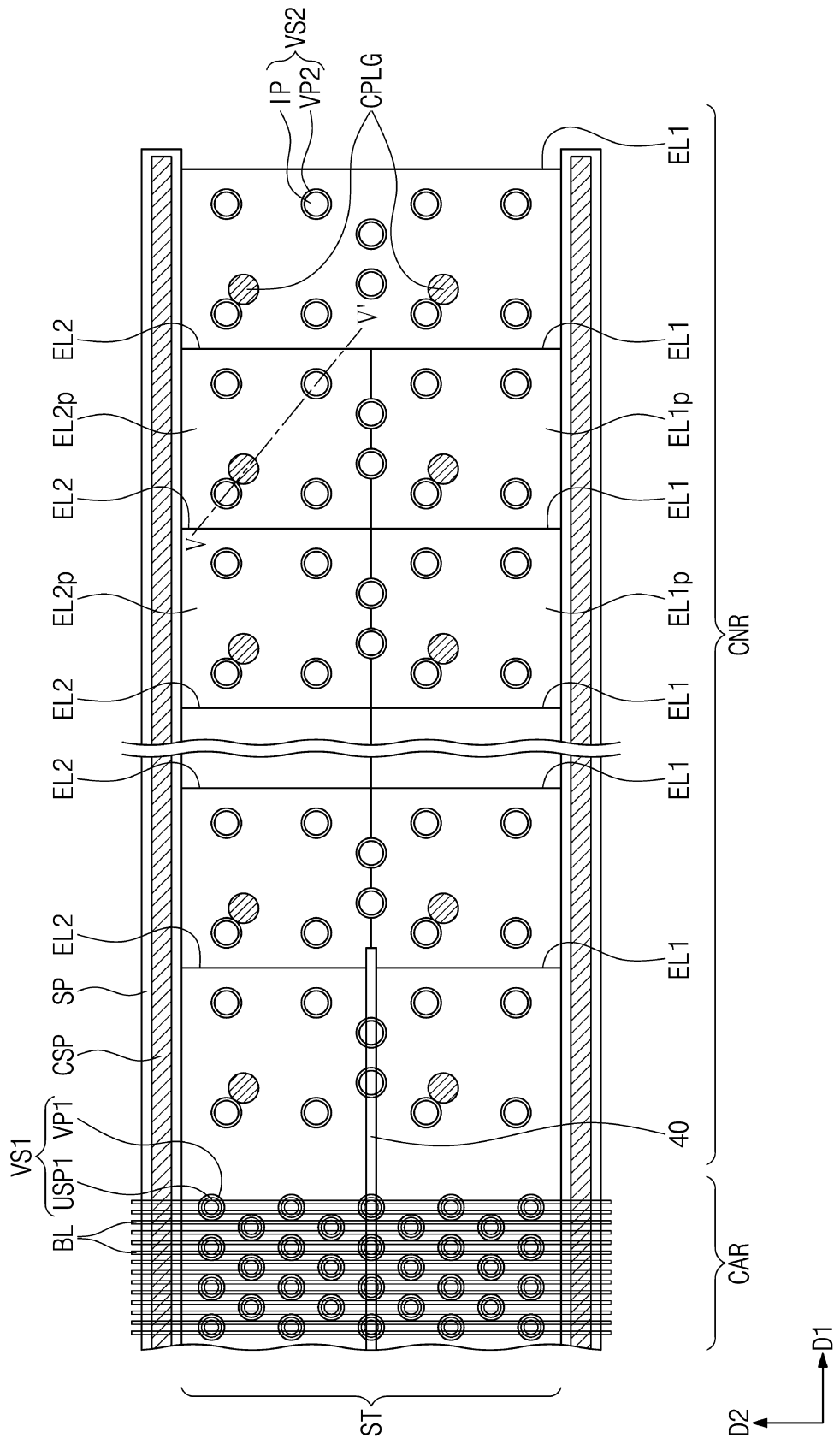


FIG. 7

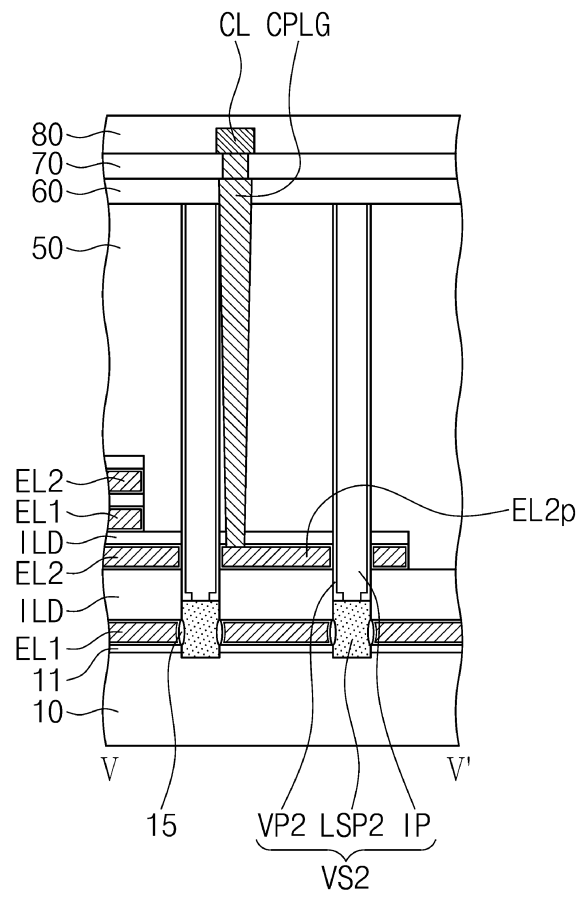


FIG. 8

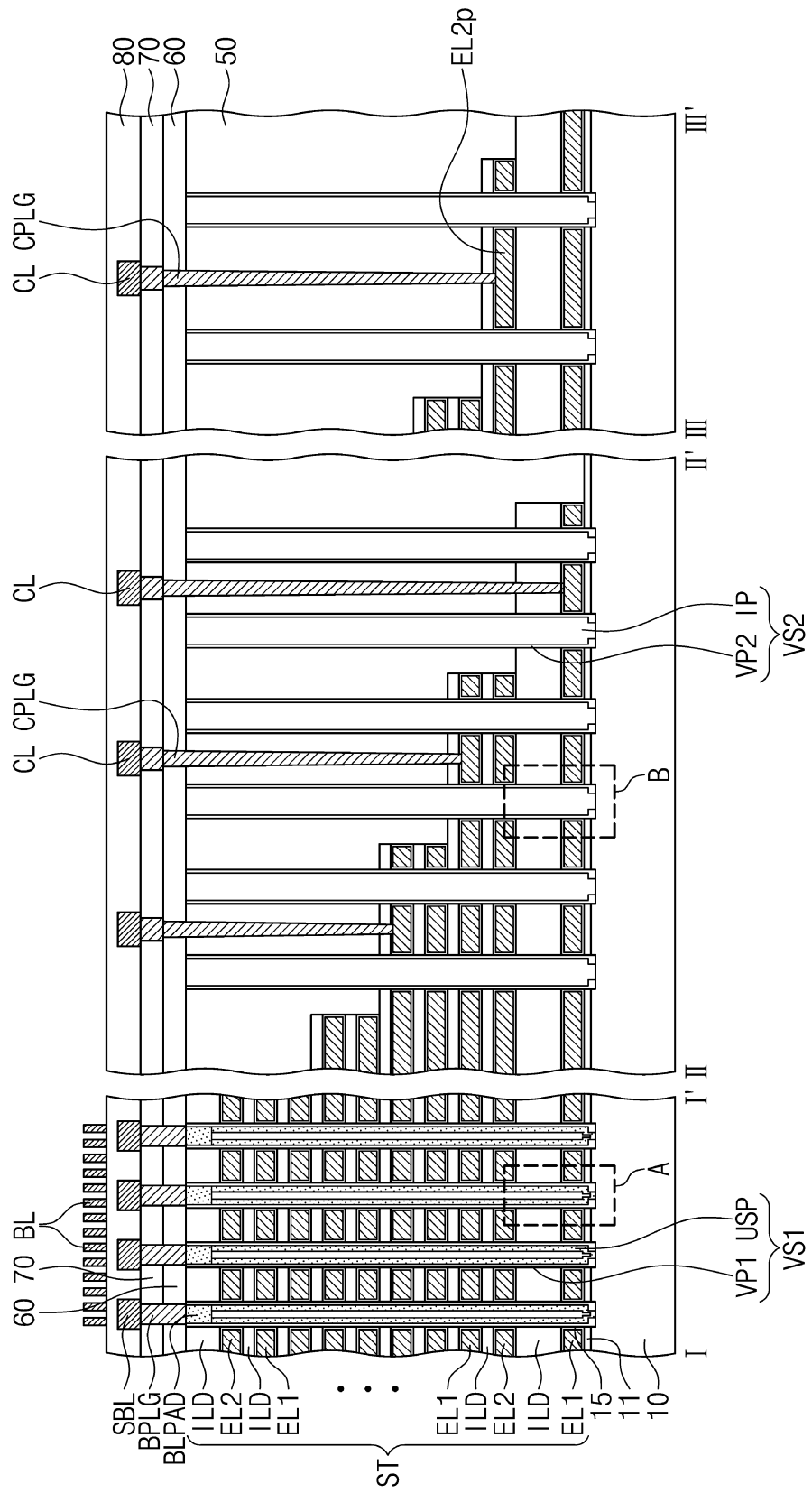


FIG. 9A

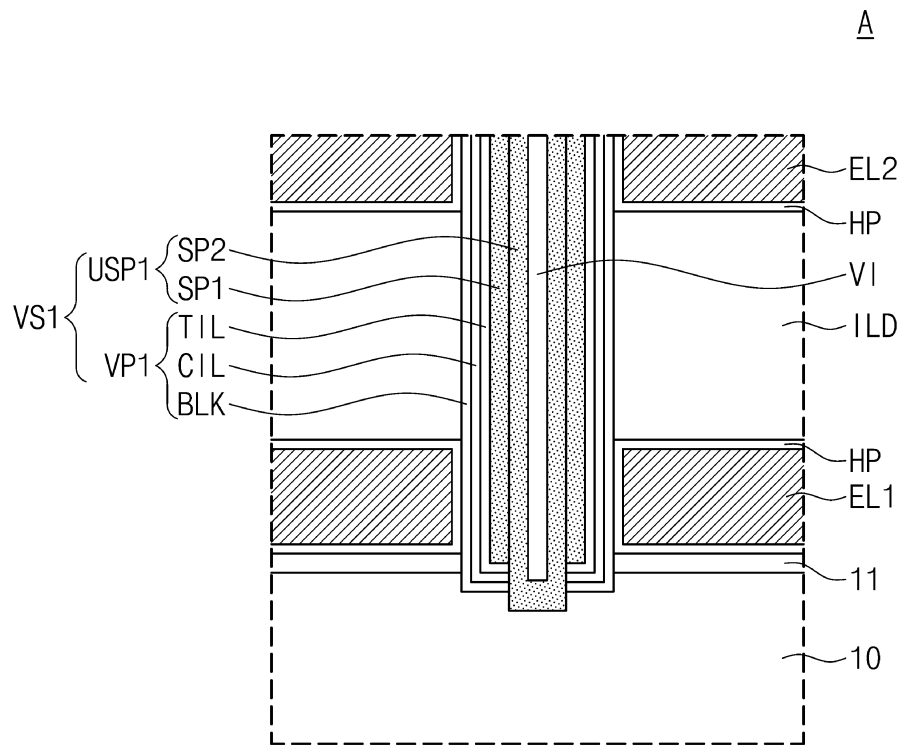


FIG. 9B

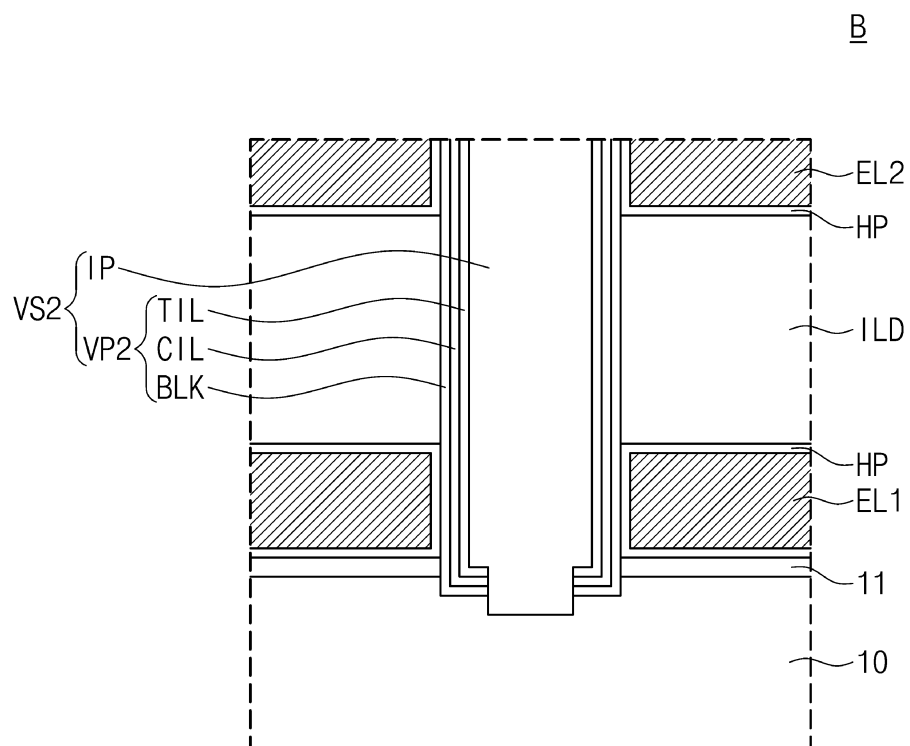
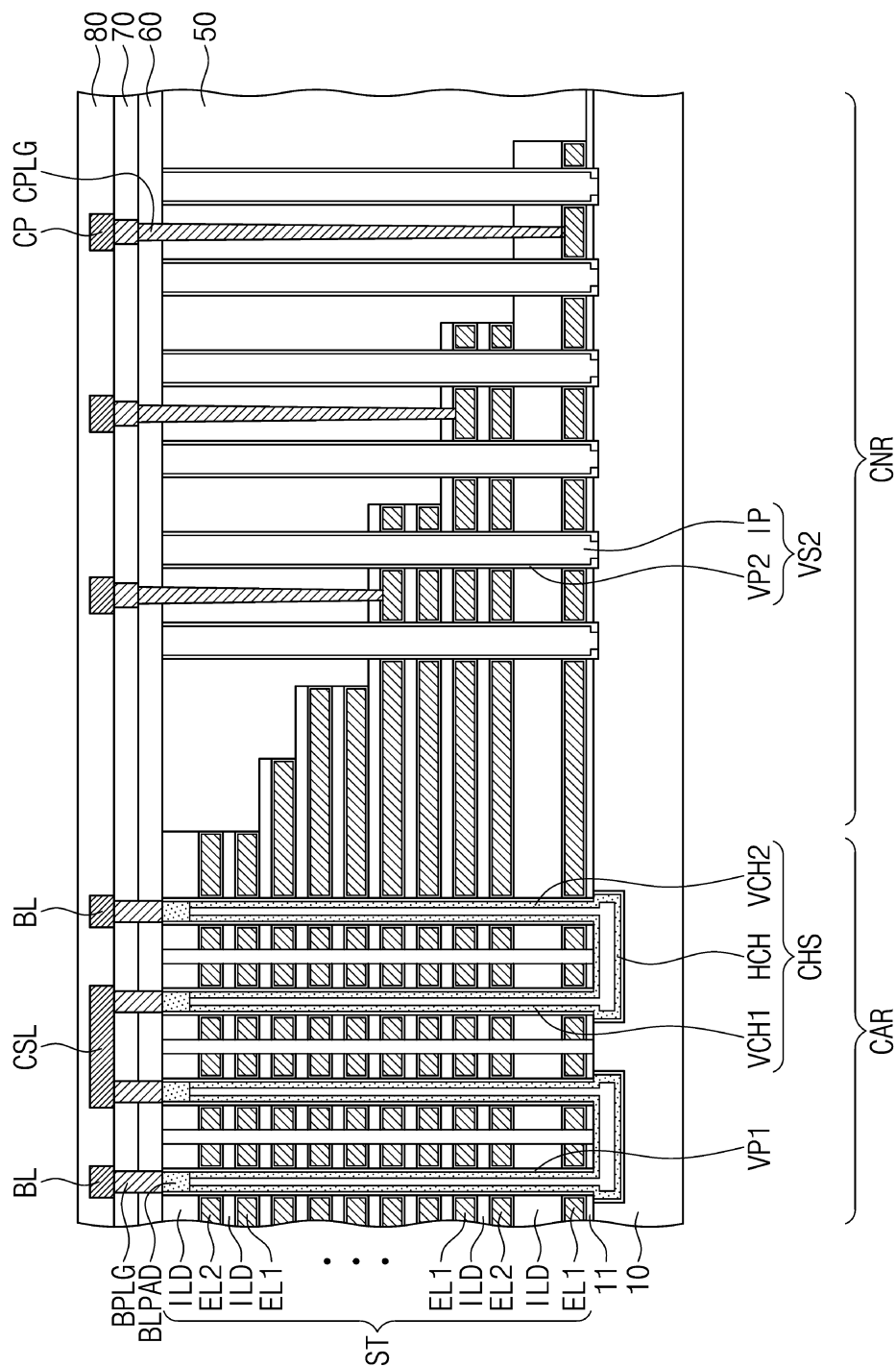


FIG. 10



[illegible]

FIG. 12

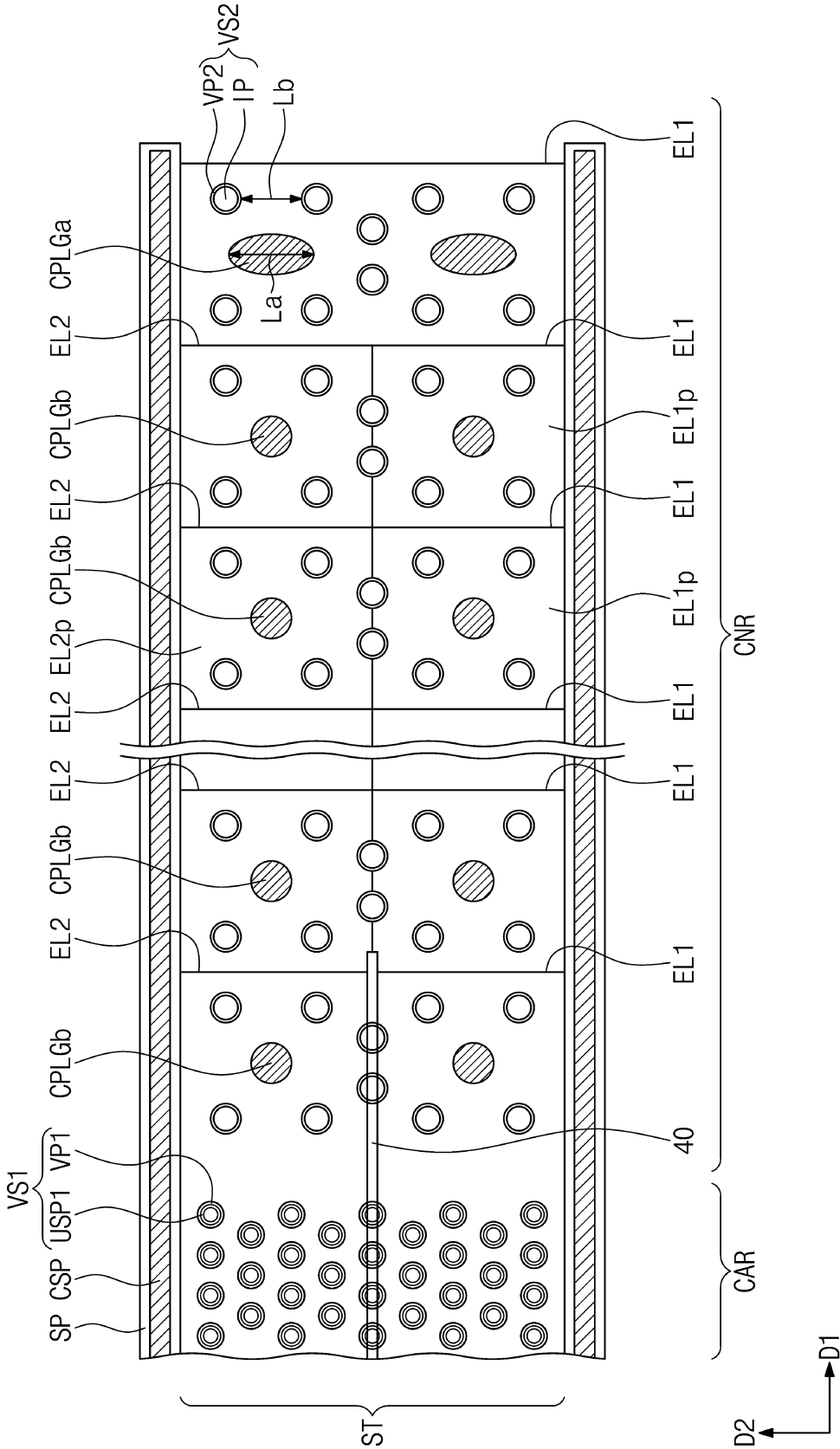


FIG. 13

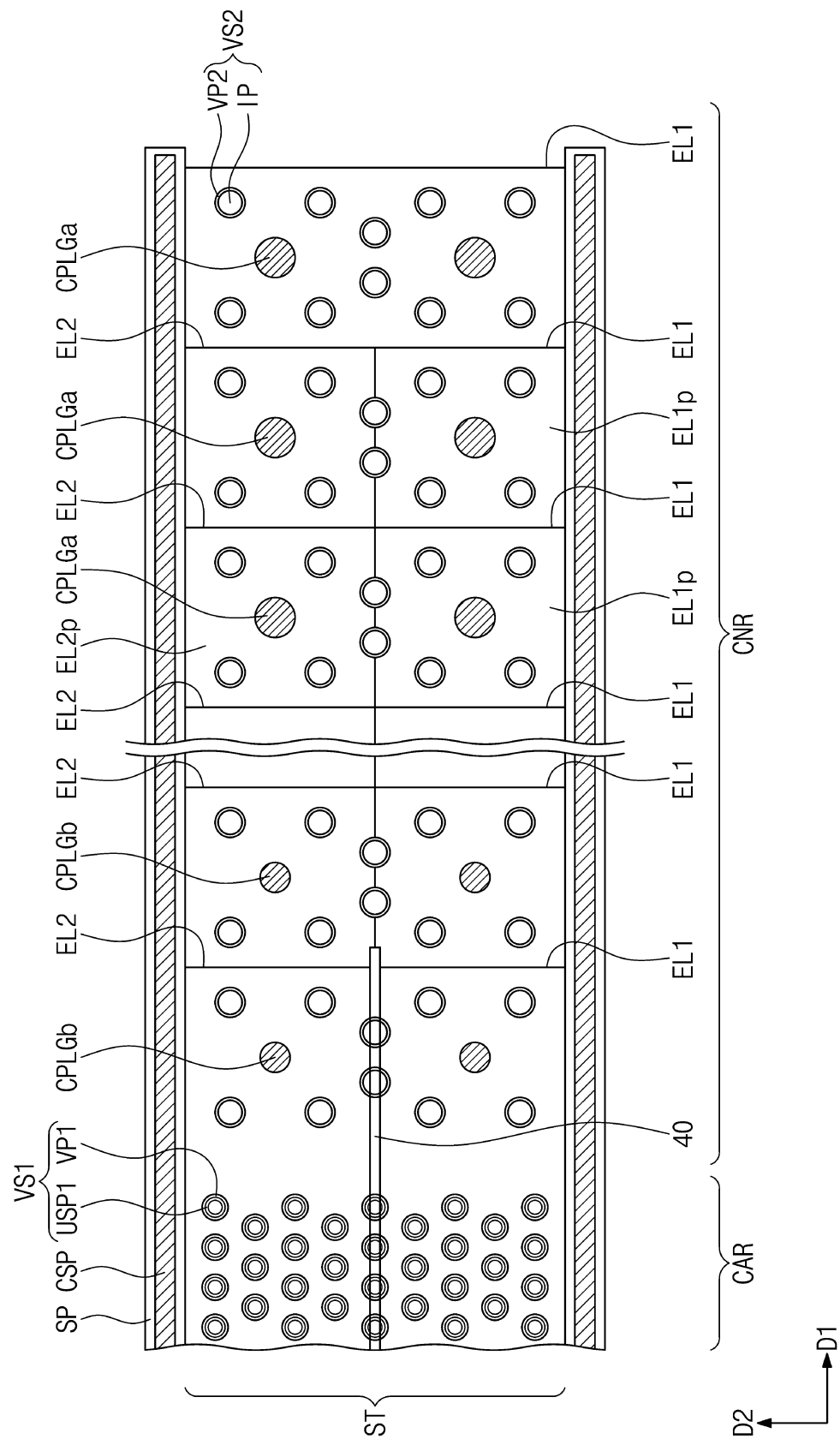


FIG. 14

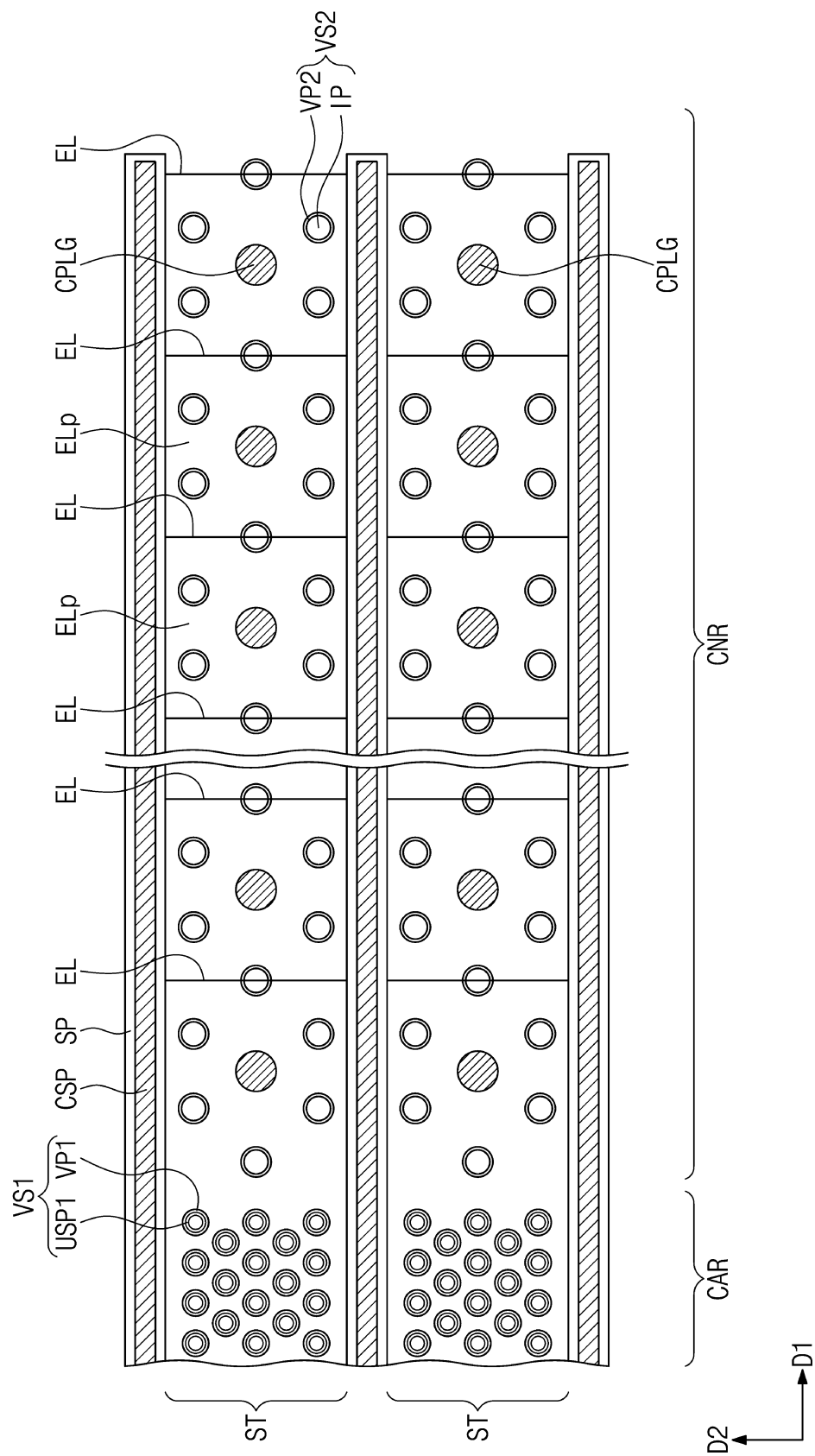


FIG. 15

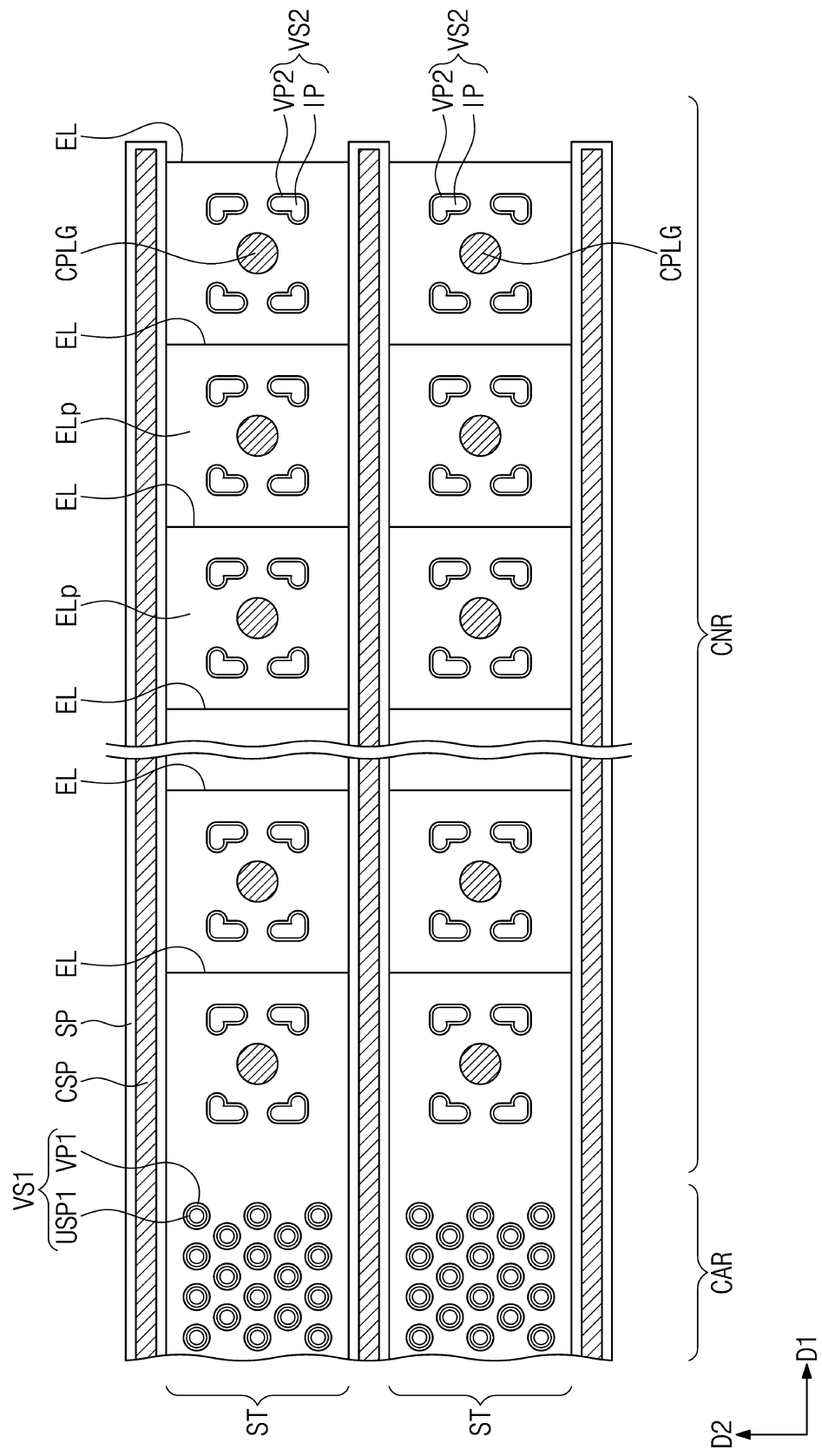


FIG. 16

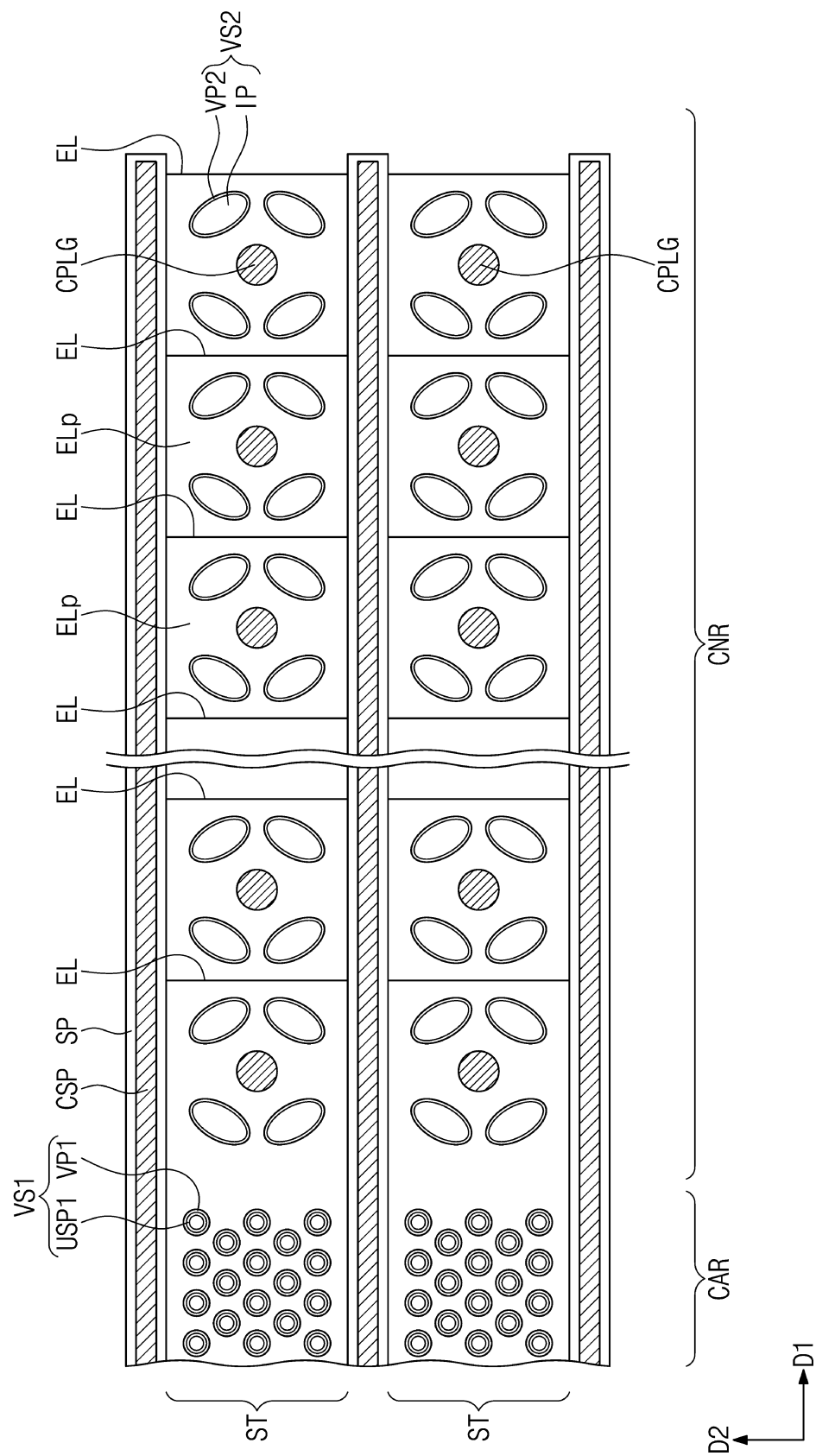


FIG. 17

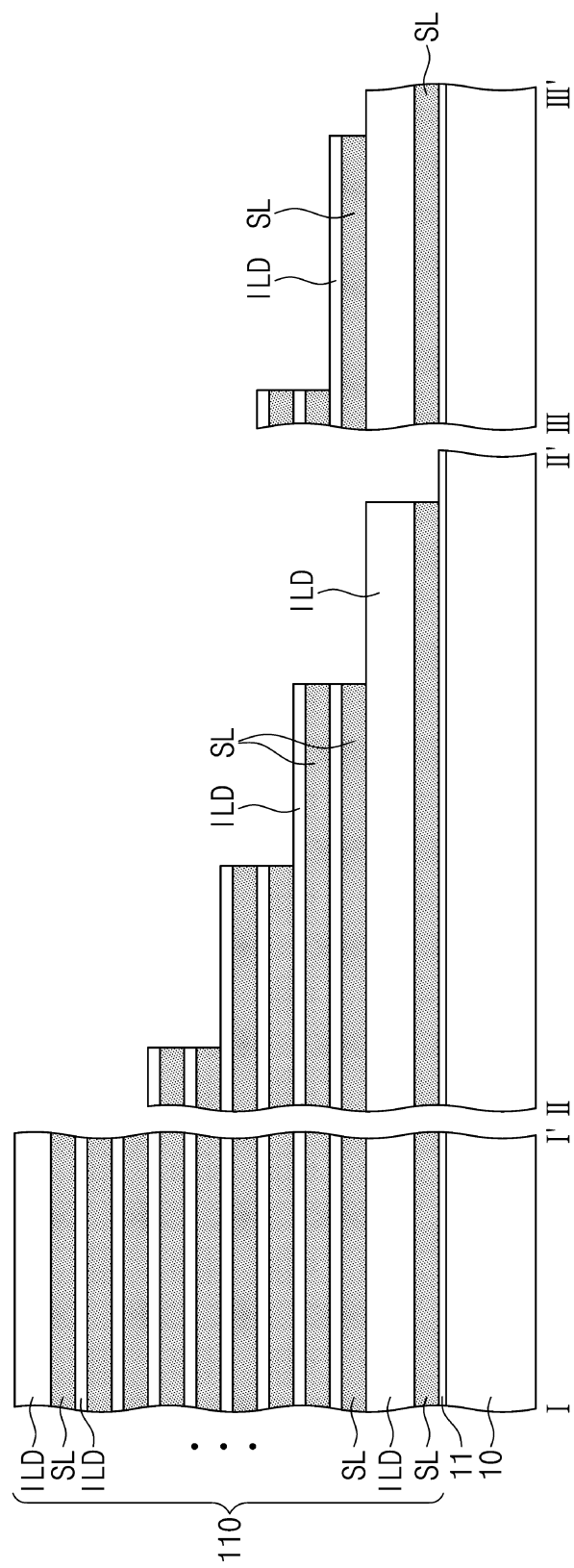




FIG. 19

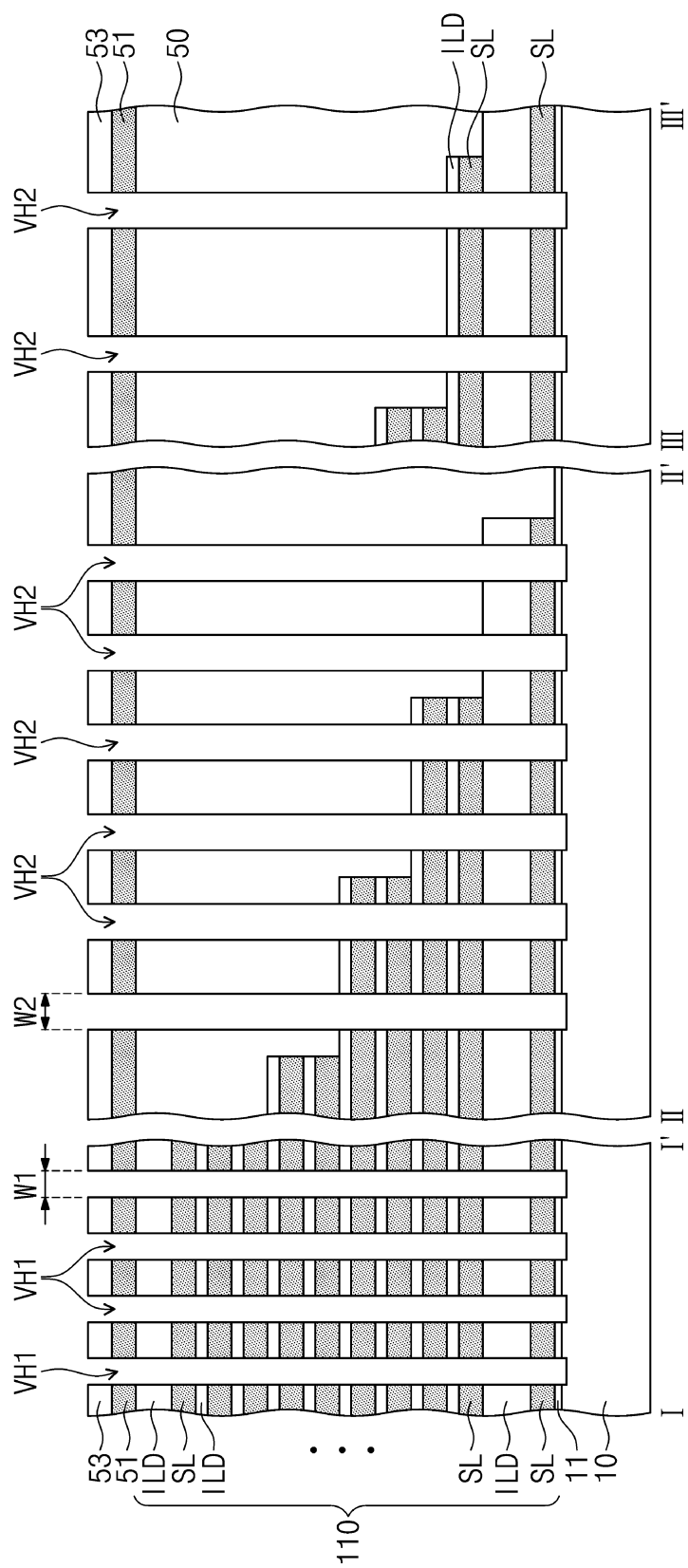


FIG. 20

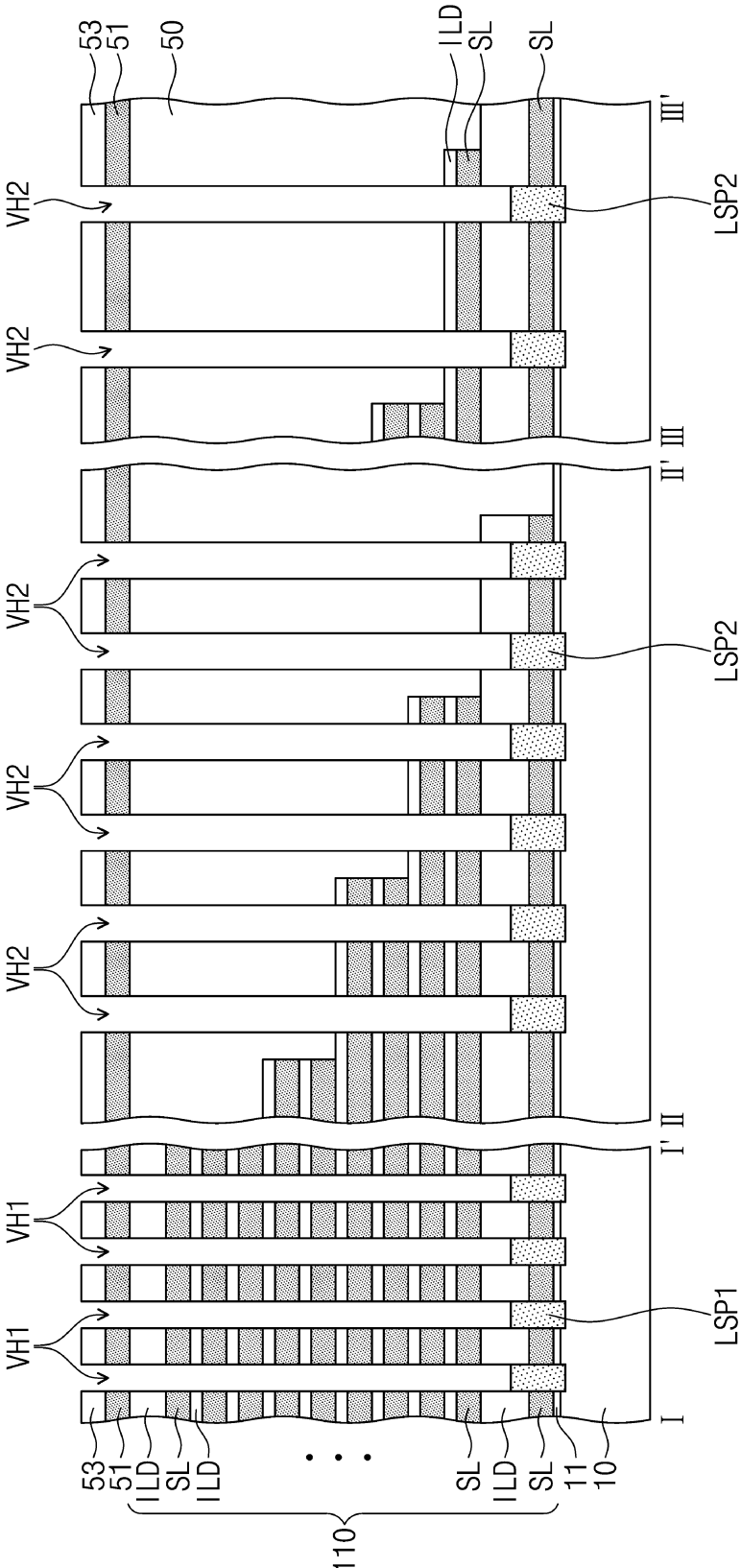


FIG. 21

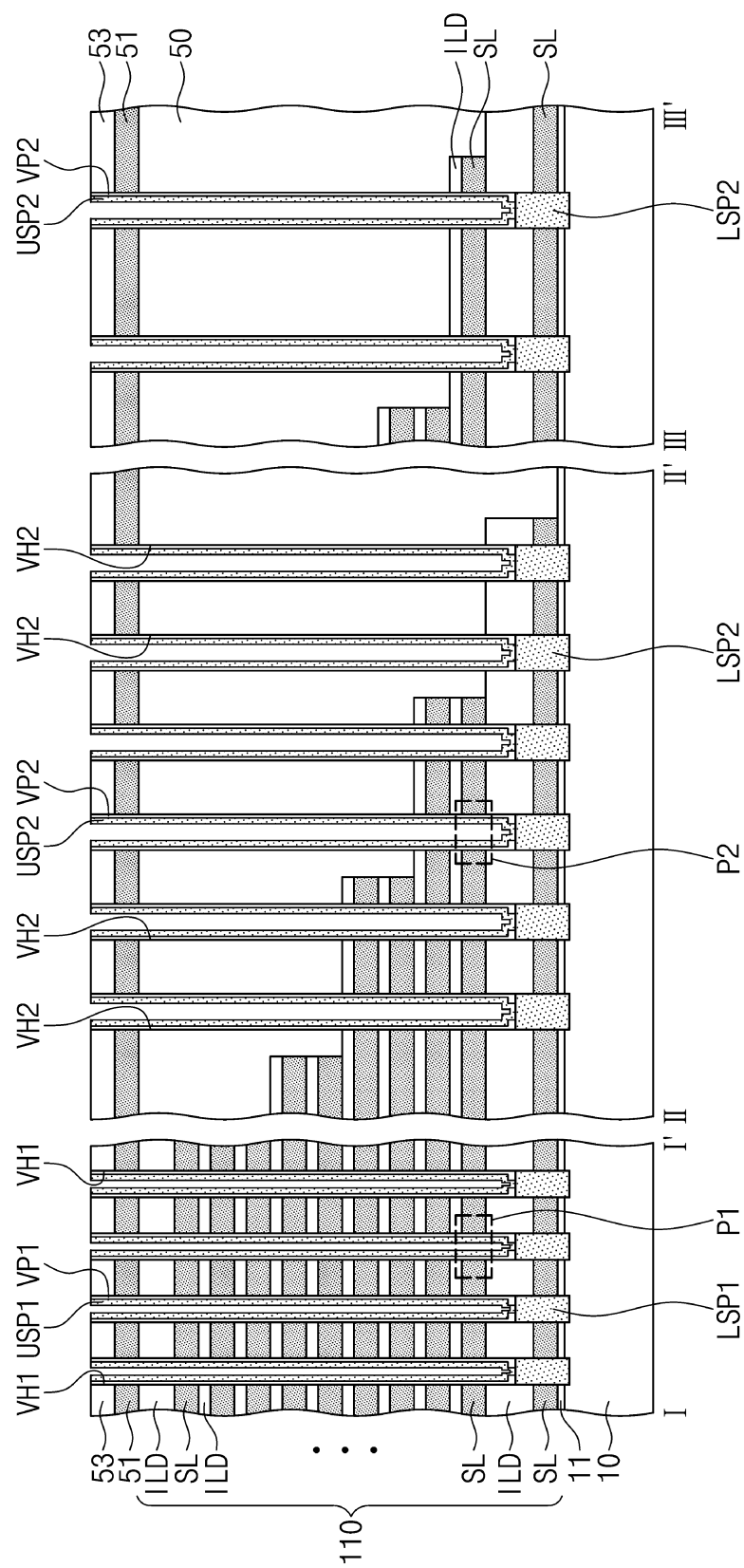


FIG. 22A

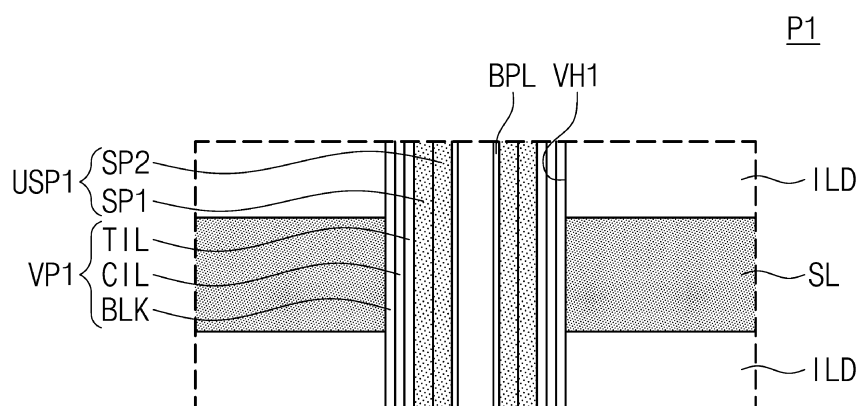


FIG. 22B

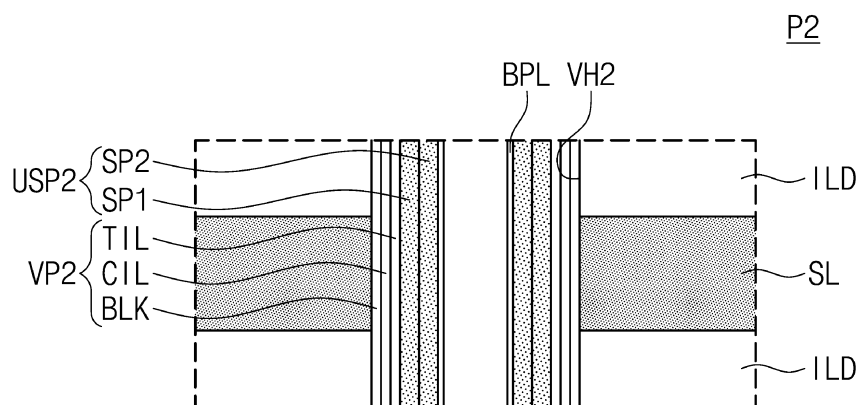


FIG. 23

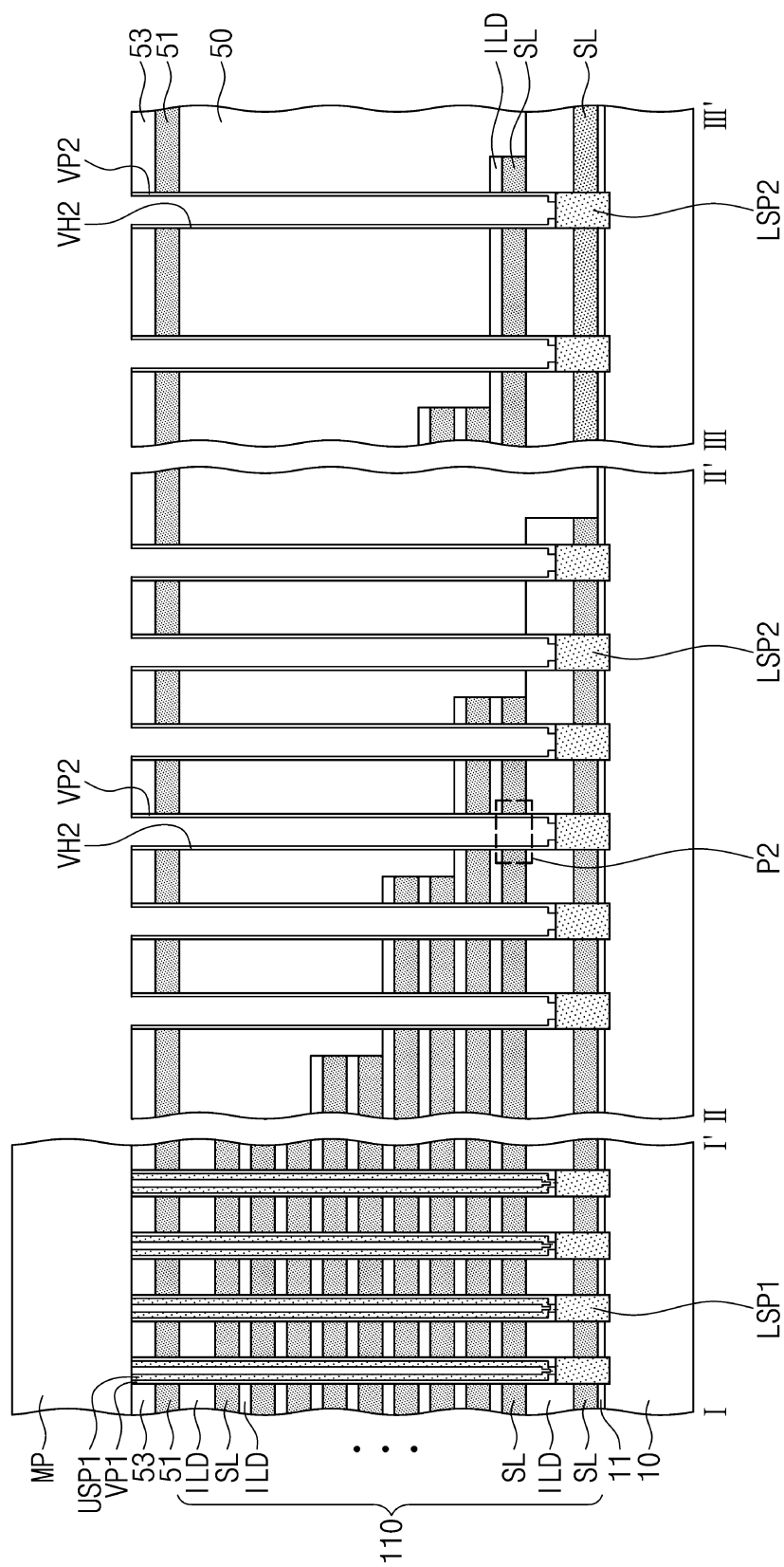


FIG. 24

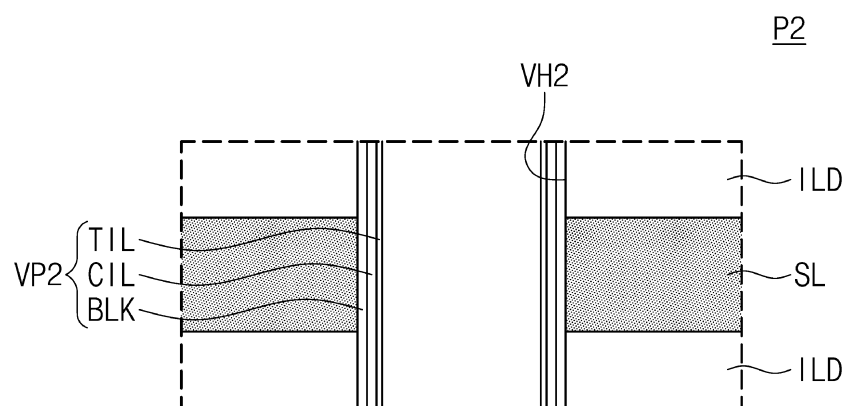




FIG. 26

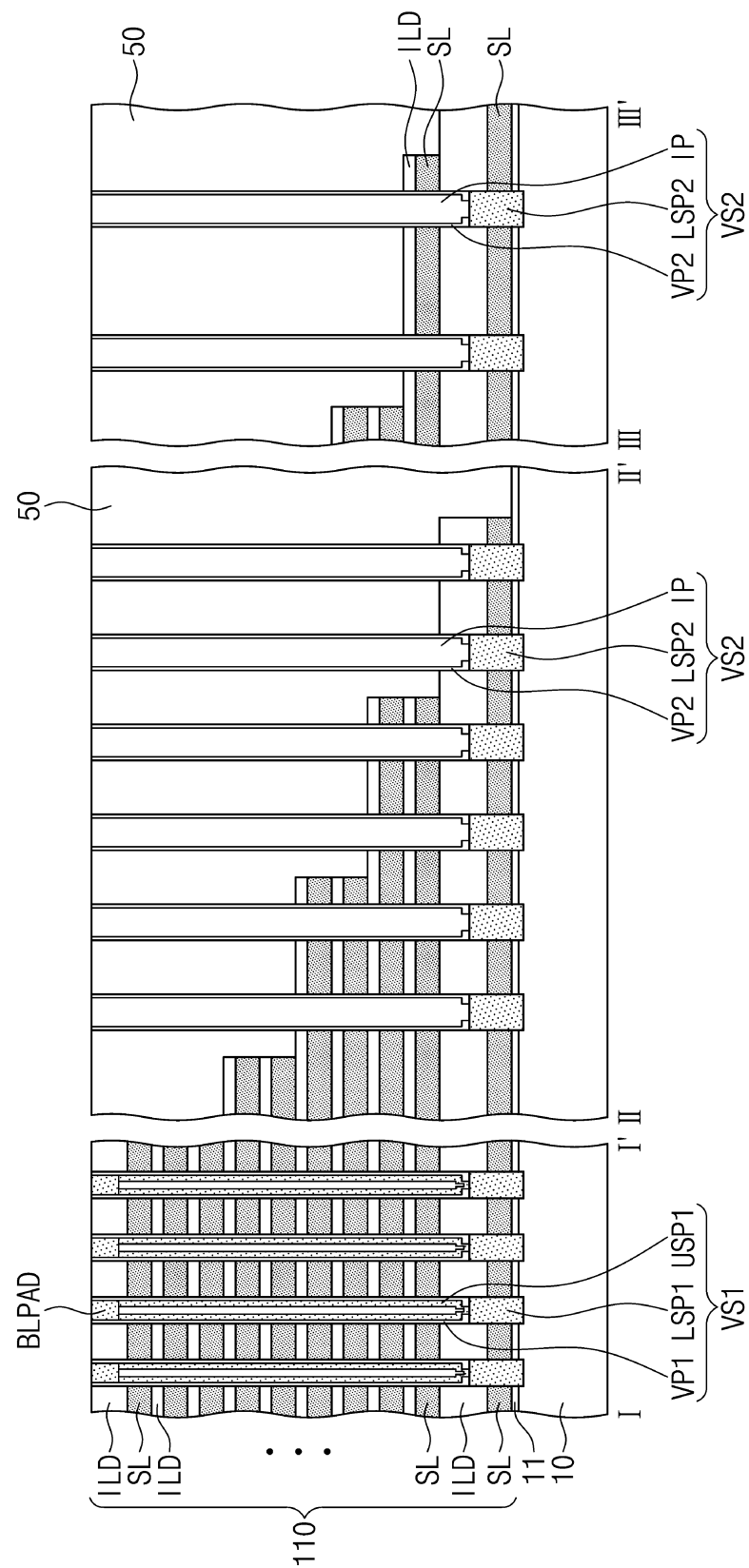


FIG. 27

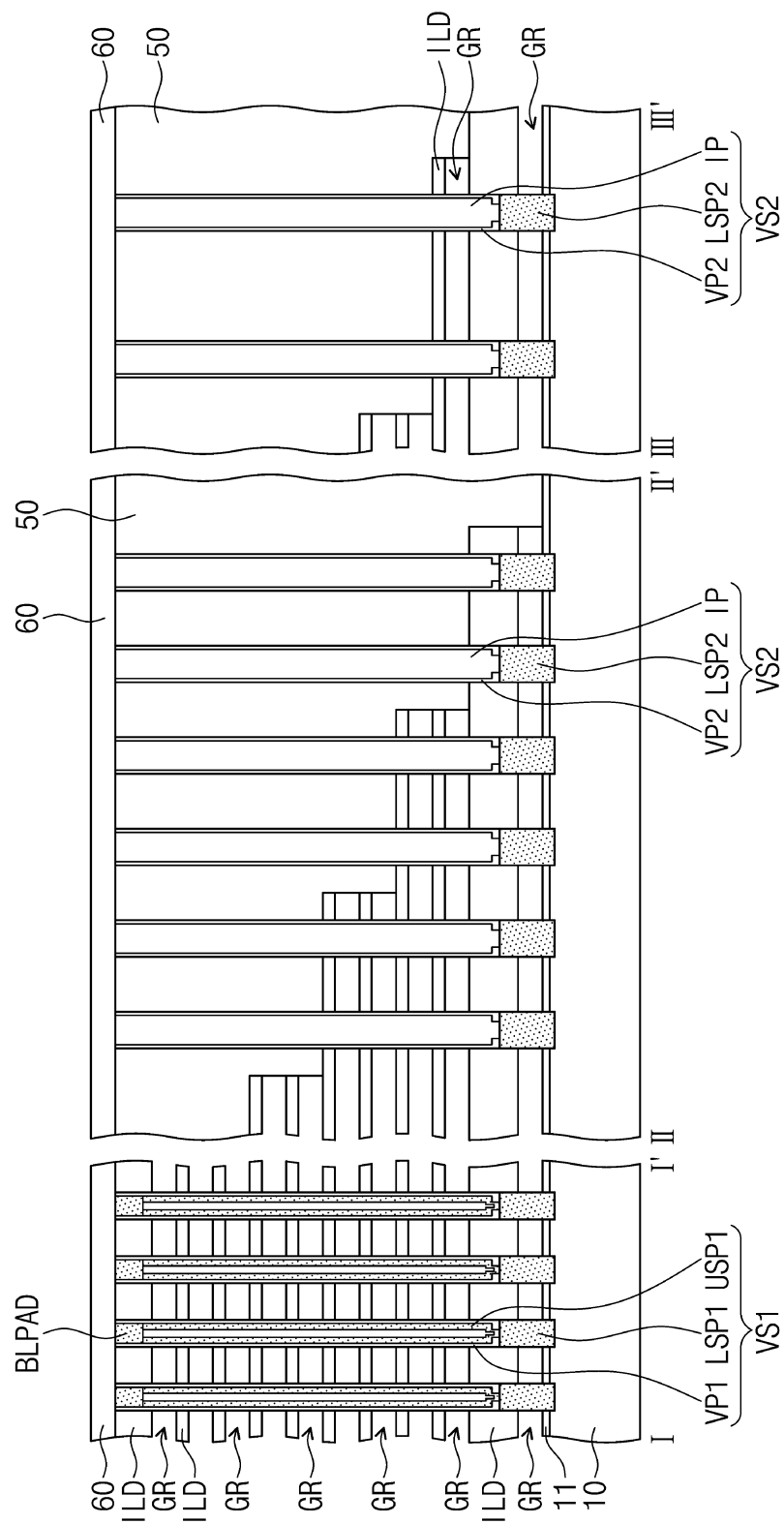


FIG. 28

