

## IMPEDANCE CALIBRATION CIRCUIT

### ABSTRACT

Embodiments provide an impedance calibration circuit, including: a calibration module configured to receive a first calibration clock signal, to perform impedance calibration on a basis of the first calibration clock signal, and to output a first stop signal when the calibration is completed; a first detection module configured to detect calibration time of the impedance calibration circuit, and to output a second stop signal when the calibration time reaches a preset value; and a calibration control module configured to receive the first stop signal and the second stop signal and to output the first calibration clock signal. When the first stop signal or the second stop signal is received, the calibration control module stops outputting the first calibration clock signal. The calibration control module not only stops outputting the first calibration clock signal according to the first stop signal, but also stops outputting the first calibration clock signal according to the second stop signal, such that the impedance calibration circuit can stop the calibration in time to meet design requirements, thereby effectively reducing power consumption, and improving reliability of the impedance calibration circuit.

[Fig. 1]