100

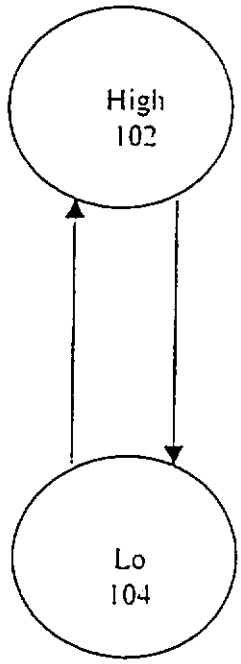


FIG. 1

200

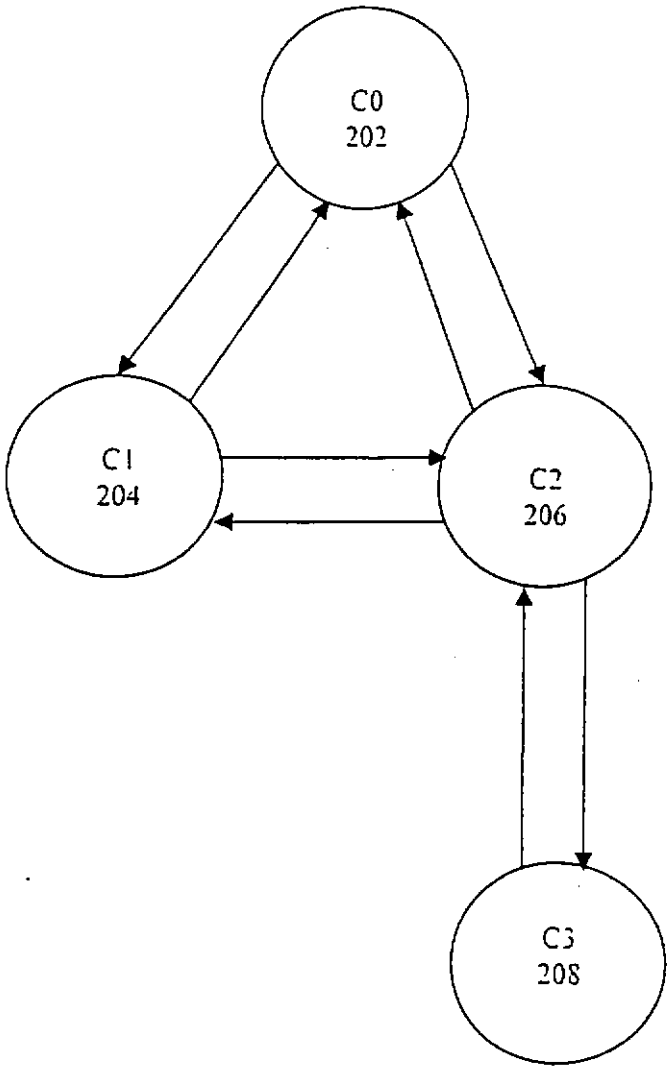
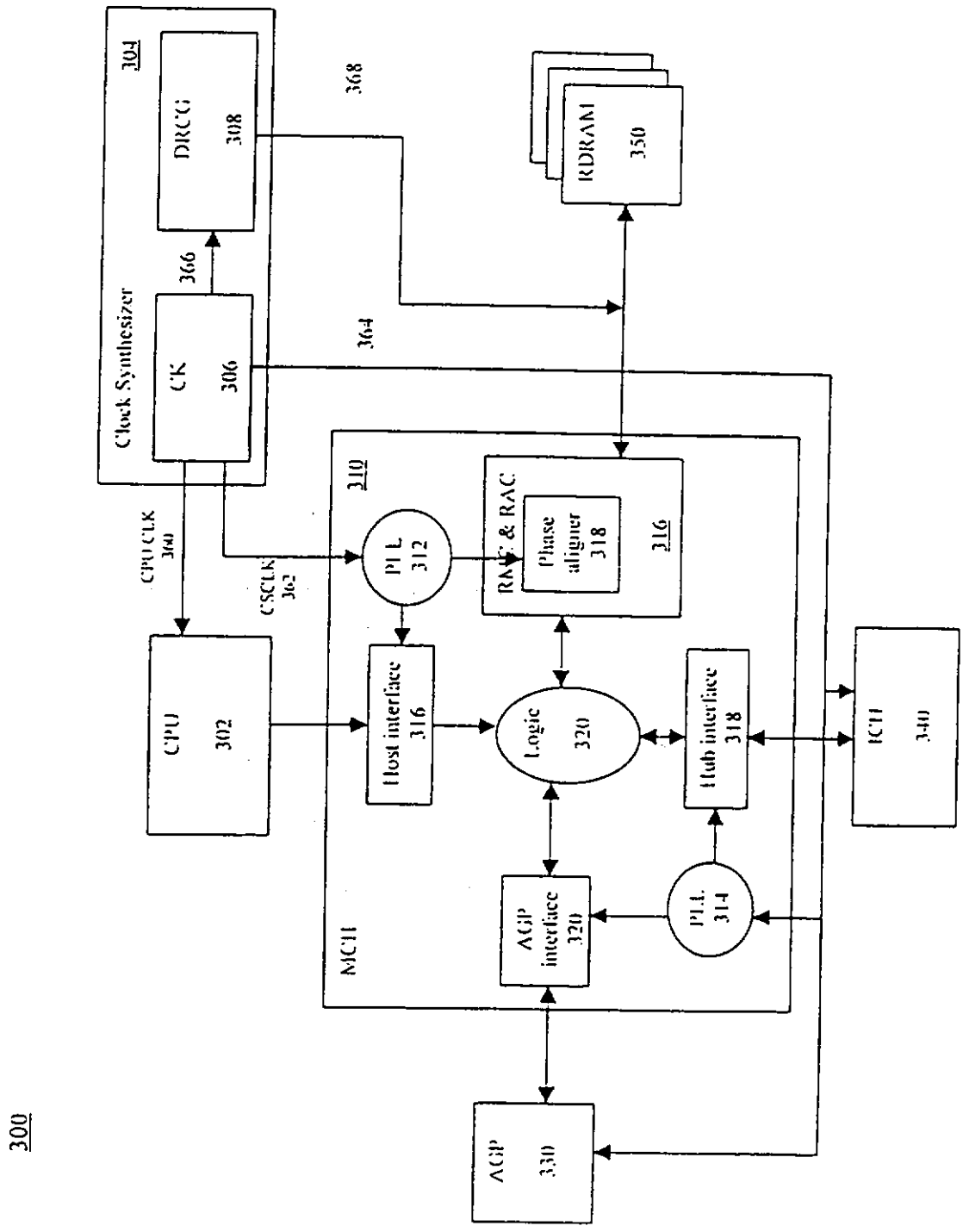


FIG. 2

FIG. 3


300
Clock Synthesizer
304
DRCG
308
366
CK
306
368
364
RDRAM
350
CPU CLK
360
CSCLK
362
310
PLL
312
RAC & RAC
Phase aligner
318
316
CPU
302
Host interface
316
Logic
320
Hub interface
318
ICH
340
MCH
AGP interface
320
PLL
314
AGP
330

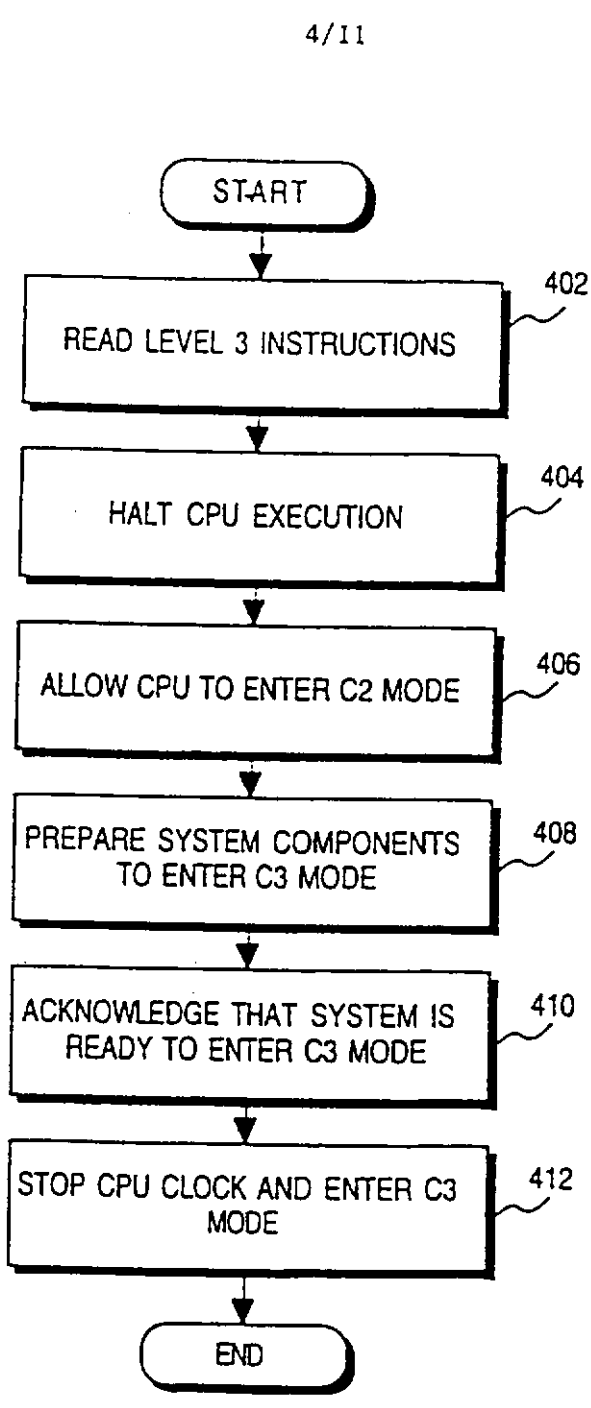

400
START
READ LEVEL 3 INSTRUCTIONS
402
HALT CPU EXECUTION
404
ALLOW CPU TO ENTER C2 MODE
406
PREPARE SYSTEM COMPONENTS TO ENTER C3 MODE
408
ACKNOWLEDGE THAT SYSTEM IS READY TO ENTER C3 MODE
410
STOP CPU CLOCK AND ENTER C3 MODE
412
END

FIG. 4

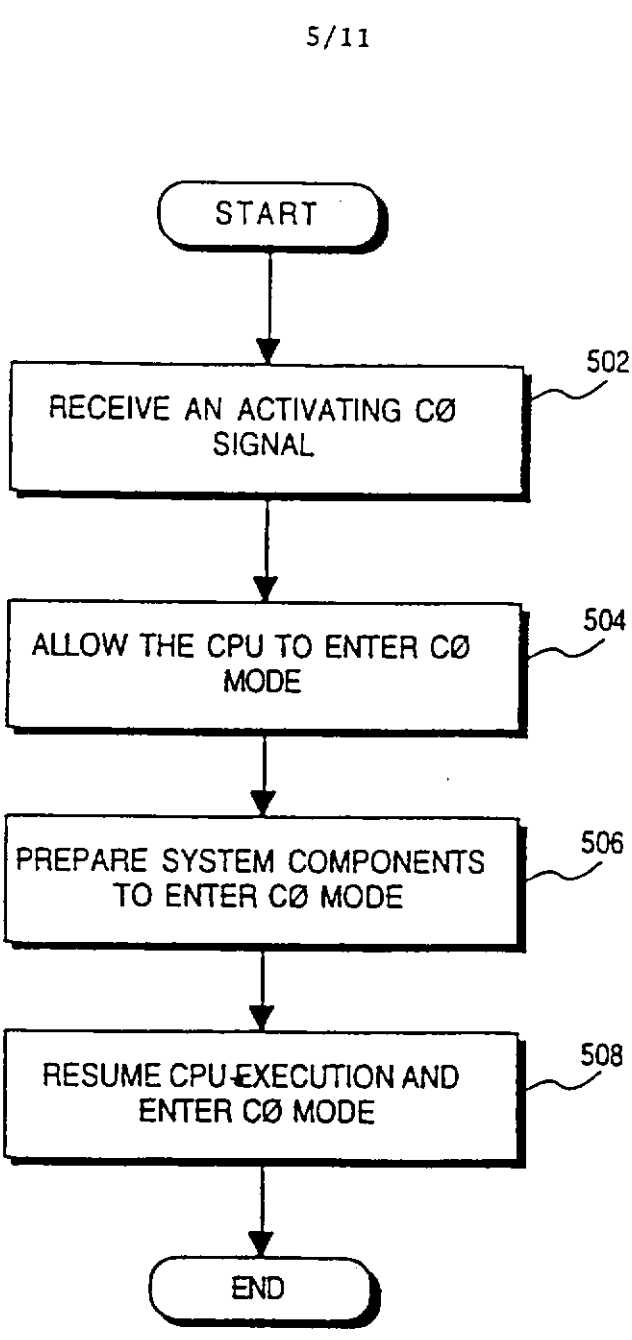

500
START
RECEIVE AN ACTIVATING CØ SIGNAL
502
ALLOW THE CPU TO ENTER CØ MODE
504
PREPARE SYSTEM COMPONENTS TO ENTER CØ MODE
506
RESUME CPU EXECUTION AND ENTER CØ MODE
508
END

FIG. 5

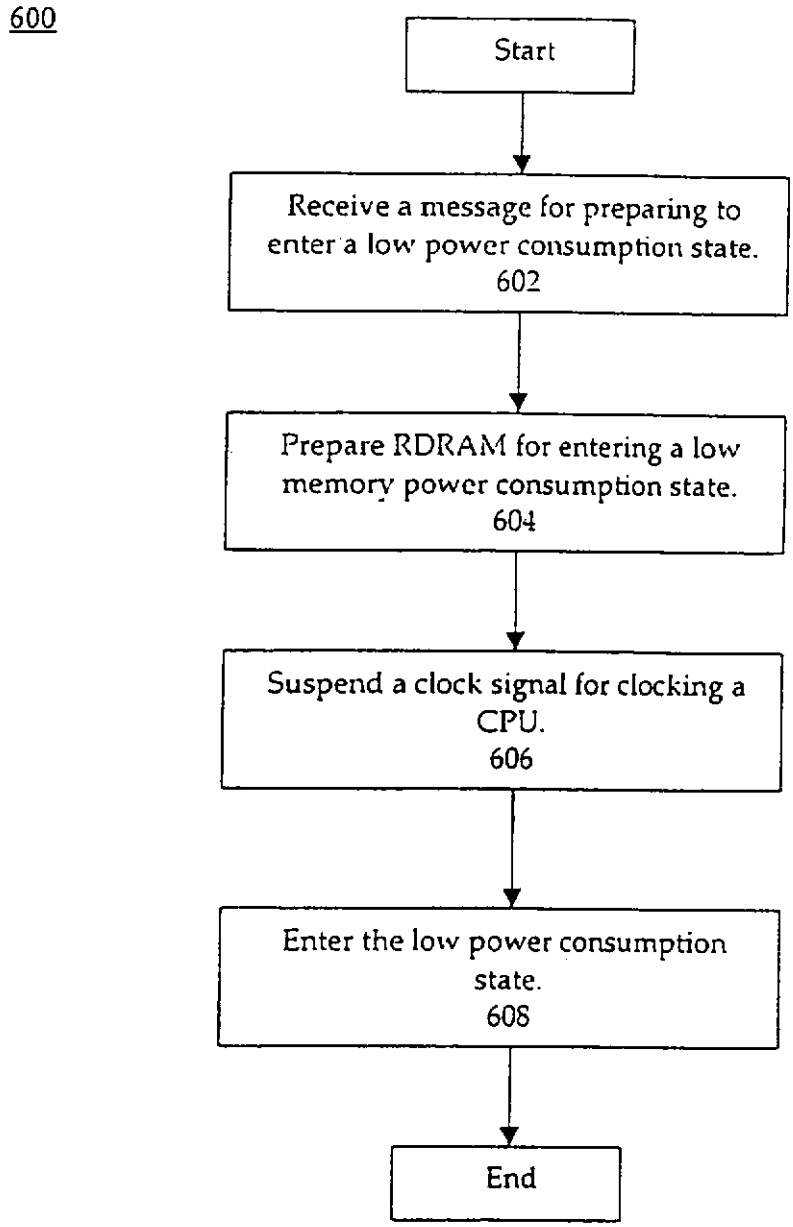

600
Start
Receive a message for preparing to enter a low power consumption state.
602
Prepare RDRAM for entering a low memory power consumption state.
604
Suspend a clock signal for clocking a CPU.
606
Enter the low power consumption state.
608
End

FIG. 6

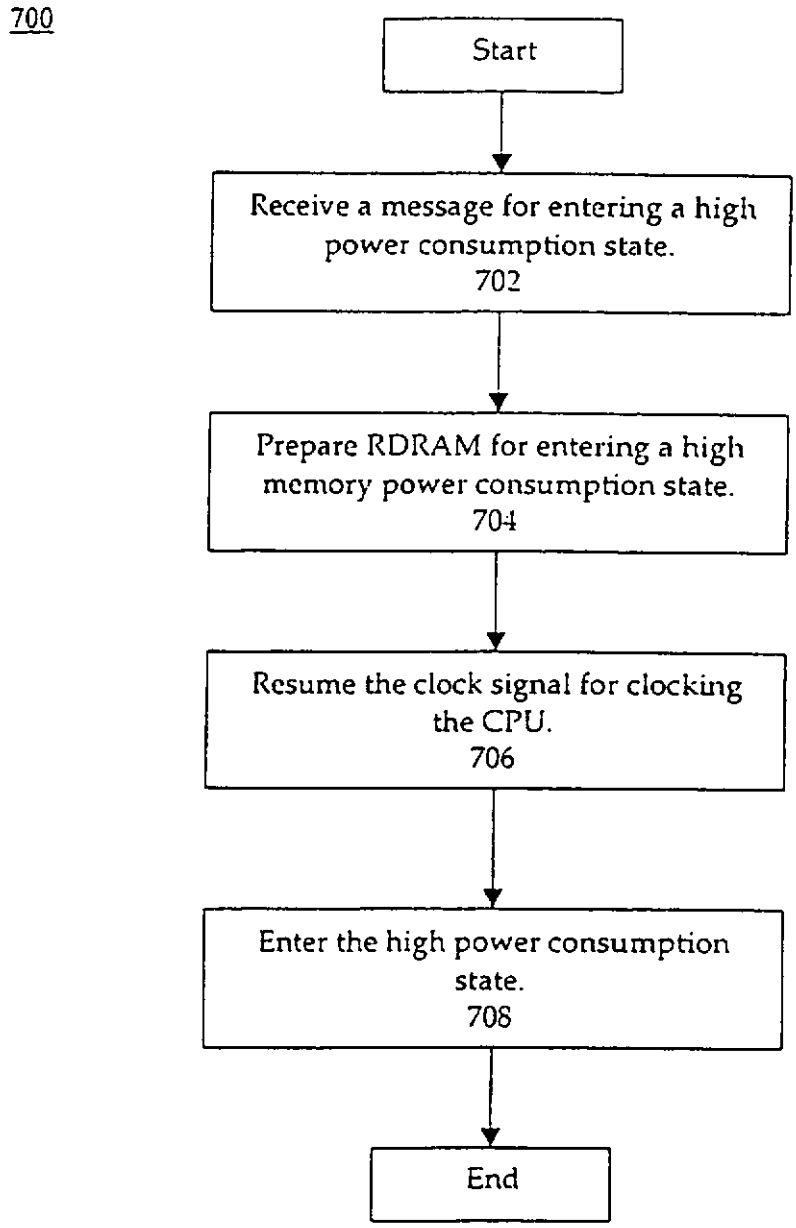

700
Start
Receive a message for entering a high power consumption state.
702
Prepare RDRAM for entering a high memory power consumption state.
704
Resume the clock signal for clocking the CPU.
706
Enter the high power consumption state.
708
End

FIG. 7

800

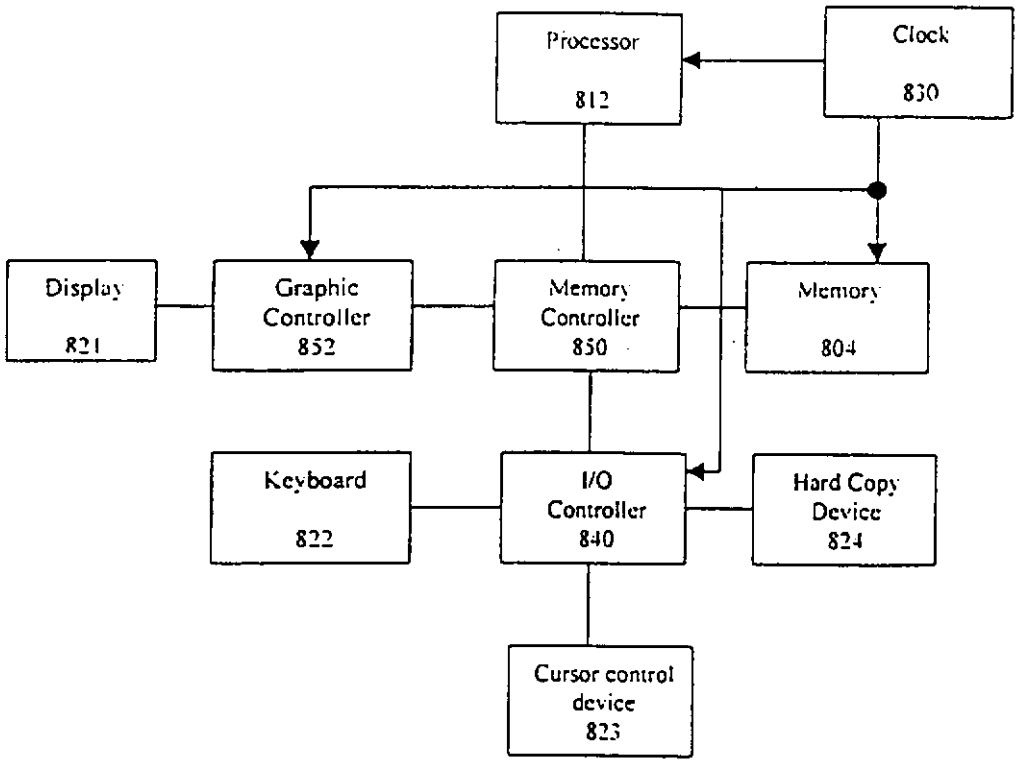

Processor
812
Clock
830
Display
821
Graphic
Controller
852
Memory
Controller
850
Memory
804
Keyboard
822
I/O
Controller
840
Hard Copy
Device
824
Cursor control
device
823

FIG. 8

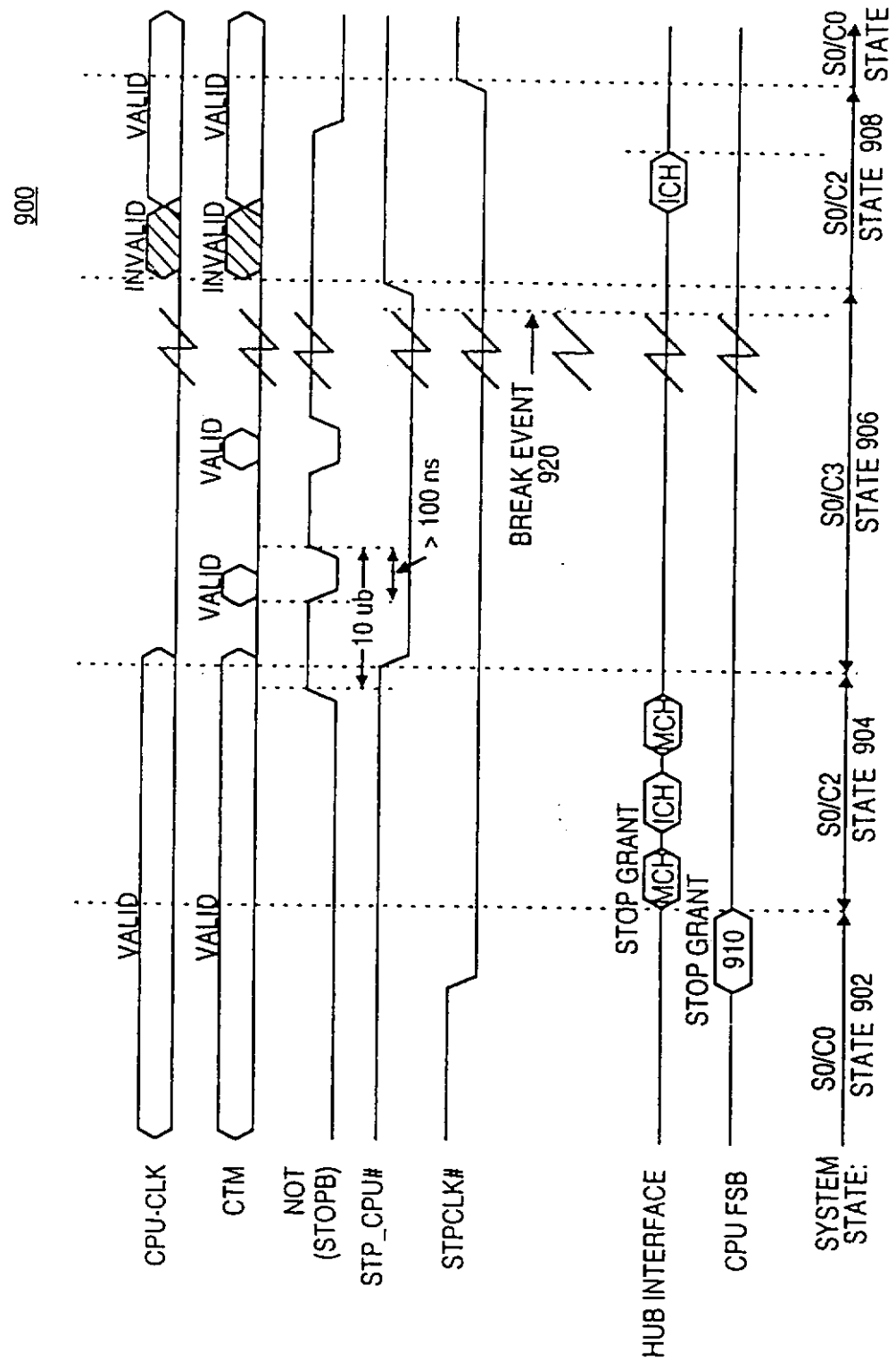

900
CPU-CLK
CTM
NOT (STOPB)
STP_CPU#
STPCLK#
HUB INTERFACE
CPU FSB
SYSTEM STATE:
VALID
INVALID
10 us
> 100 ns
BREAK EVENT 920
STOP GRANT
MCH
ICH
910
S0/C0
STATE 902
S0/C2
STATE 904
S0/C3
STATE 906
S0/C2
STATE 908
S0/C0
STATE
S0/C0 -> S0/C3 -> S0/C0 STATE

FIG. 9

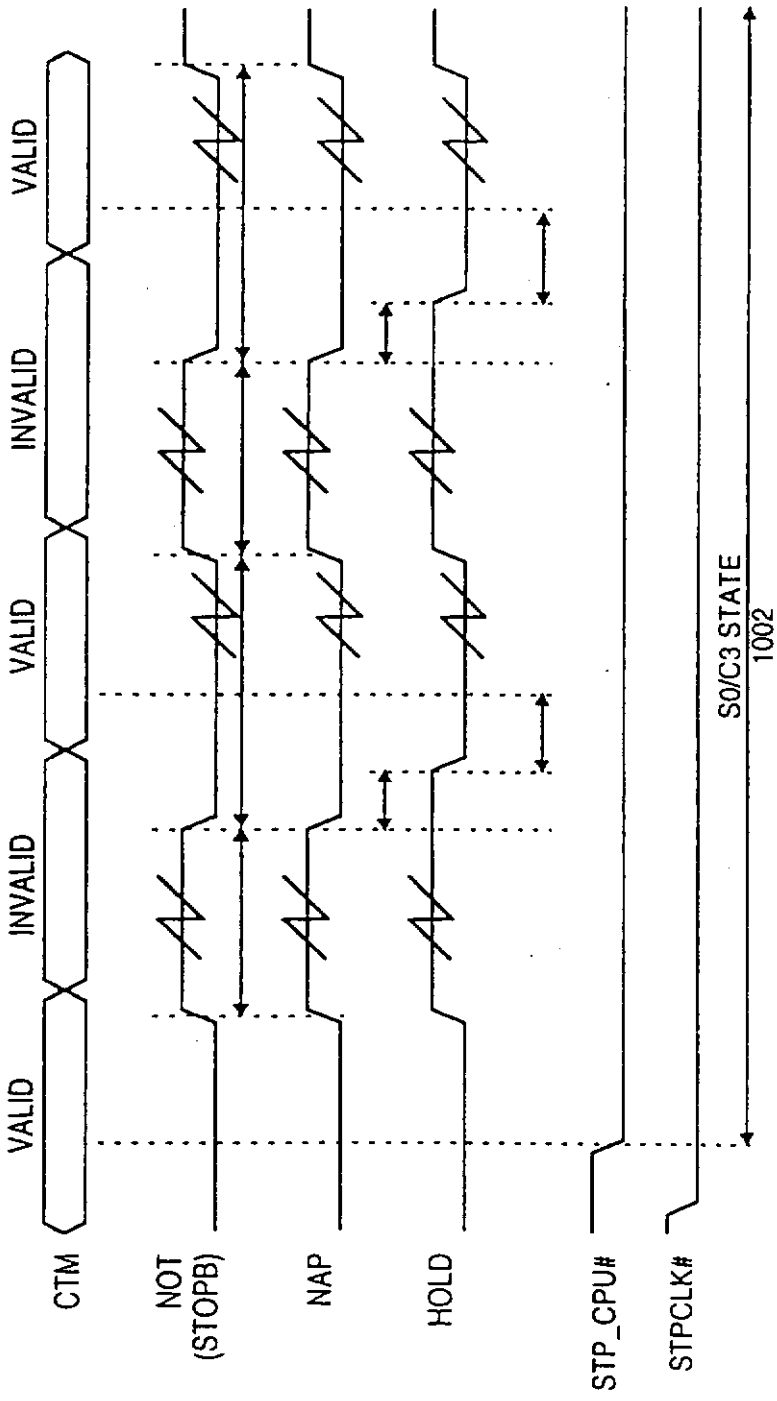

VALID
INVALID
VALID
INVALID
VALID
CTM
NOT (STOPB)
NAP
HOLD
STP_CPU#
STPCLK#
S0/C3 STATE
1002

FIG. 10

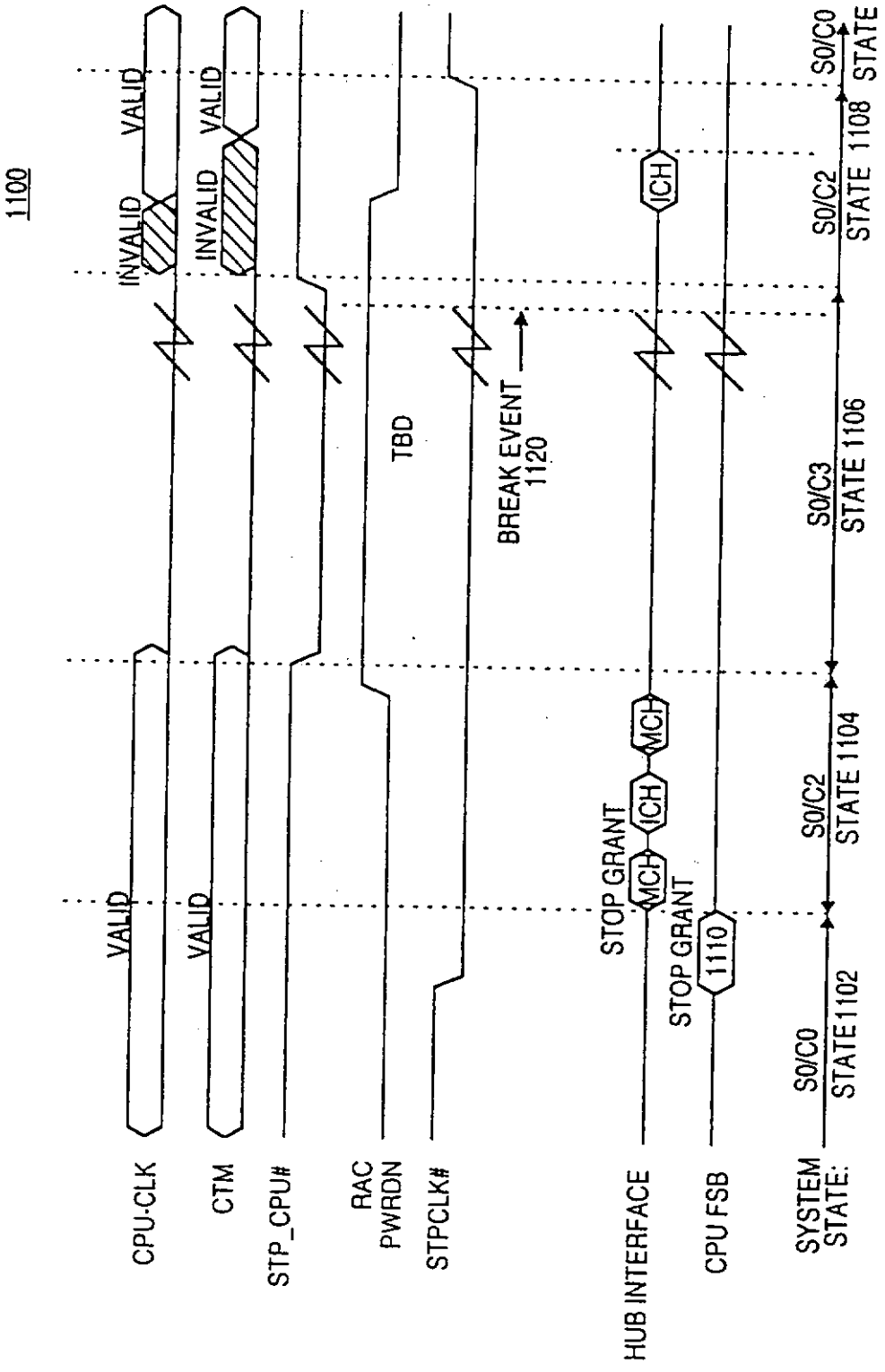

1100
CPU-CLK
CTM
STP_CPU#
RAC
PWRDN
STPCLK#
HUB INTERFACE
CPU FSB
SYSTEM
STATE:
VALID
INVALID
TBD
BREAK EVENT
1120
STOP GRANT
MCH
ICH
1110
S0/C0
STATE1102
S0/C2
STATE 1104
S0/C3
STATE 1106
S0/C2
STATE 1108
S0/C0
STATE

FIG. 11