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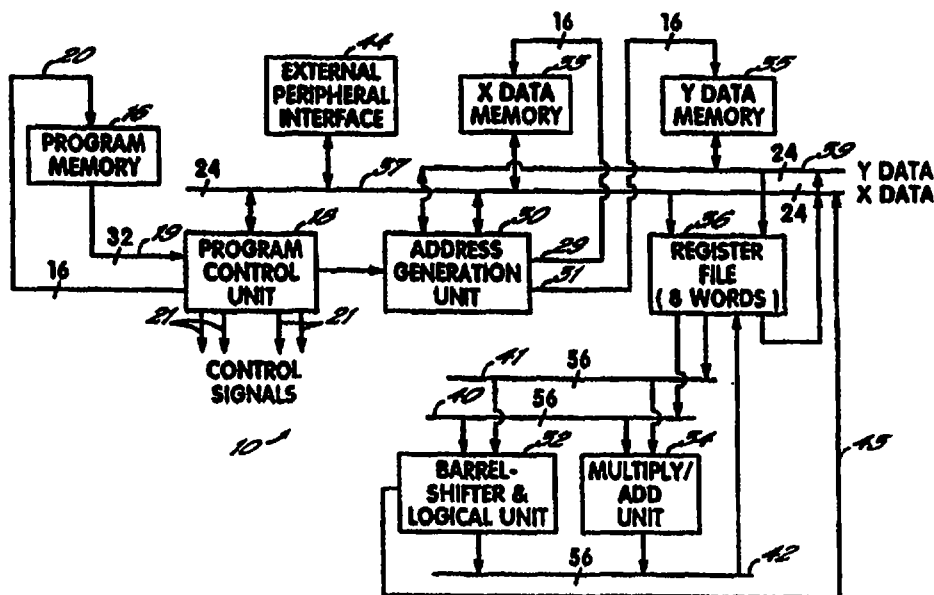
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(54) Title: DIGITAL SIGNAL PROCESSOR PARTICULARLY SUITED FOR DECODING DIGITAL AUDIO



(57) Abstract

A digital signal processor (10) particularly adapted for decoding digital audio. The barrel shifter (32) of the processor includes logical circuitry, so that operations involving a combination of a logical operation and a shift, can be performed in a single pass through the combined barrel shifter/logical unit, rather than requiring separate passes through the barrel shifter and ALU, which would require more instruction cycles. The address generator (30) of the processor, includes circuitry which concatenates the most significant bits of a base address of a table to the least significant bits of an index, to thereby rapidly generate addresses of indexed locations in a table.