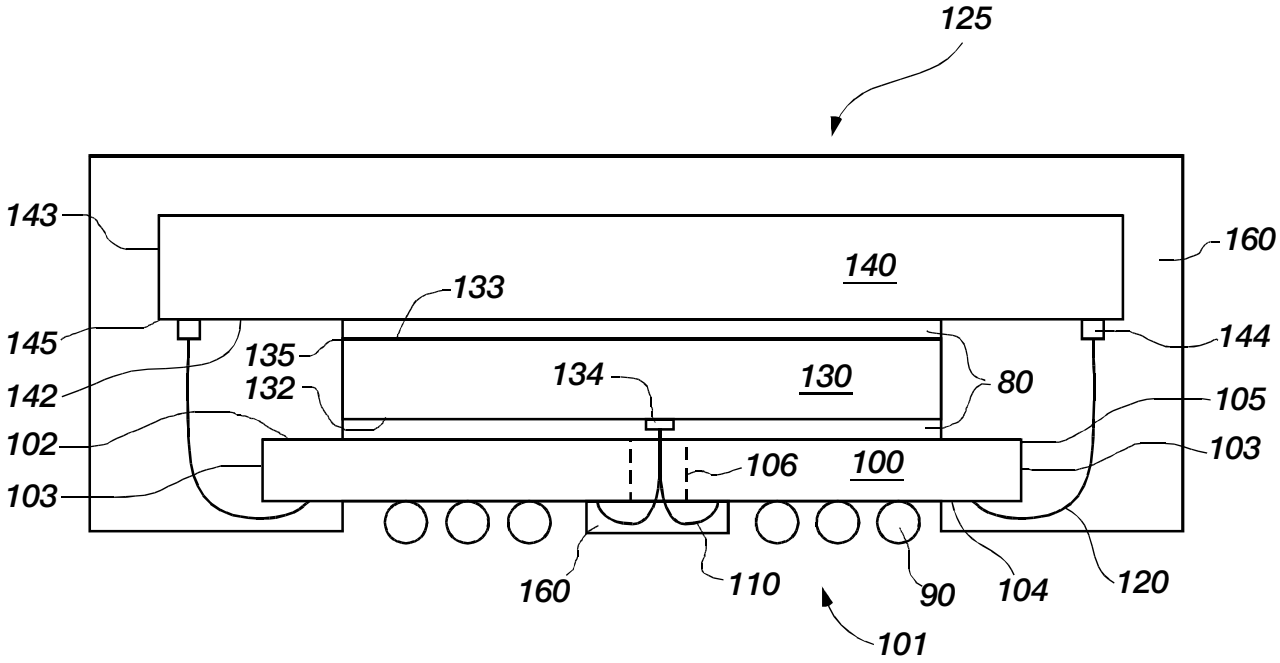

125
143
140
160
133
145
135
144
134
130
80
142
132
105
102
106
100
103
103
160
110
90
104
120
101

FIG. 1A

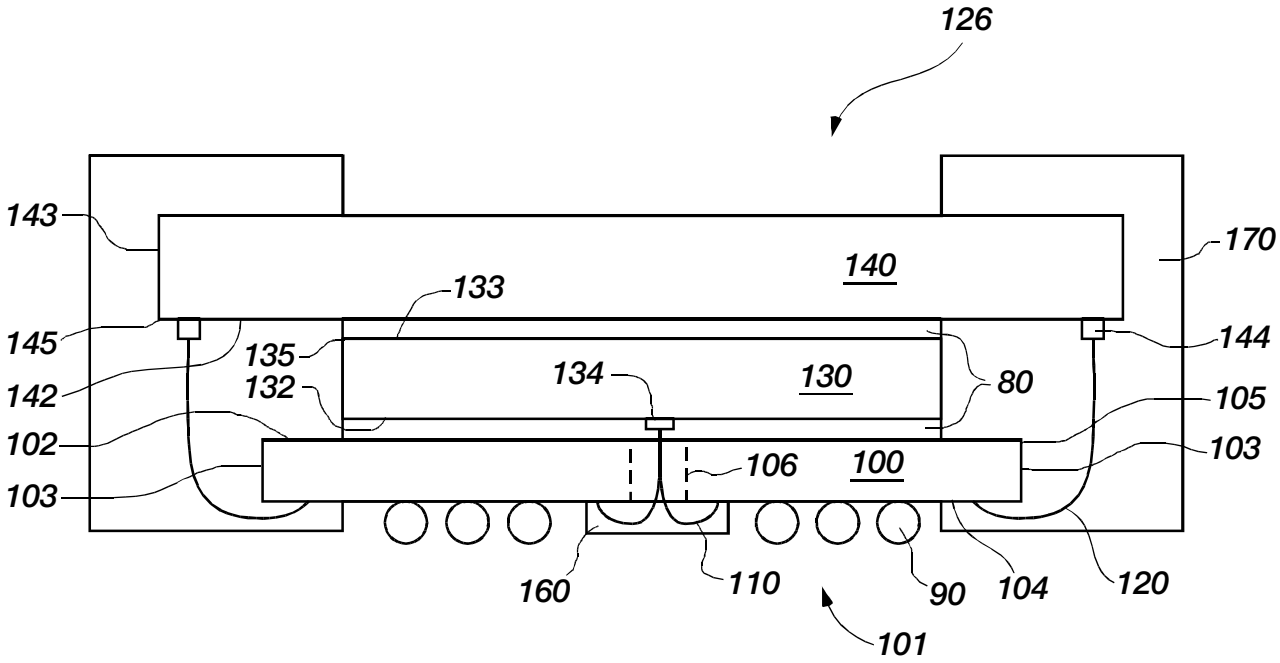

126
143
140
170
133
145
135
144
134
130
80
142
132
105
102
106
100
103
103
160
110
90
104
120
101

FIG. 1B

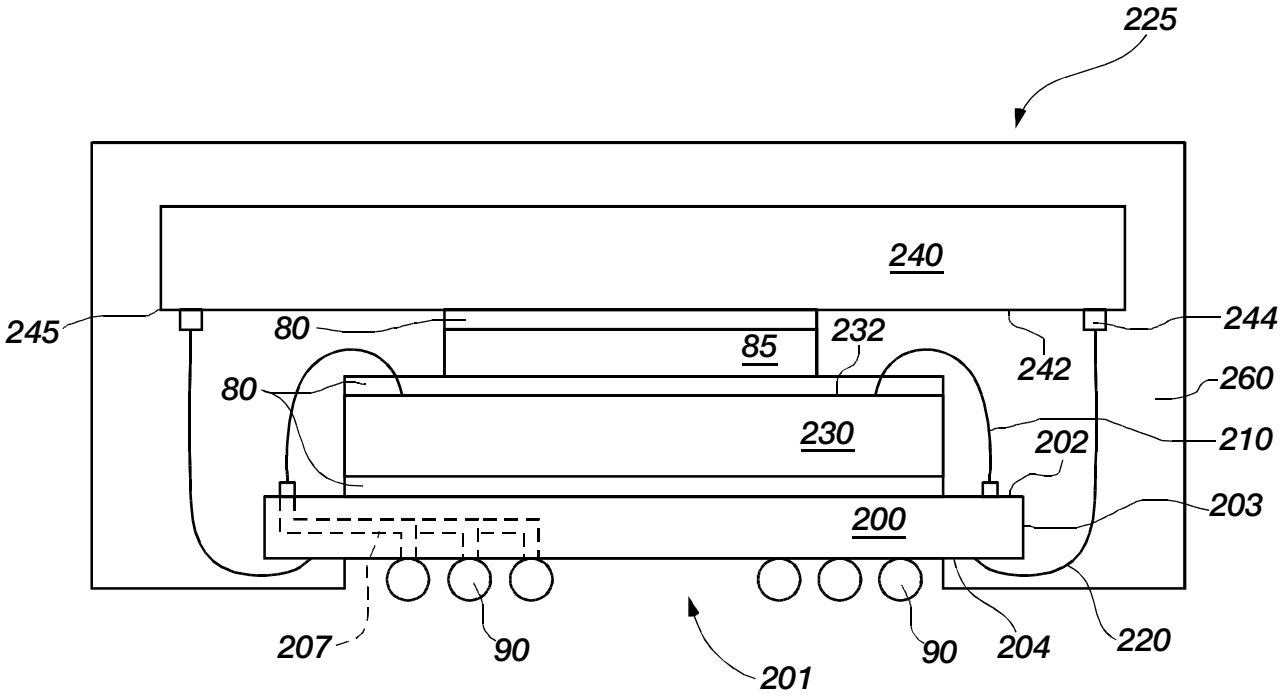

225
240
245
80
85
232
242
244
80
260
230
210
202
200
203
207
90
201
90
204
220

FIG. 2A

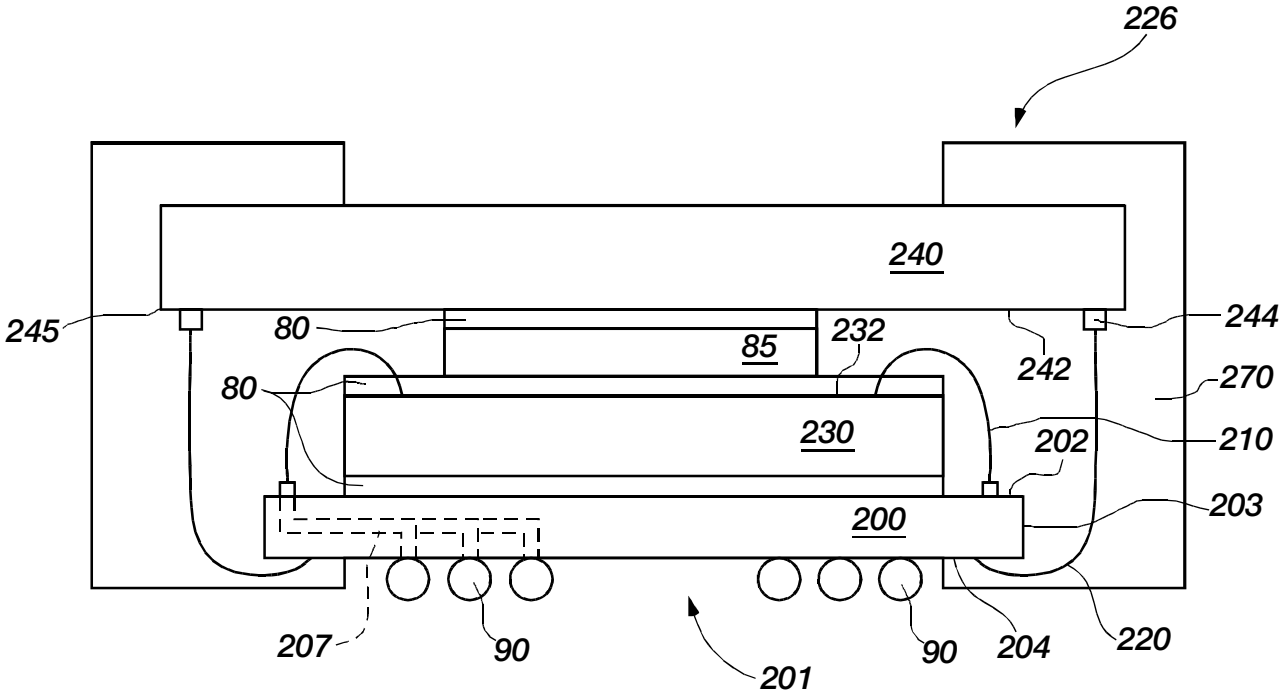

226
240
245
80
85
232
242
244
80
270
230
210
202
200
203
207
90
201
90
204
220

FIG. 2B

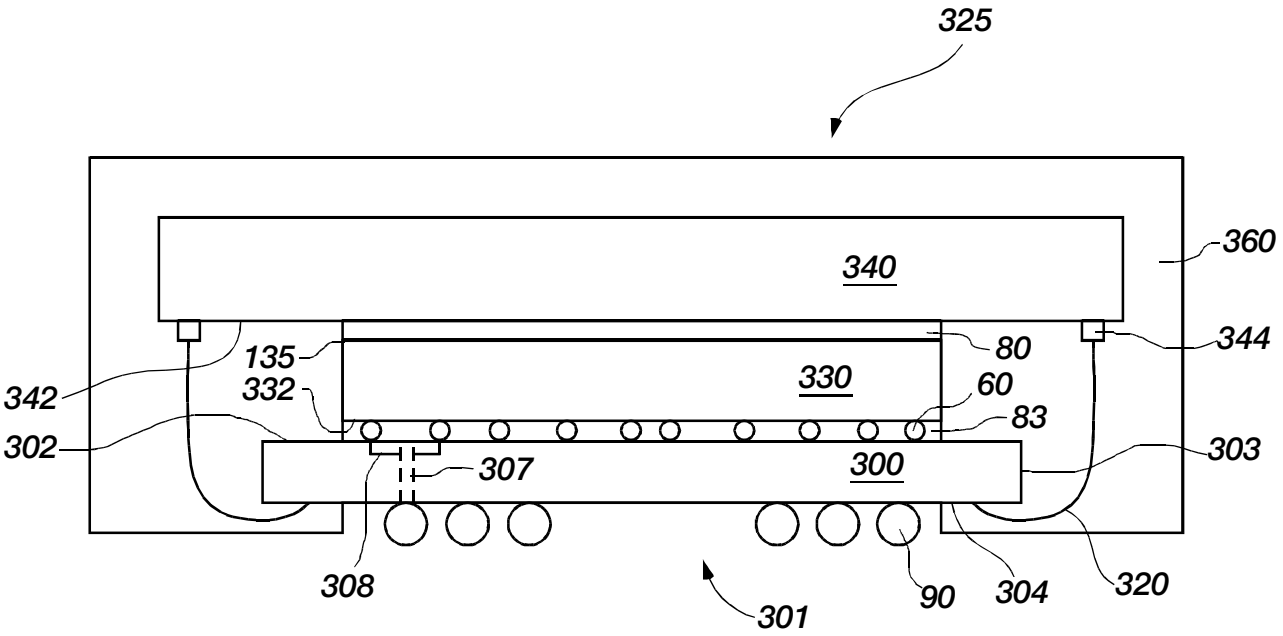

325
360
340
344
80
135
330
60
342
332
83
302
303
307
300
308
301
90
304
320

FIG. 3A

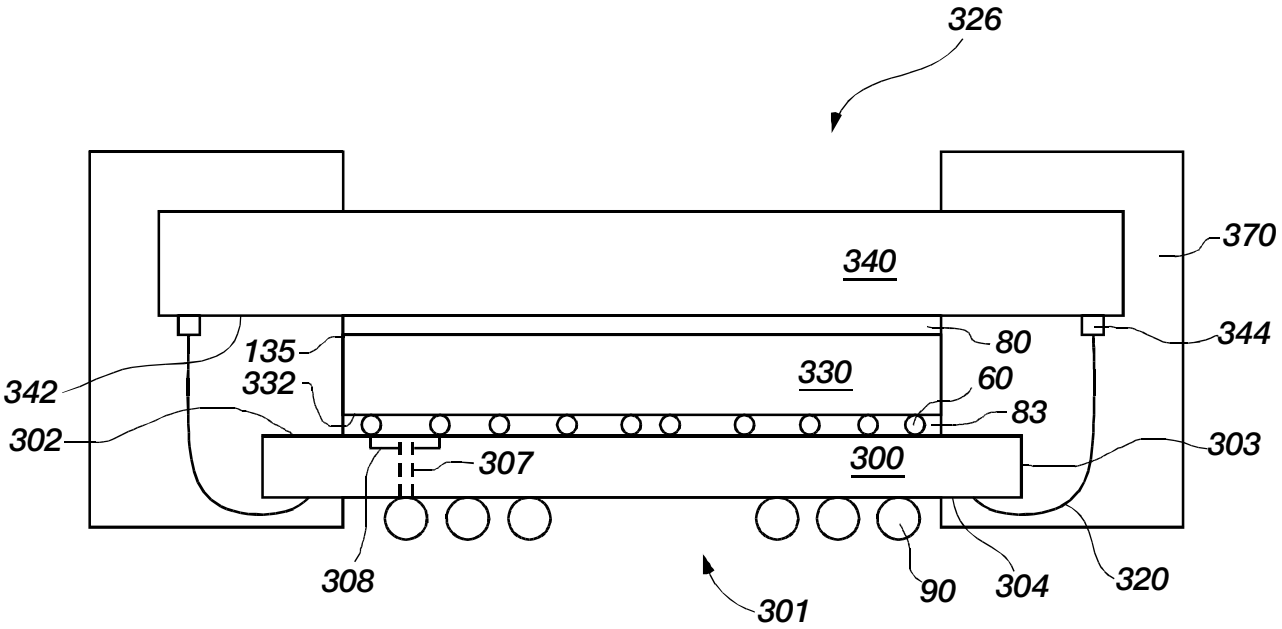

326
370
340
344
80
135
330
60
342
332
83
302
303
307
300
308
301
90
304
320

FIG. 3B

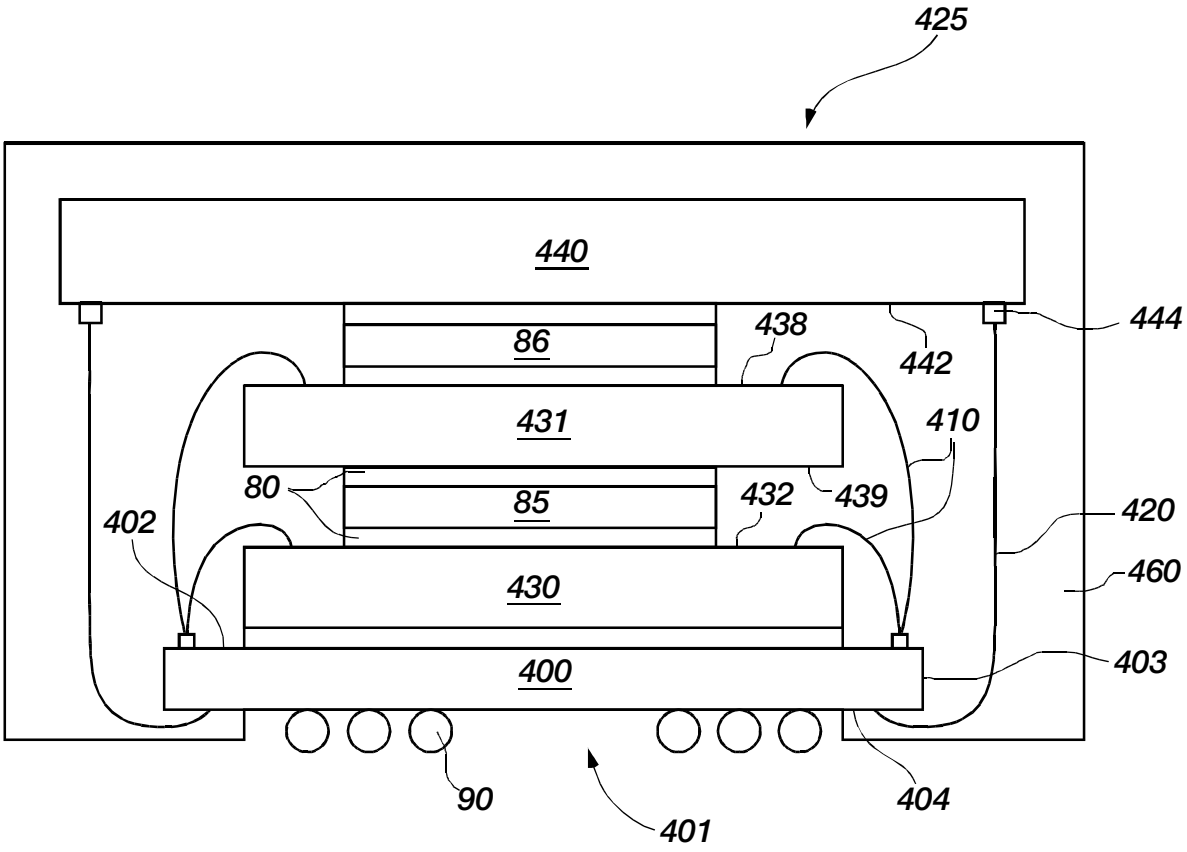

425
440
444
86
438
442
431
410
80
85
432
439
420
402
430
460
400
403
90
401
404

FIG. 4A

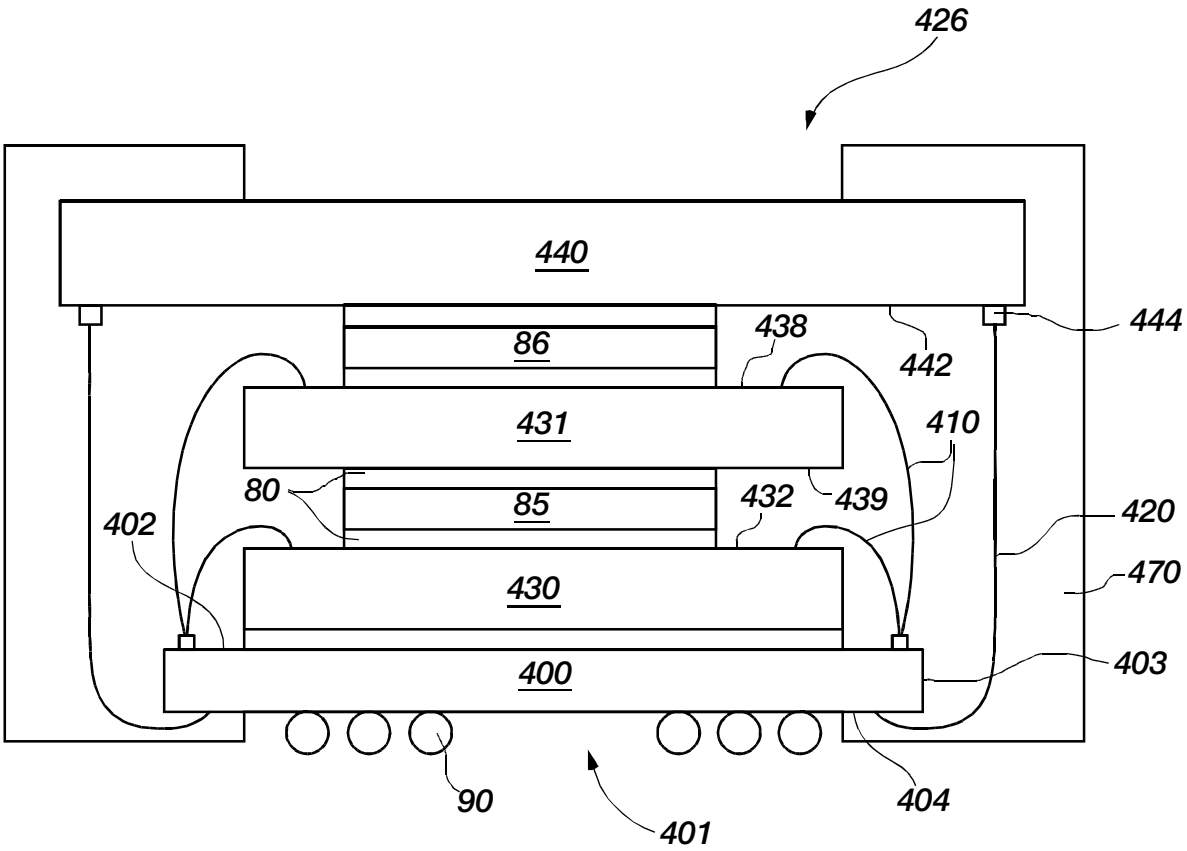

426
440
444
86
438
442
431
410
80
85
432
439
420
402
430
470
400
403
90
401
404

FIG. 4B

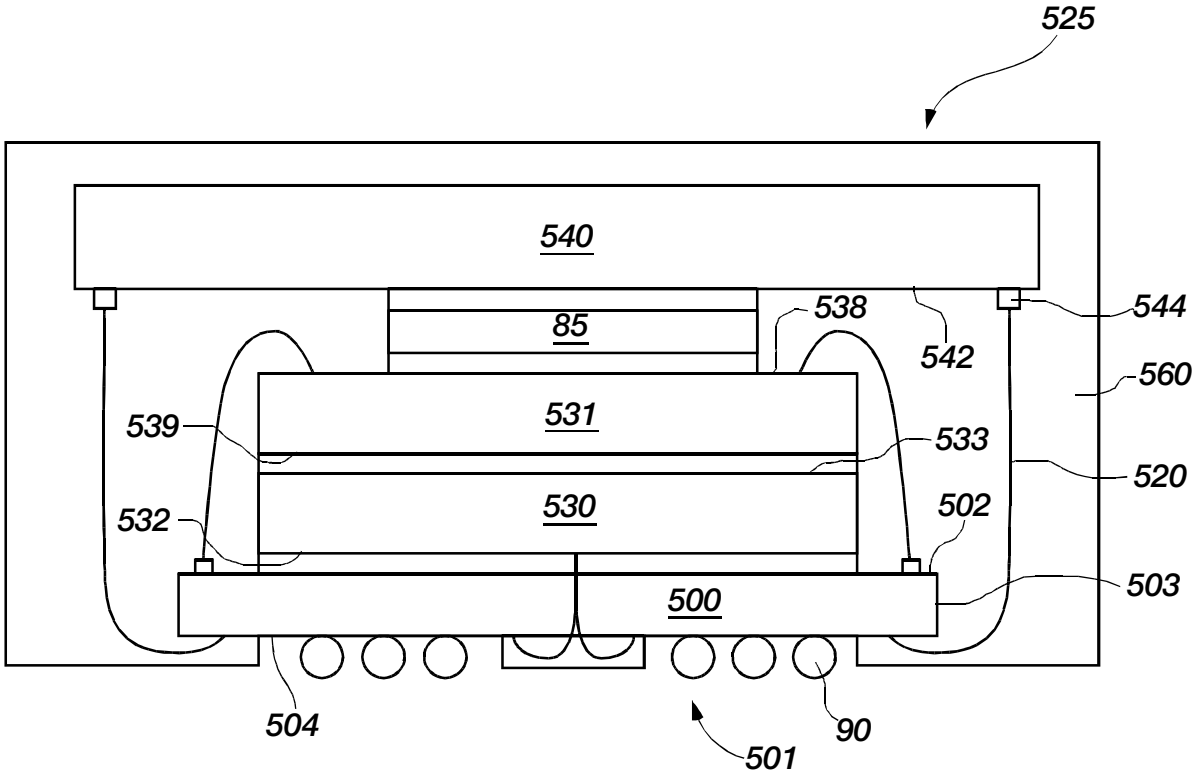

525
540
85
538
542
544
560
539
531
533
520
532
530
502
503
500
504
501
90

FIG. 5A

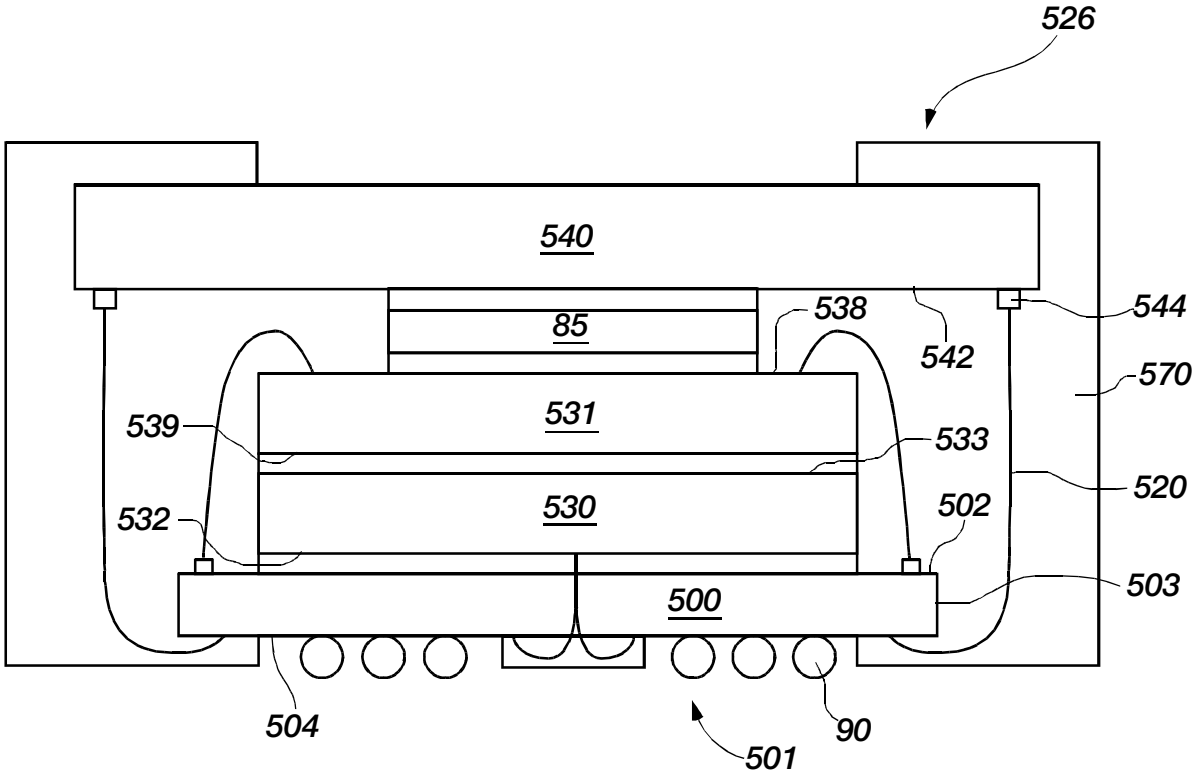

526
540
85
538
542
544
570
539
531
533
520
532
530
502
503
500
504
501
90

FIG. 5B

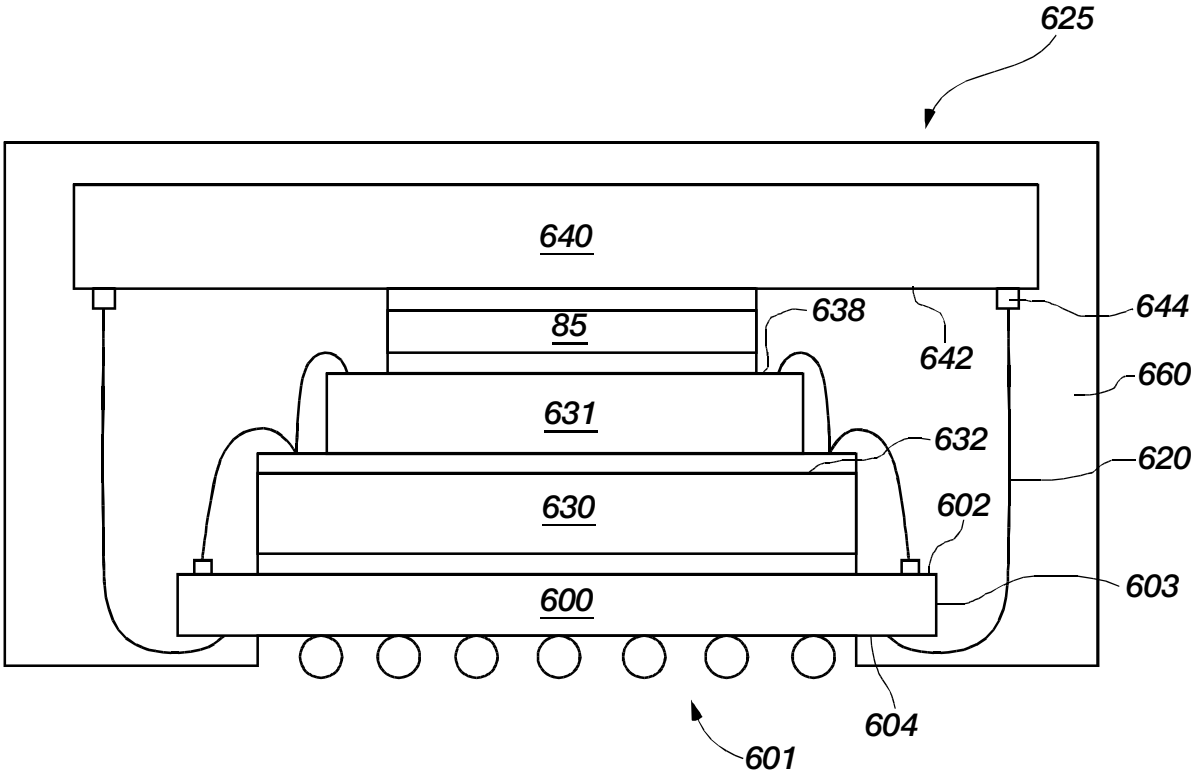

625
640
85
638
642
644
660
631
632
620
630
602
603
600
604
601

FIG. 6A

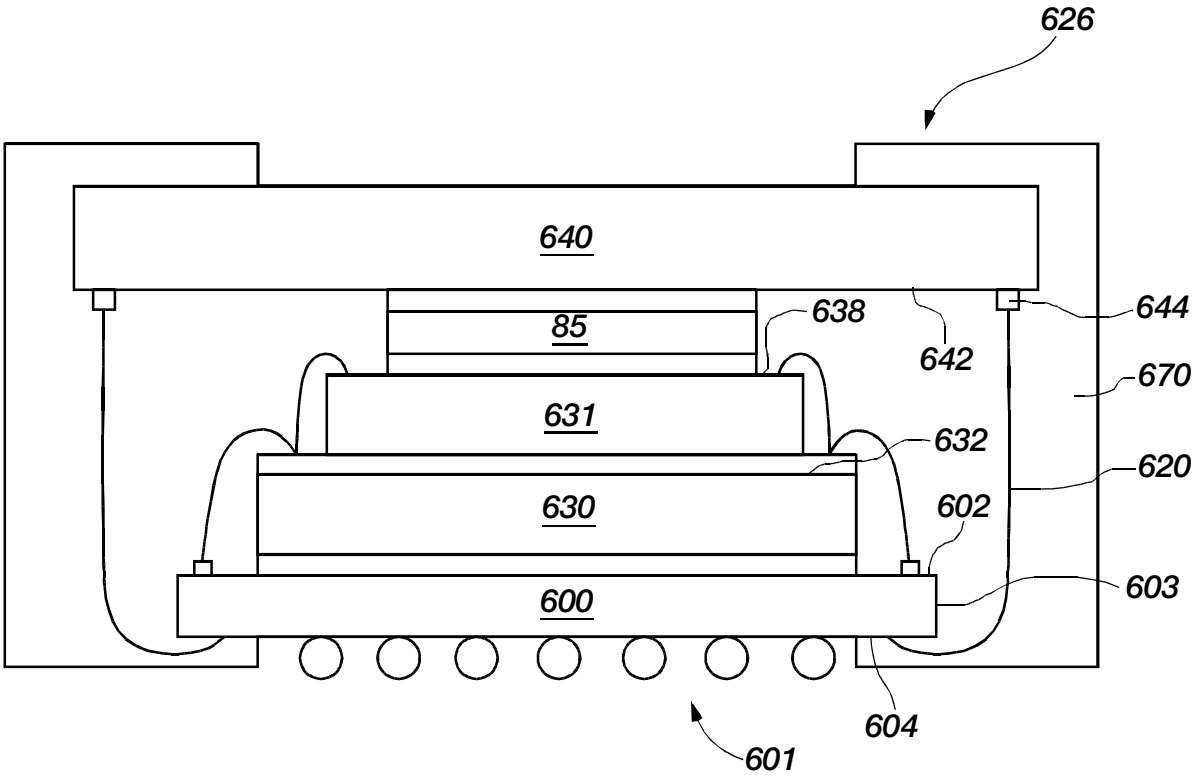

626
640
85
638
642
644
670
631
632
620
630
602
603
600
604
601

FIG. 6B

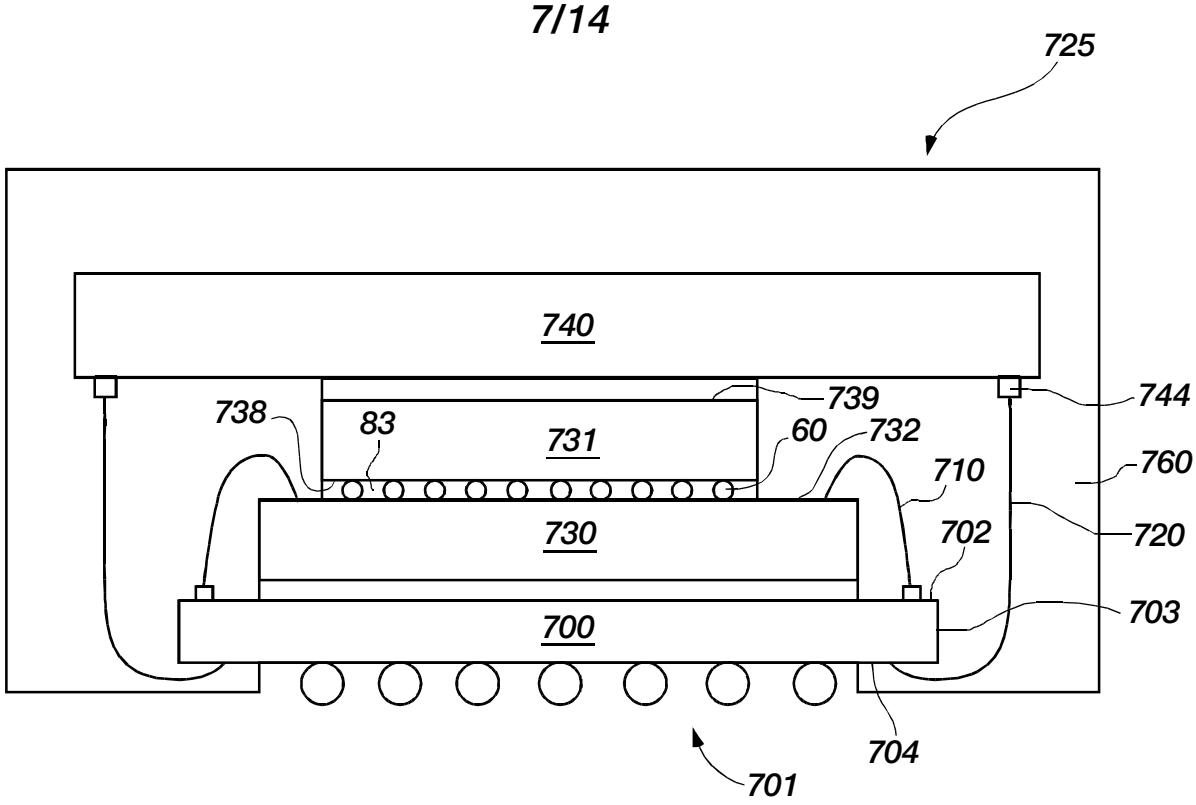

725
740
738
83
731
739
60
732
744
760
710
730
720
702
703
700
704
701

FIG. 7A

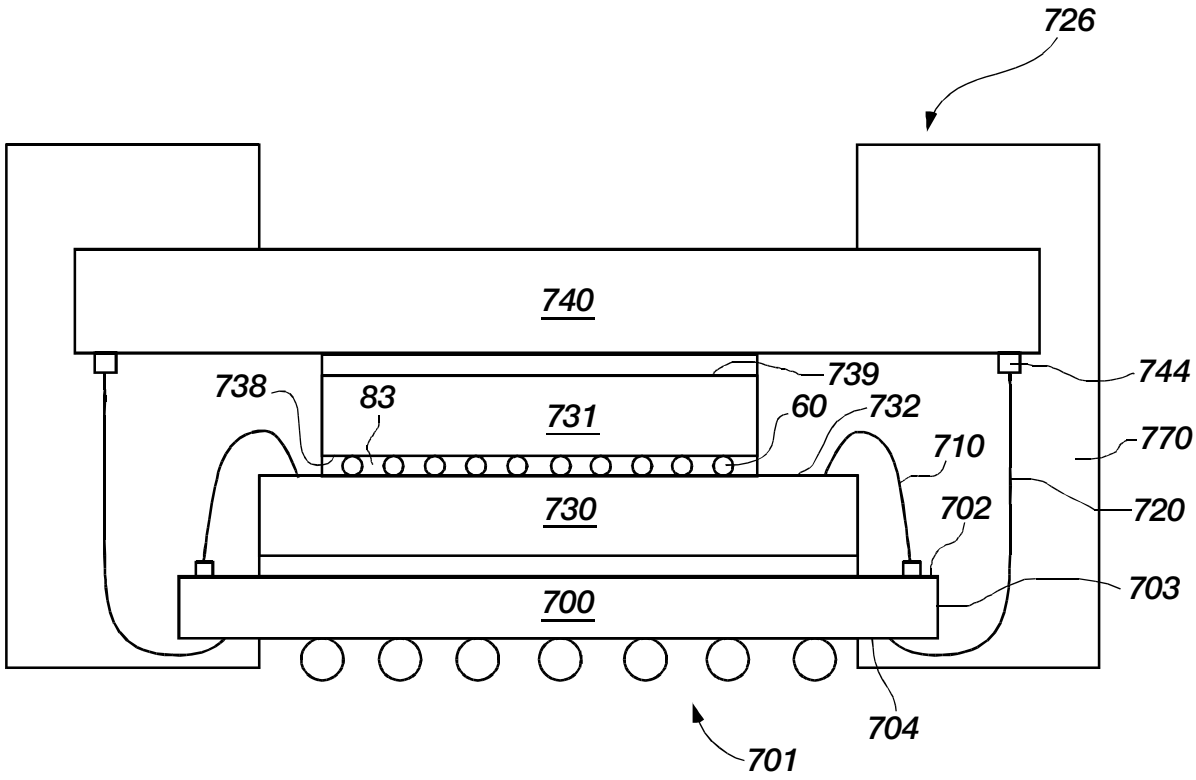

726
740
738
83
731
739
60
732
744
770
710
730
720
702
703
700
704
701

FIG. 7B

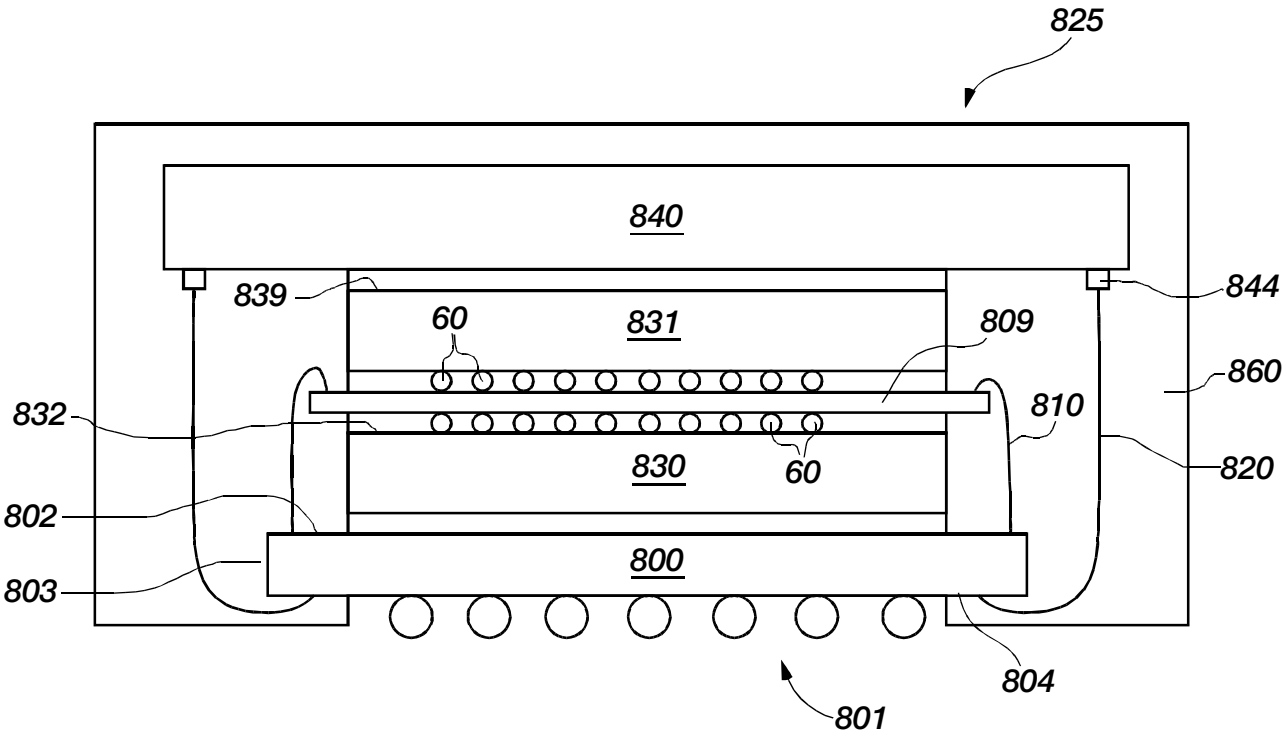

825
840
839
60
831
809
844
860
832
810
830
60
820
802
800
803
804
801

FIG. 8A

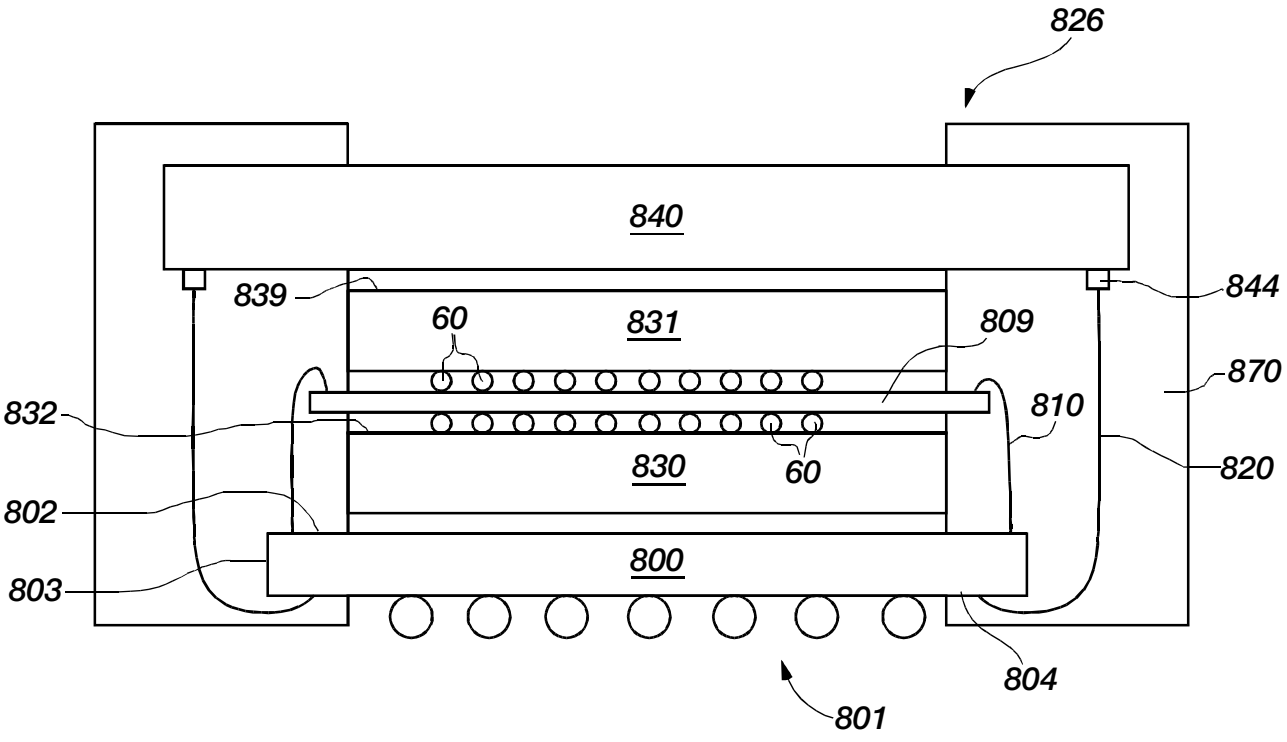

826
840
839
60
831
809
844
870
832
810
830
60
820
802
800
803
804
801

FIG. 8B

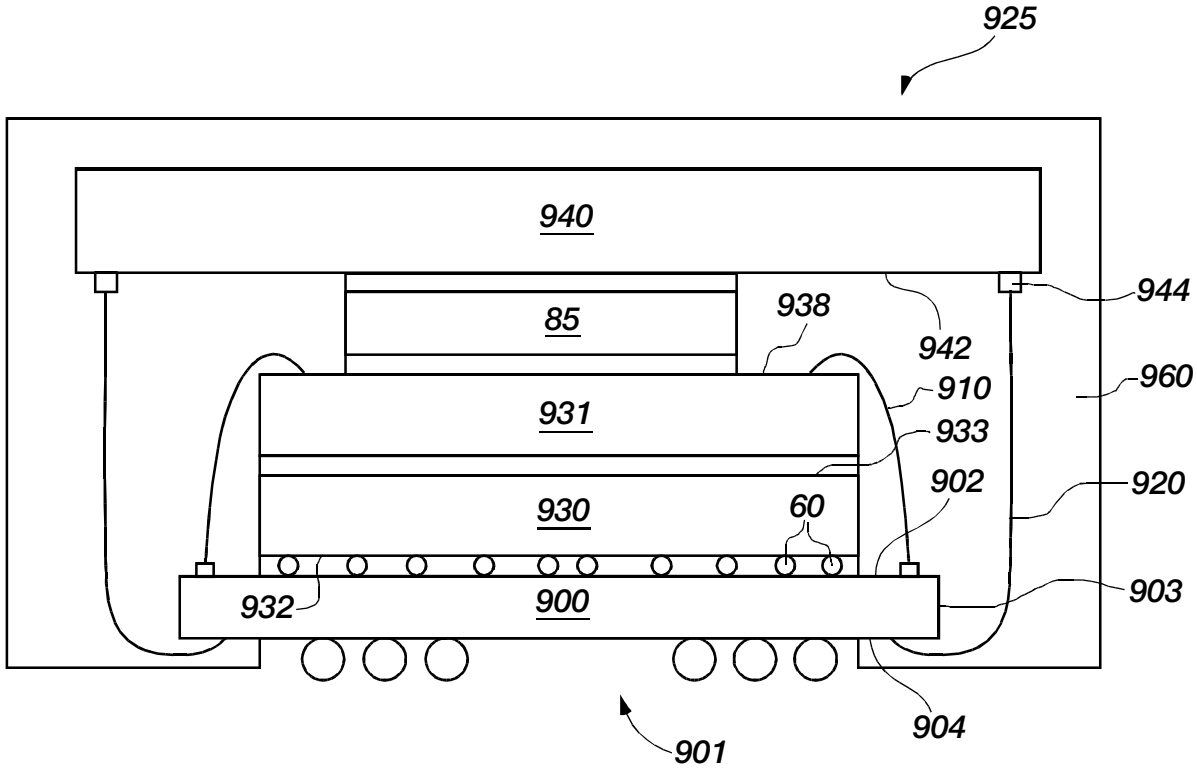

925
940
85
938
942
944
960
931
910
933
902
920
930
60
932
900
903
904
901

FIG. 9A

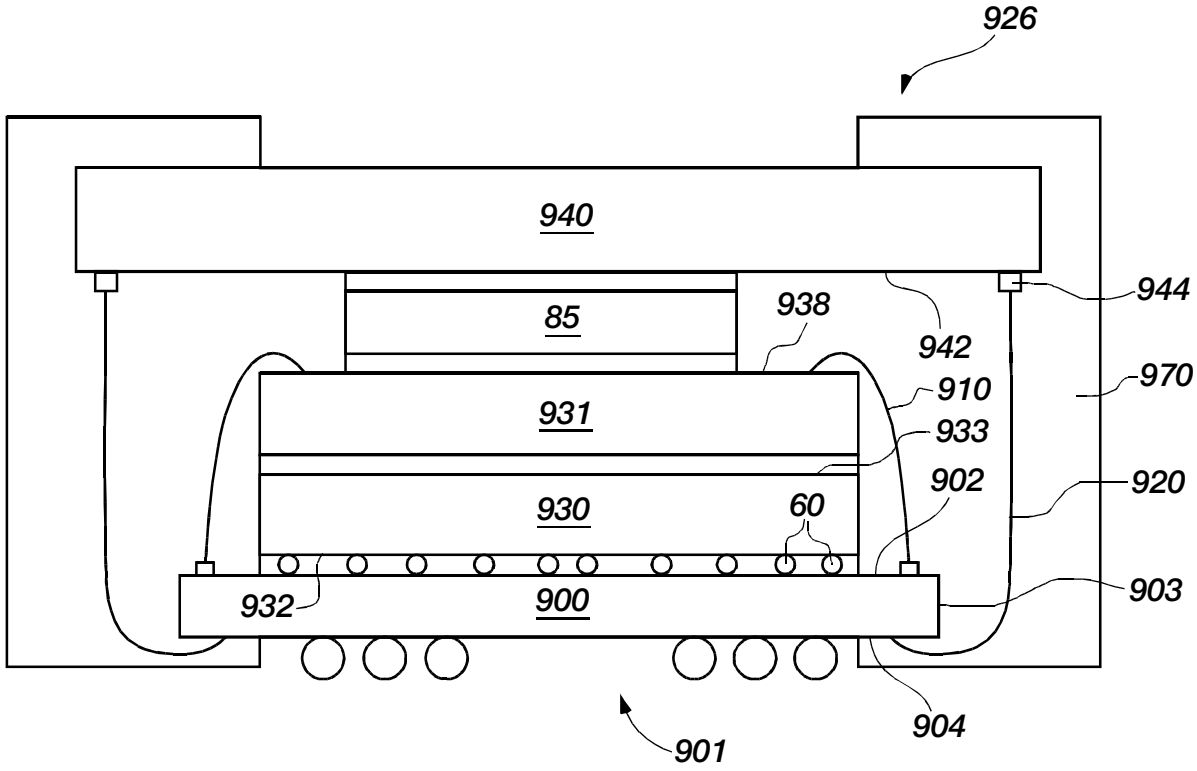

926
940
85
938
942
944
970
931
910
933
902
920
930
60
932
900
903
904
901

FIG. 9B

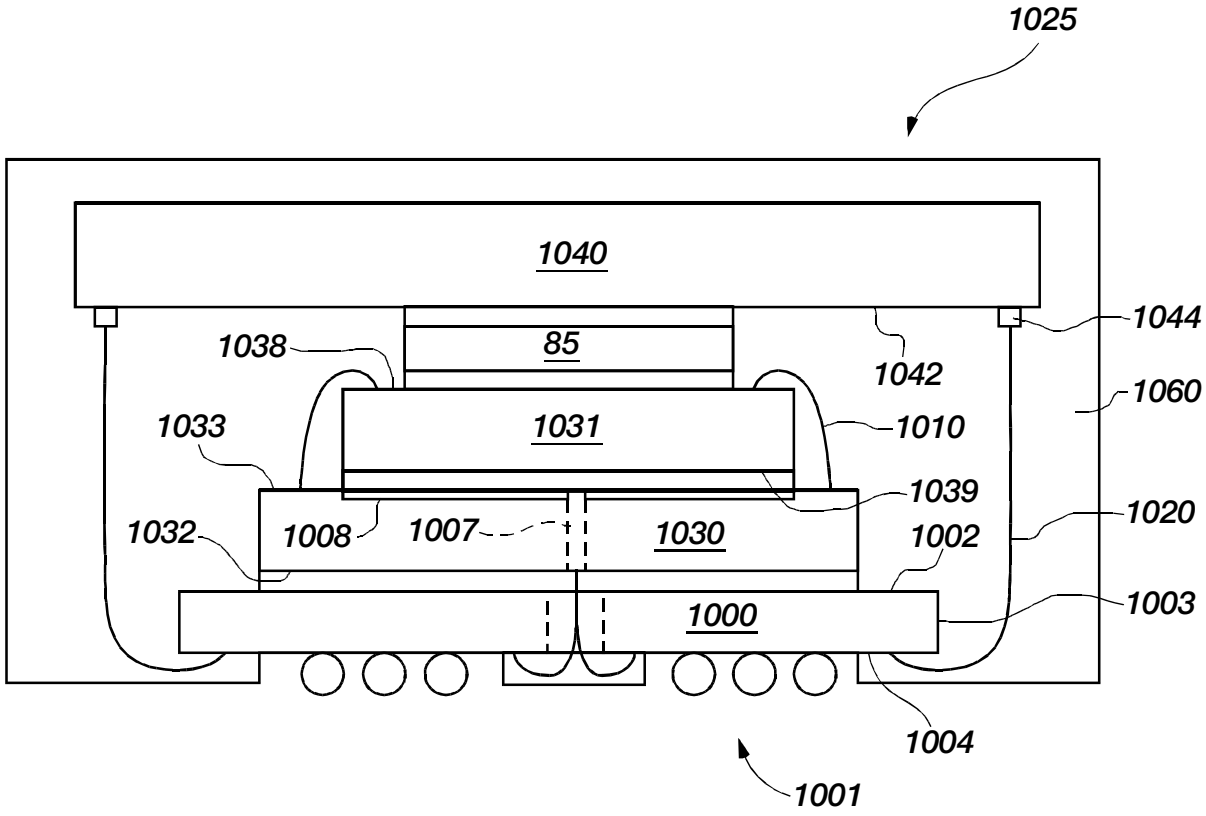


FIG. 10A

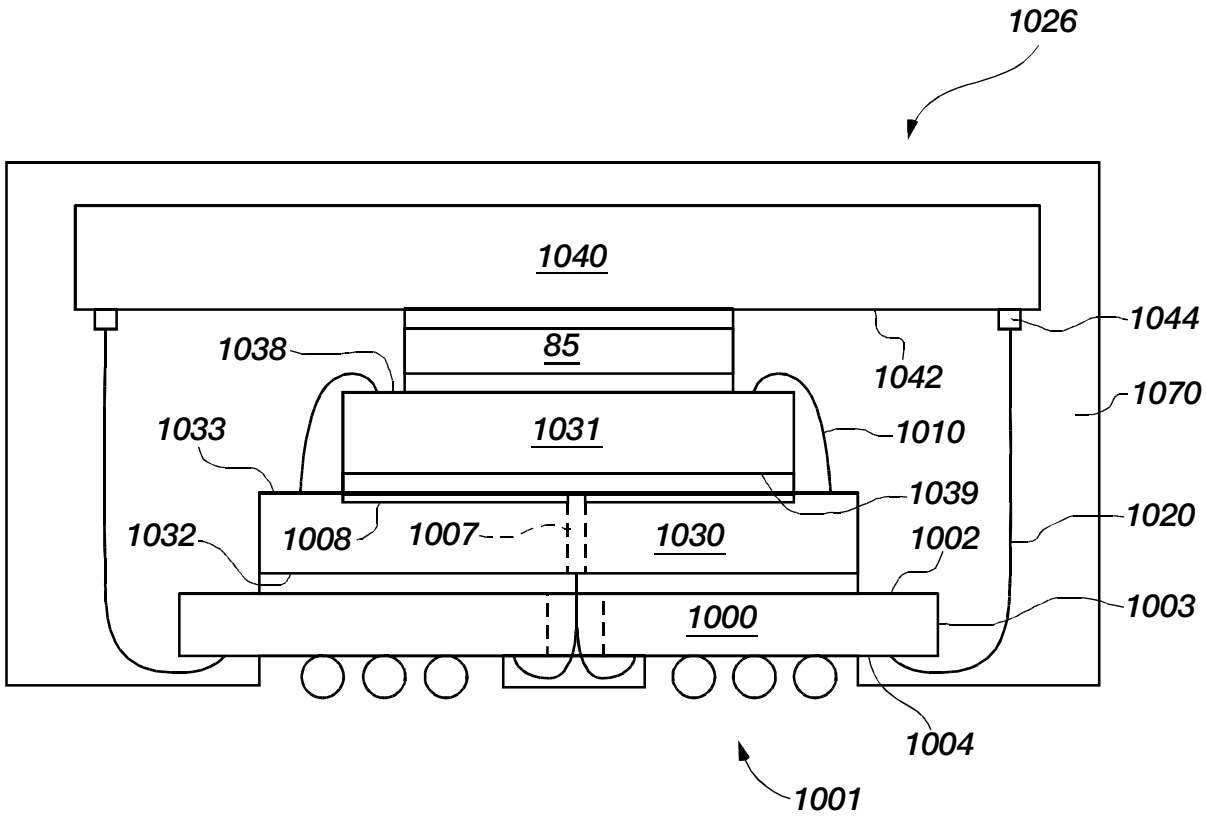


FIG. 10B

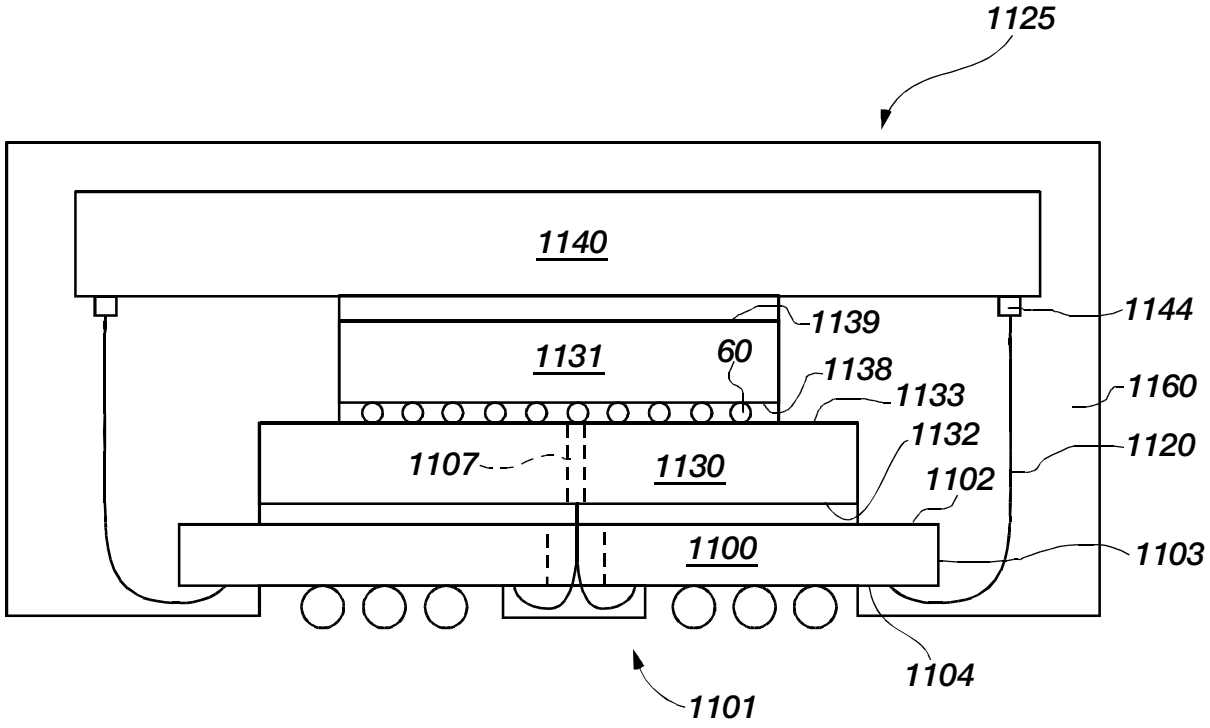


FIG. 11A

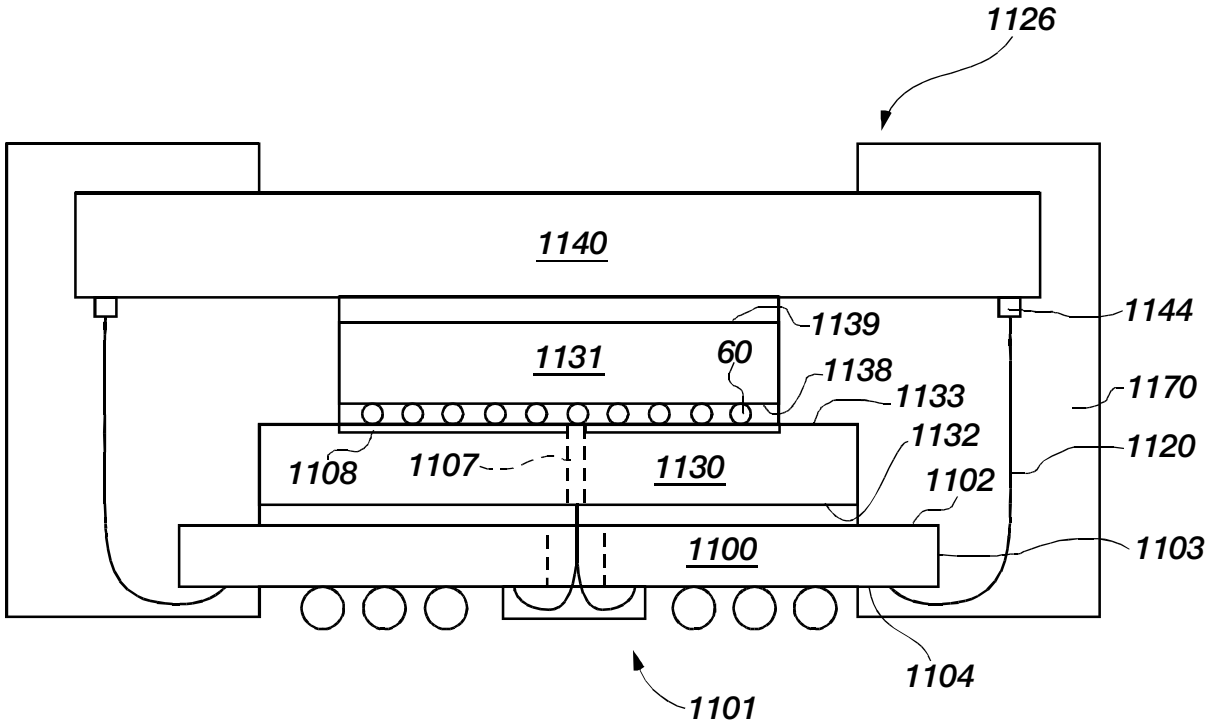


FIG. 11B

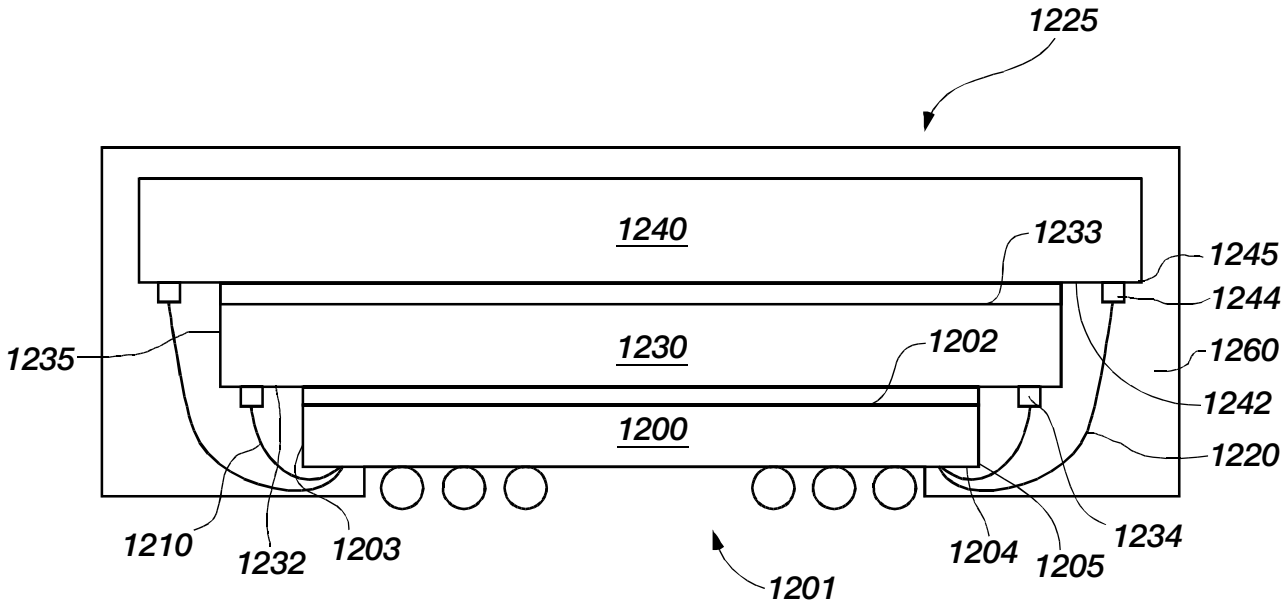


FIG. 12A

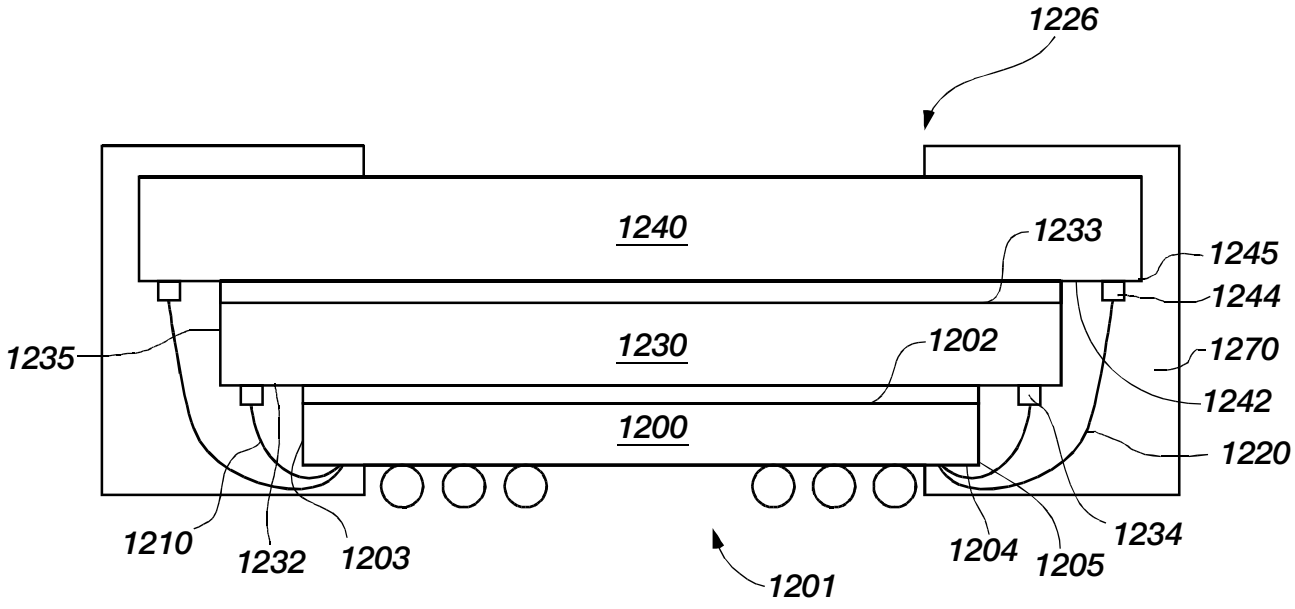


FIG. 12B

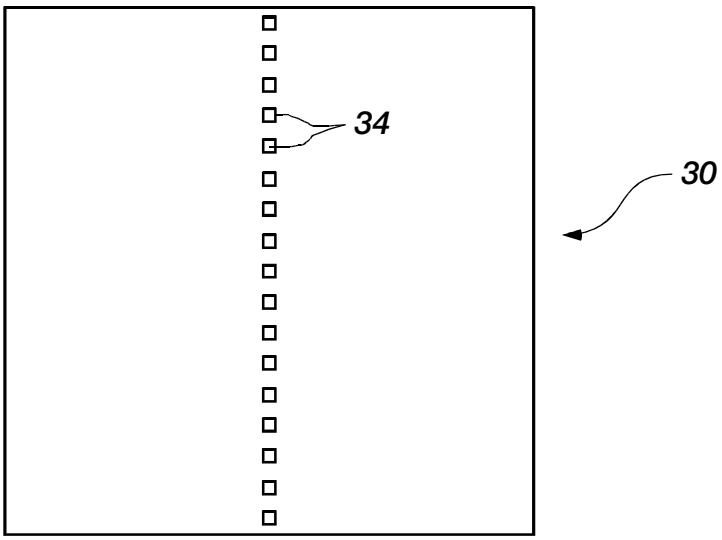


FIG. 13

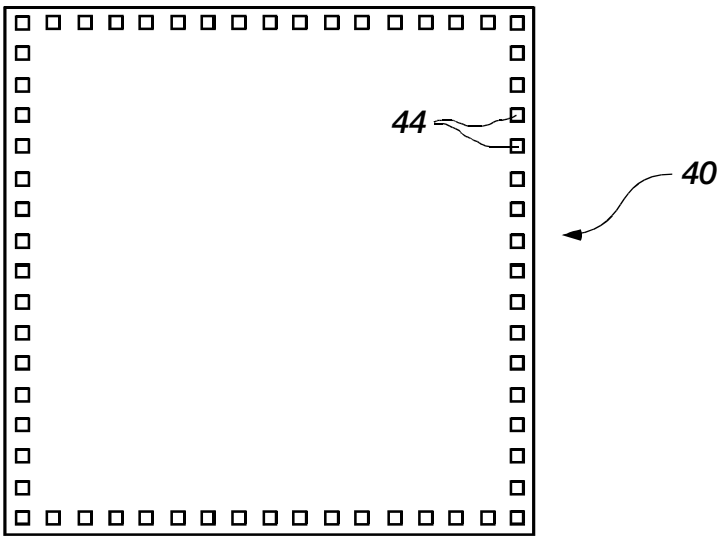


FIG. 14

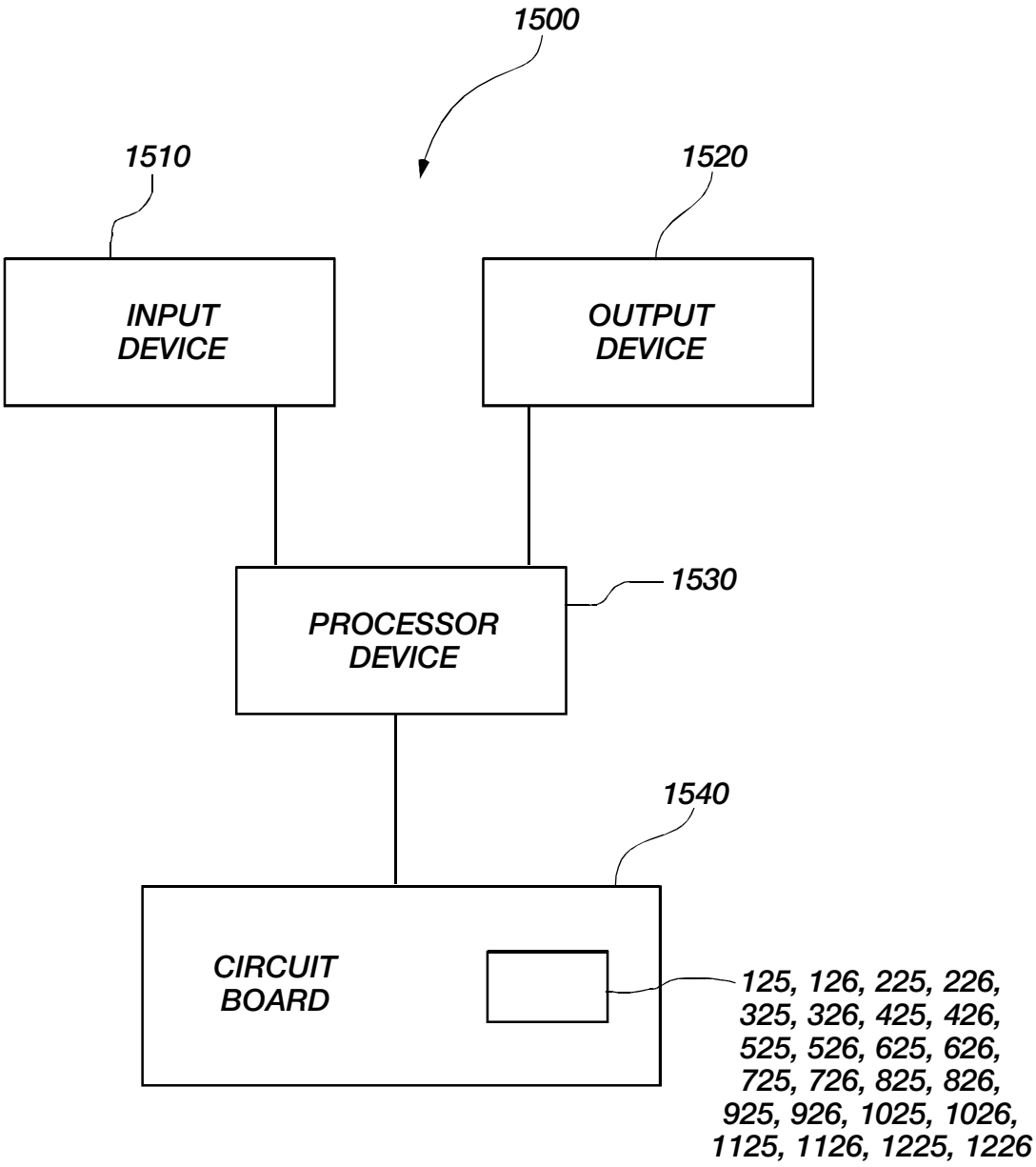

1500
1510
1520
INPUT DEVICE
OUTPUT DEVICE
1530
PROCESSOR DEVICE
1540
CIRCUIT BOARD
125, 126, 225, 226,
325, 326, 425, 426,
525, 526, 625, 626,
725, 726, 825, 826,
925, 926, 1025, 1026,
1125, 1126, 1225, 1226

FIG. 15