

**COMMUNICATION SYSTEM, COMMUNICATION APPARATUS,
AND COMMUNICATION METHOD**

ABSTRACT

Multicast communication is performed in IEEE1394 standard communication. A first comparator outputs 1 or 0 to an AND circuit and a transmission and receiving section in accordance with the value of a bus ID field extracted from a packet by a bus ID extraction circuit. An address decoder converts the value of a physical ID field extracted by a physical ID extraction circuit to an address of a multicast address control register. The multicast address control register outputs the value of a storage area indicated by that address to the AND circuit. A second comparator outputs 1 or 0 to the AND circuit in accordance with the value of an offset address field extracted by an offset-address extraction circuit. The AND circuit outputs the logical AND of all the supplied values to the transmission and receiving section. The transmission and receiving section receives packets in accordance with the value from the AND circuit and the first comparator of a determination circuit.

Figure 14