



- (51) International Patent Classification:
G06F 9/455 (2018.01)
- (21) International Application Number:
PCT/EP2024/076533
- (22) International Filing Date:
20 September 2024 (20.09.2024)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
2314388.6 20 September 2023 (20.09.2023) GB
2314389.4 20 September 2023 (20.09.2023) GB
2316660.6 31 October 2023 (31.10.2023) GB
2403869.7 18 March 2024 (18.03.2024) GB
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- (81) Designated States (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CV, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IQ, IR, IS, IT, JM, JO, JP, KE, KG,

(54) Title: MULTIKERNEL ARCHITECTURE AND VIRTUAL MACHINES

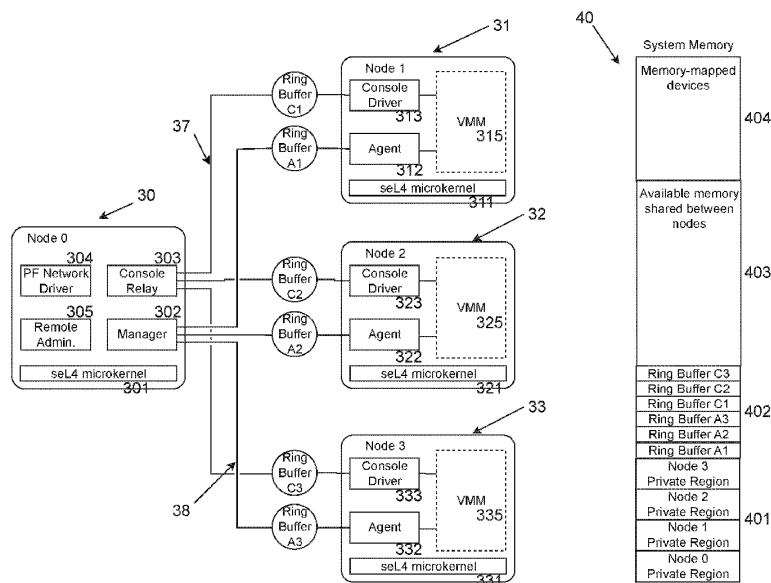


Figure 3

(57) **Abstract:** A computer system having a multikernel architecture comprising a plurality of operating system microkernels is described. The computer system comprises a plurality of processing cores, a memory comprising a plurality of private memory areas and a shared memory area, and a plurality of computing nodes each comprising one or more of the processing cores and further comprising one of the plurality of operating system microkernels and an associated user space for non-kernel operations, wherein only the operating system microkernel runs at a highest privilege level of the processing core, or processing cores, of a computing node. Each of the plurality of computing nodes has an associated private memory area from the plurality of private memory areas for use by its operating system microkernel, and for non-kernel operations each of the plurality of computing nodes accesses the shared memory area. Associated methods and computer systems are also described.

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KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY,
MA, MD, MG, MK, MN, MU, MW, MX, MY, MZ, NA,
NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO,
RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, ST, SV, SY, TH,
TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, WS,
ZA, ZM, ZW.

- (84) Designated States** (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, CV, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SC, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, ME, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

- with international search report (*Art. 21(3)*)
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (*Rule 48.2(h)*)
- in black and white; the international application as filed contained color or greyscale and is available for download from PATENTSCOPE

(88) Date of publication of the international search report:

01 May 2025 (01.05.2025)