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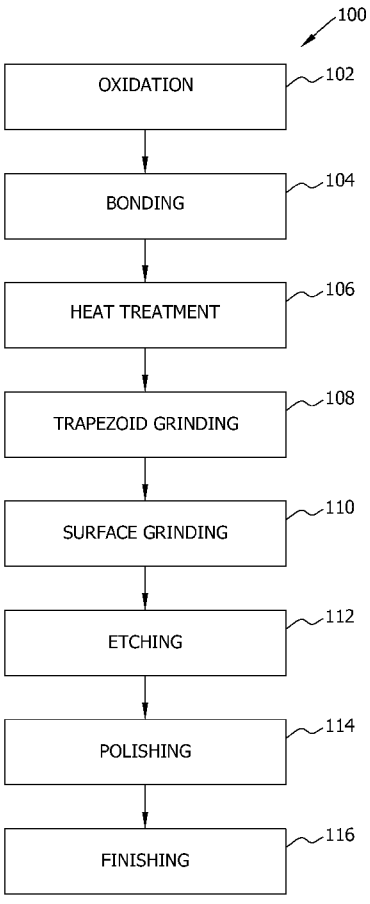
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(54) Title: SEMICONDUCTOR AND SOLAR WAFERS AND METHOD FOR PROCESSING SAME

(57) Abstract: A method for manufacturing a silicon-on-insulator structure including a substrate wafer, an active wafer, and an oxide layer between the substrate wafer and the active wafer. The method includes the steps of heat treating the structure, trapezoid grinding edges of the wafer, and grinding a surface of the wafer.

FIG. 6



WO 2012/014136 A1

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