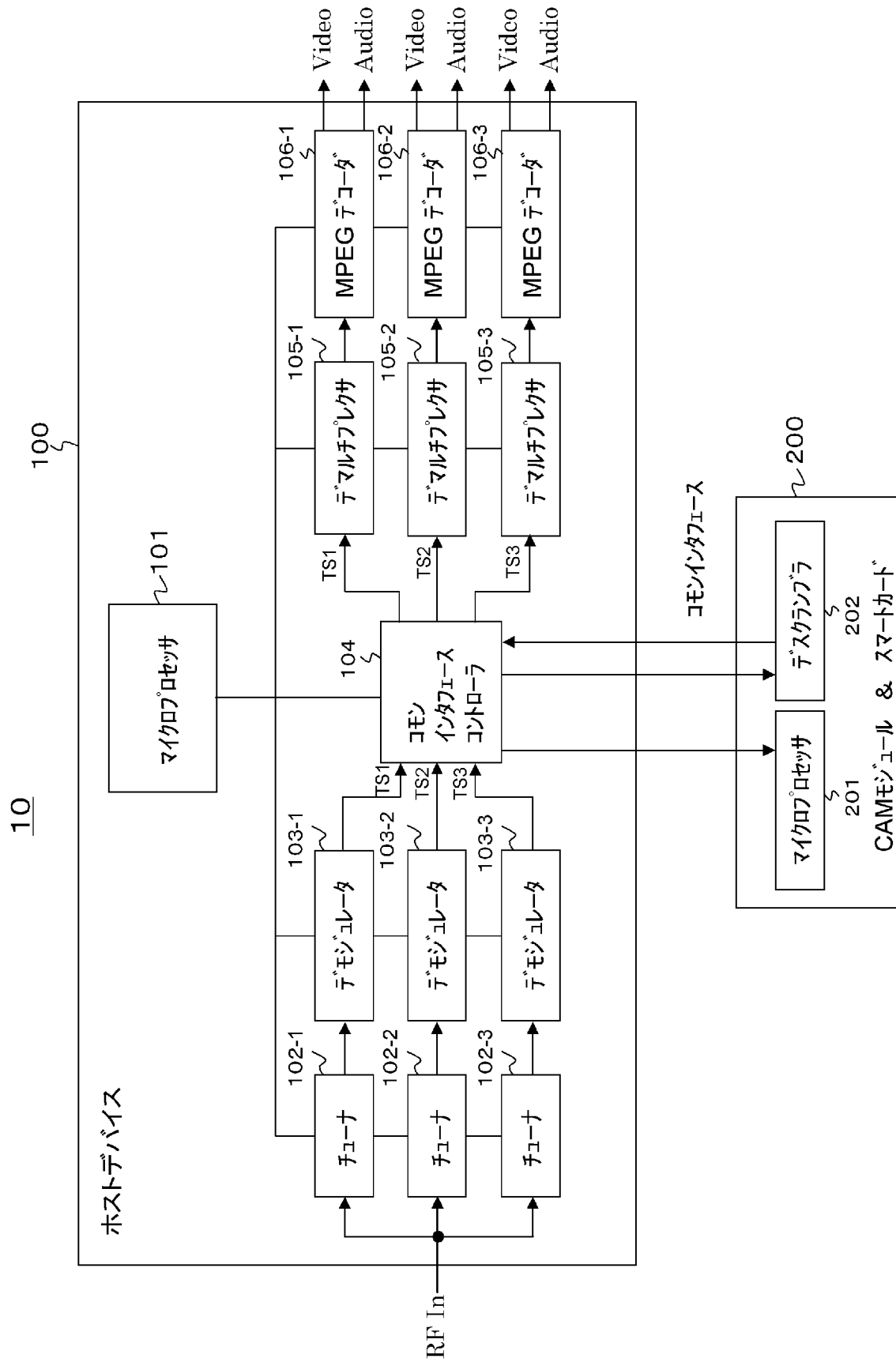
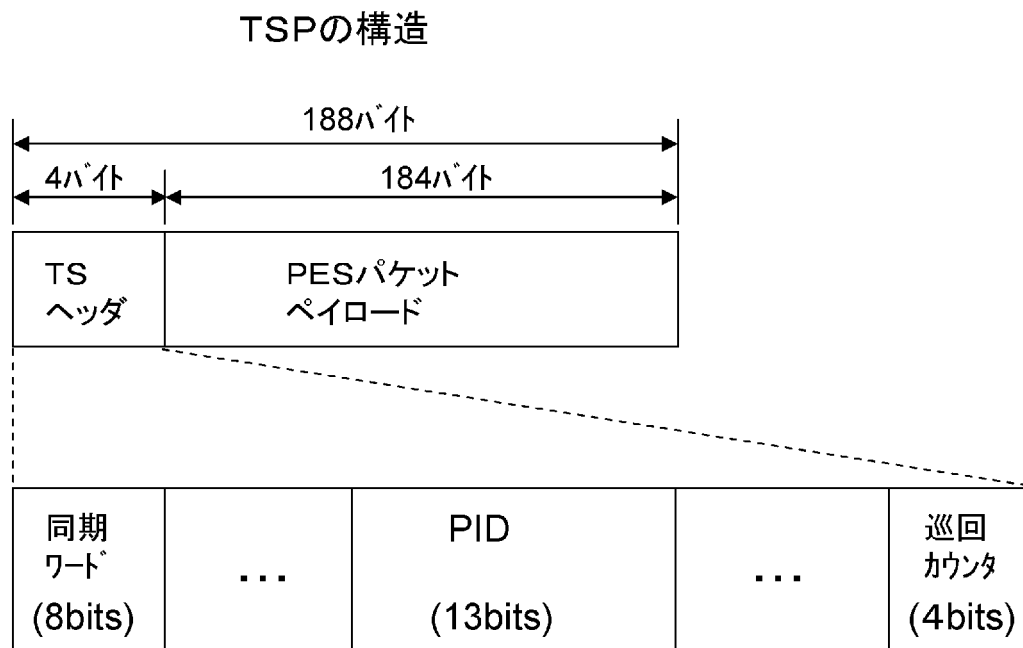


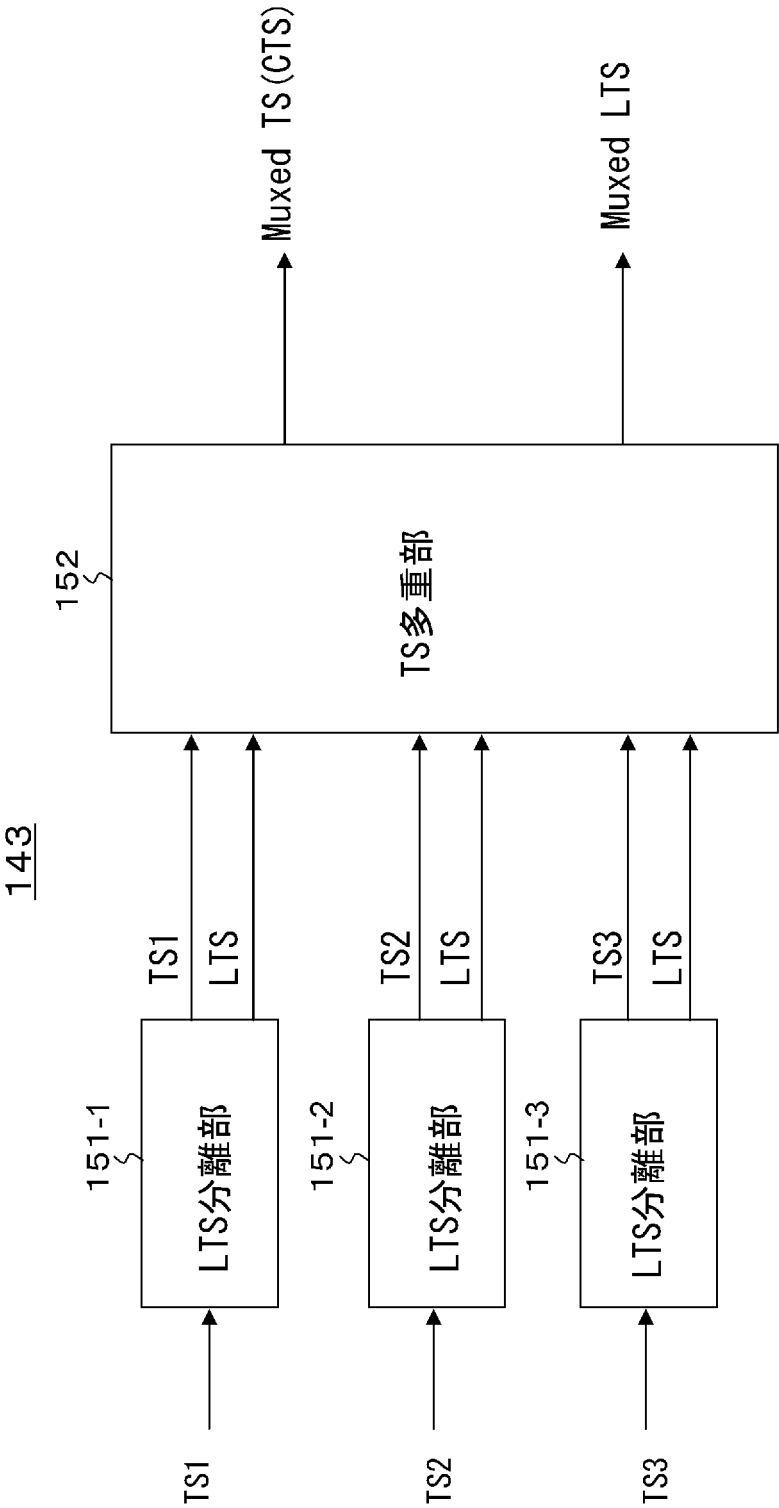
[図1]



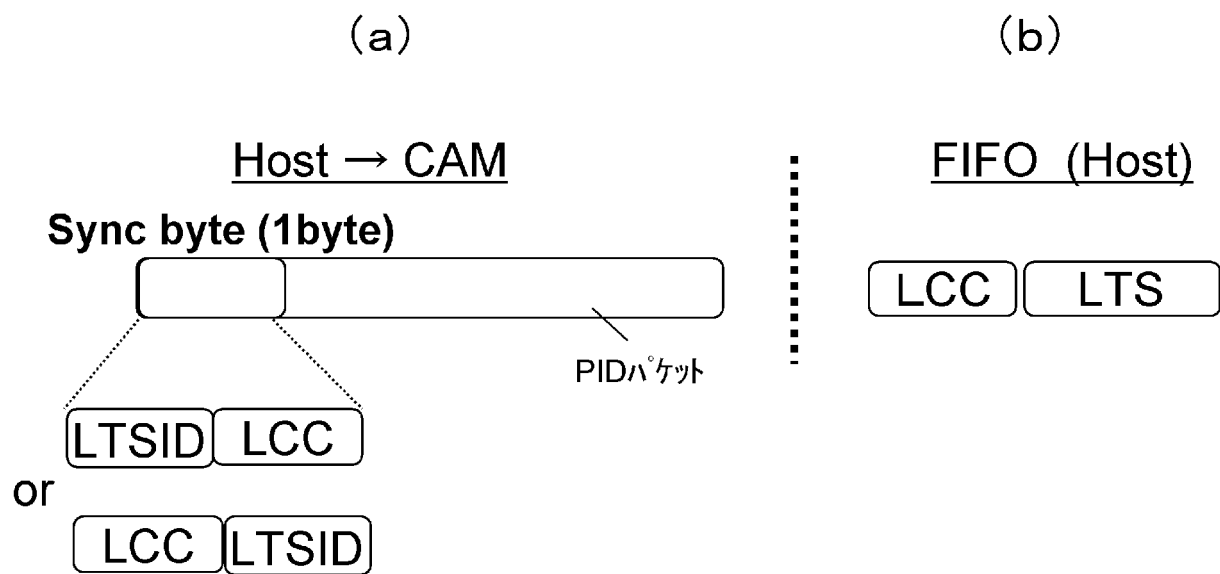
[図3]



[図4]



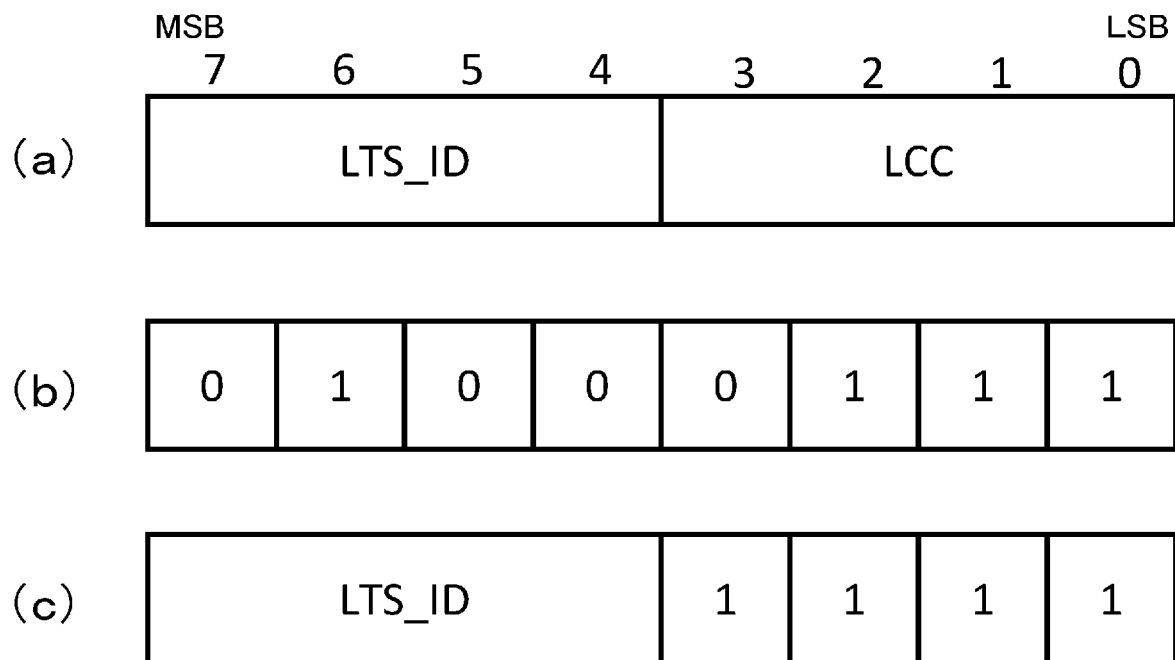
[図5]



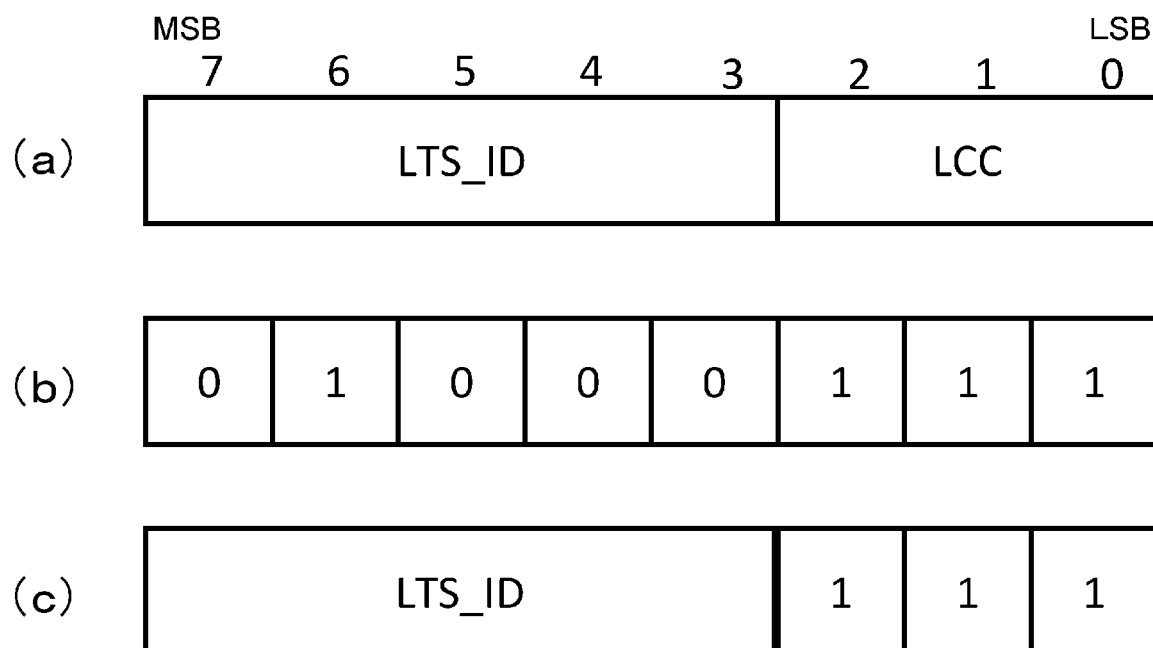
[図6]

	LTSID	LCC	Recover range of packet order change
1	6bit	2bit	64TS / recover less than 2 packets order change
2	5bit	3bit	32TS / recover less than 6 packets order change
3	4bit	4bit	16TS / recover less than 14 packets order change
4	3bit	5bit	8TS / recover less than 30 packets order change
5	2bit	6bit	4TS / recover less than 62 packets order change
6	1bit	7bit	2TS / recover less than 126 packets order change

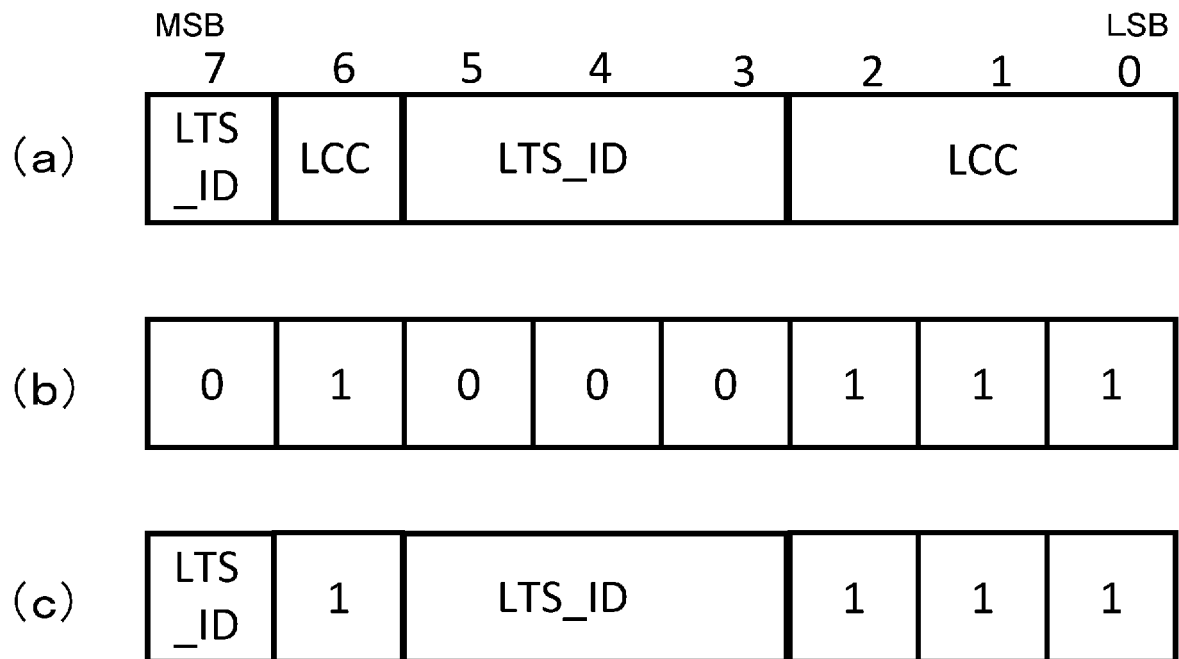
[図7]



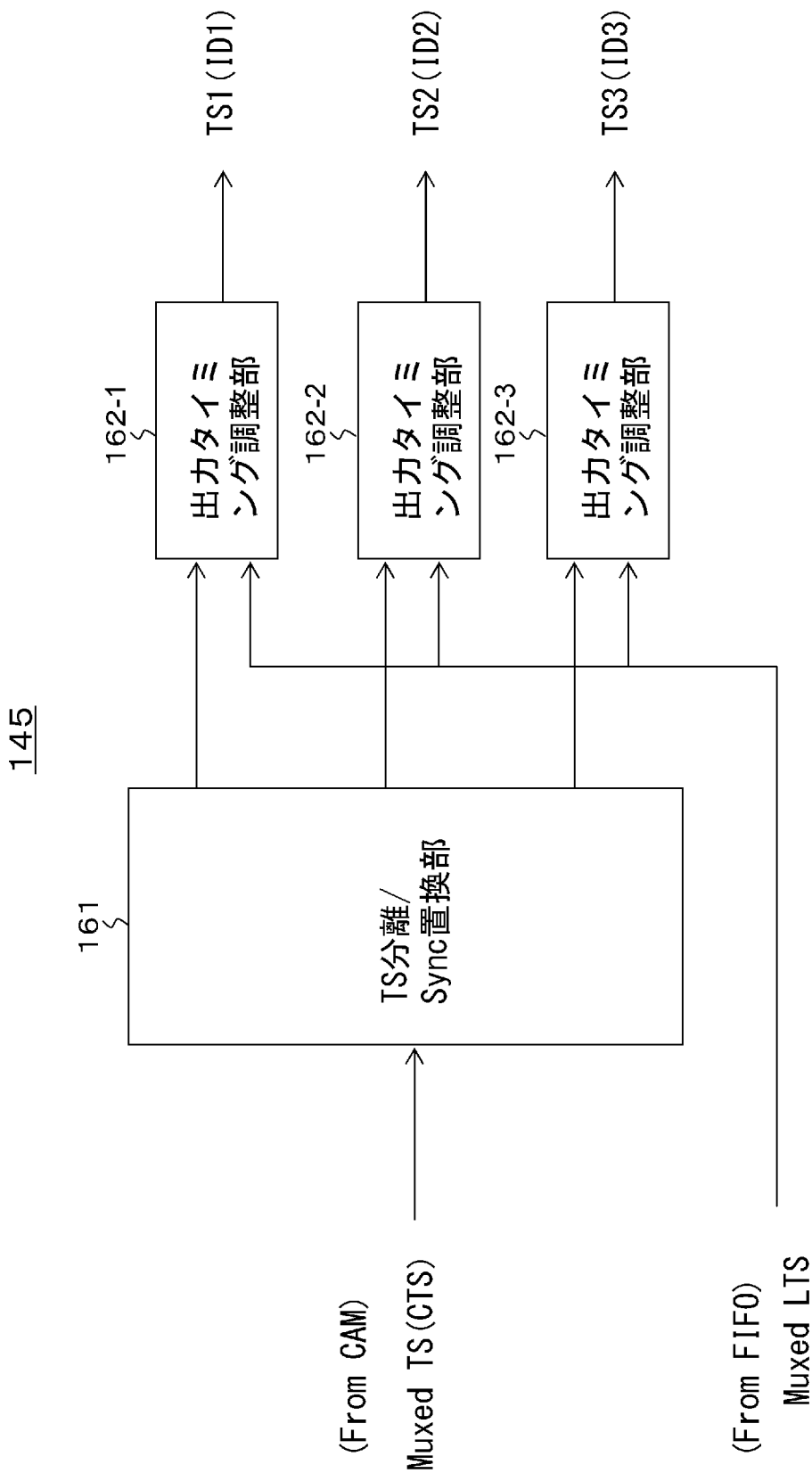
[図8]



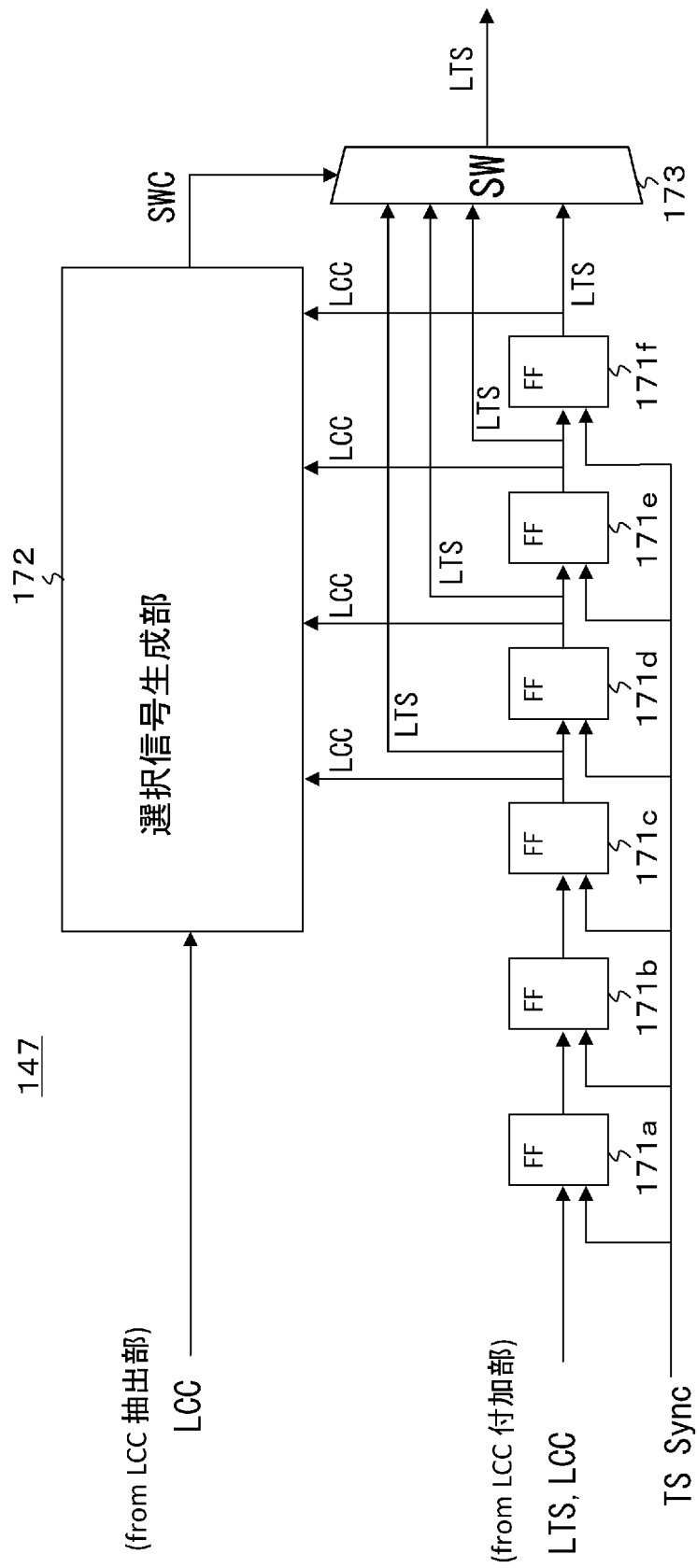
[図9]



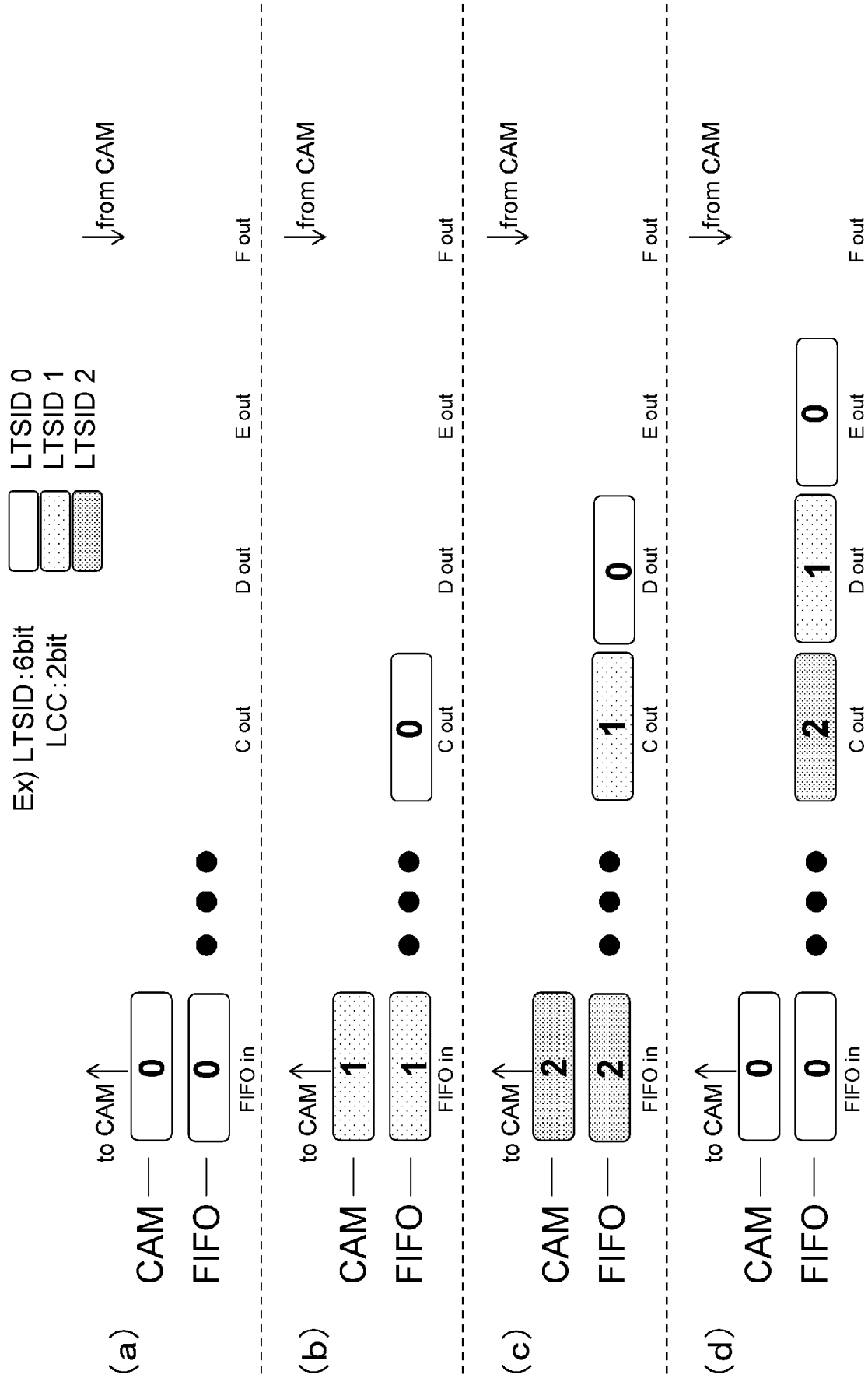
[図10]



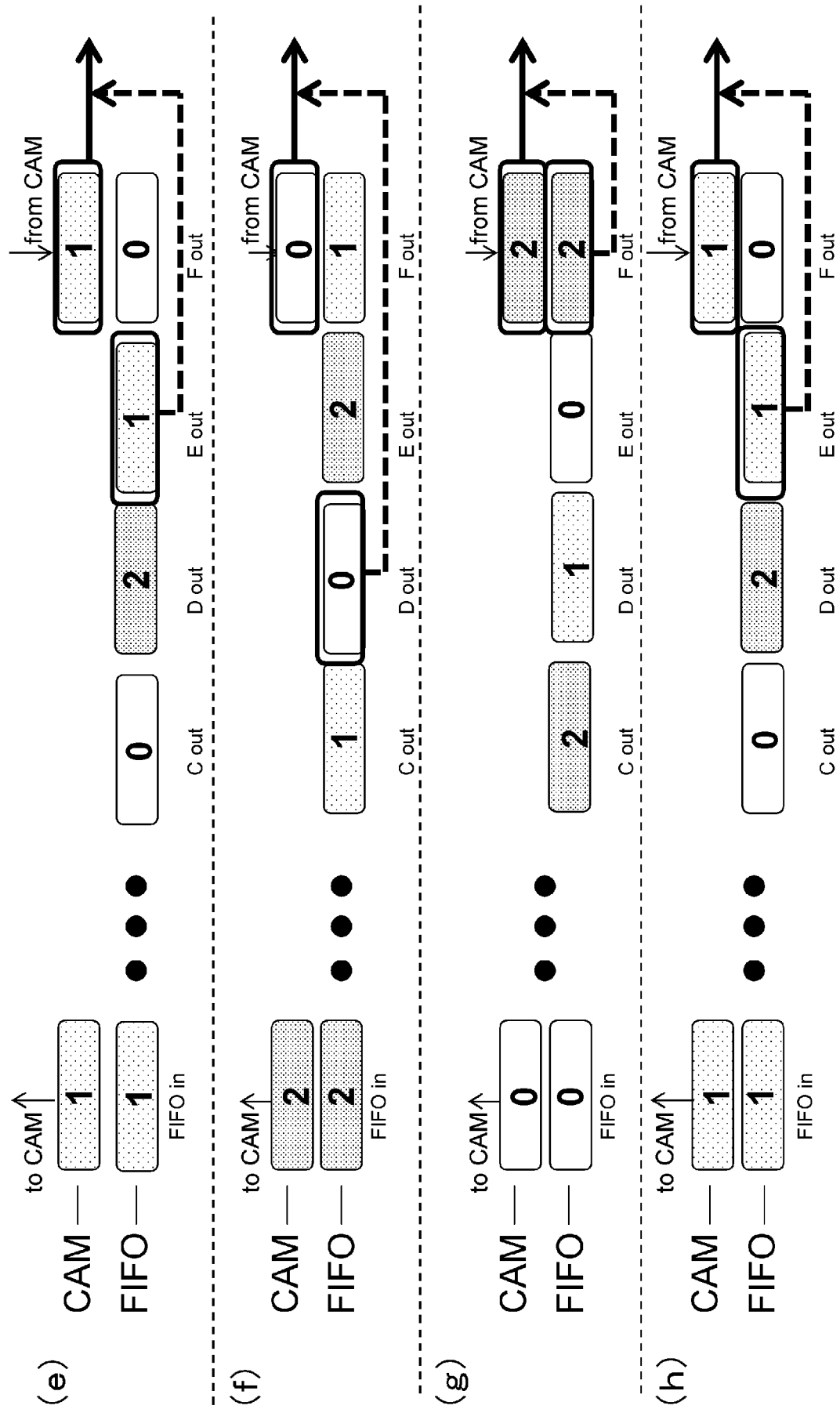
[図11]



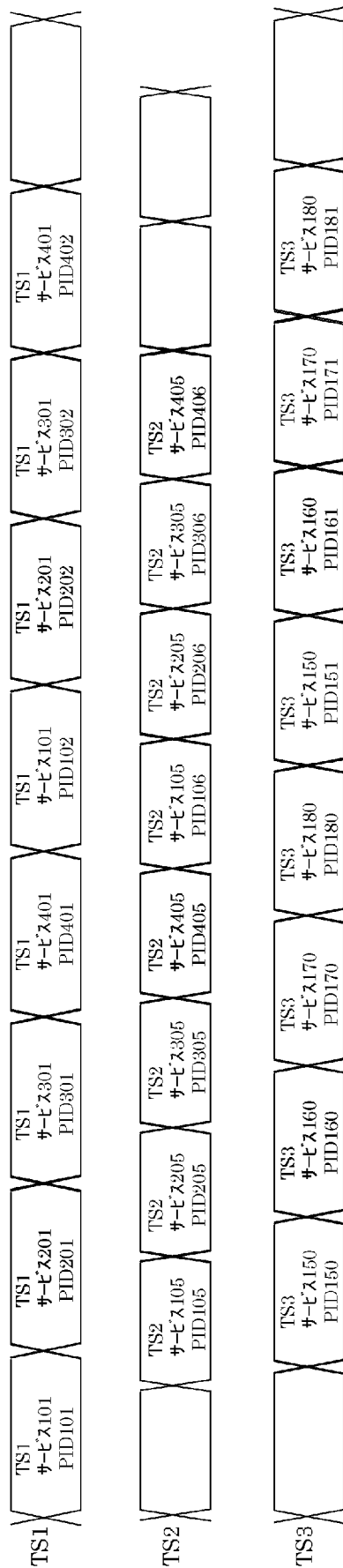
[図12]



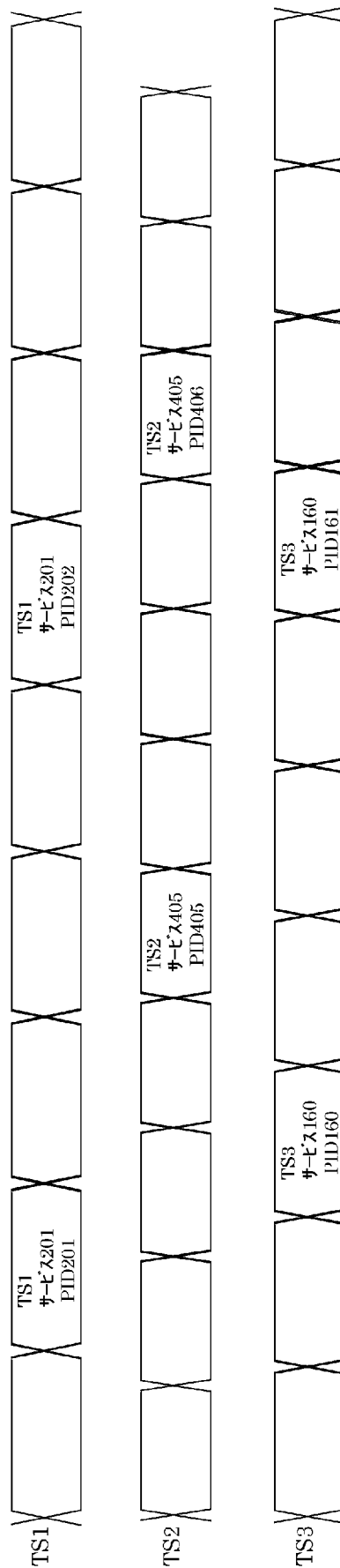
[図13]



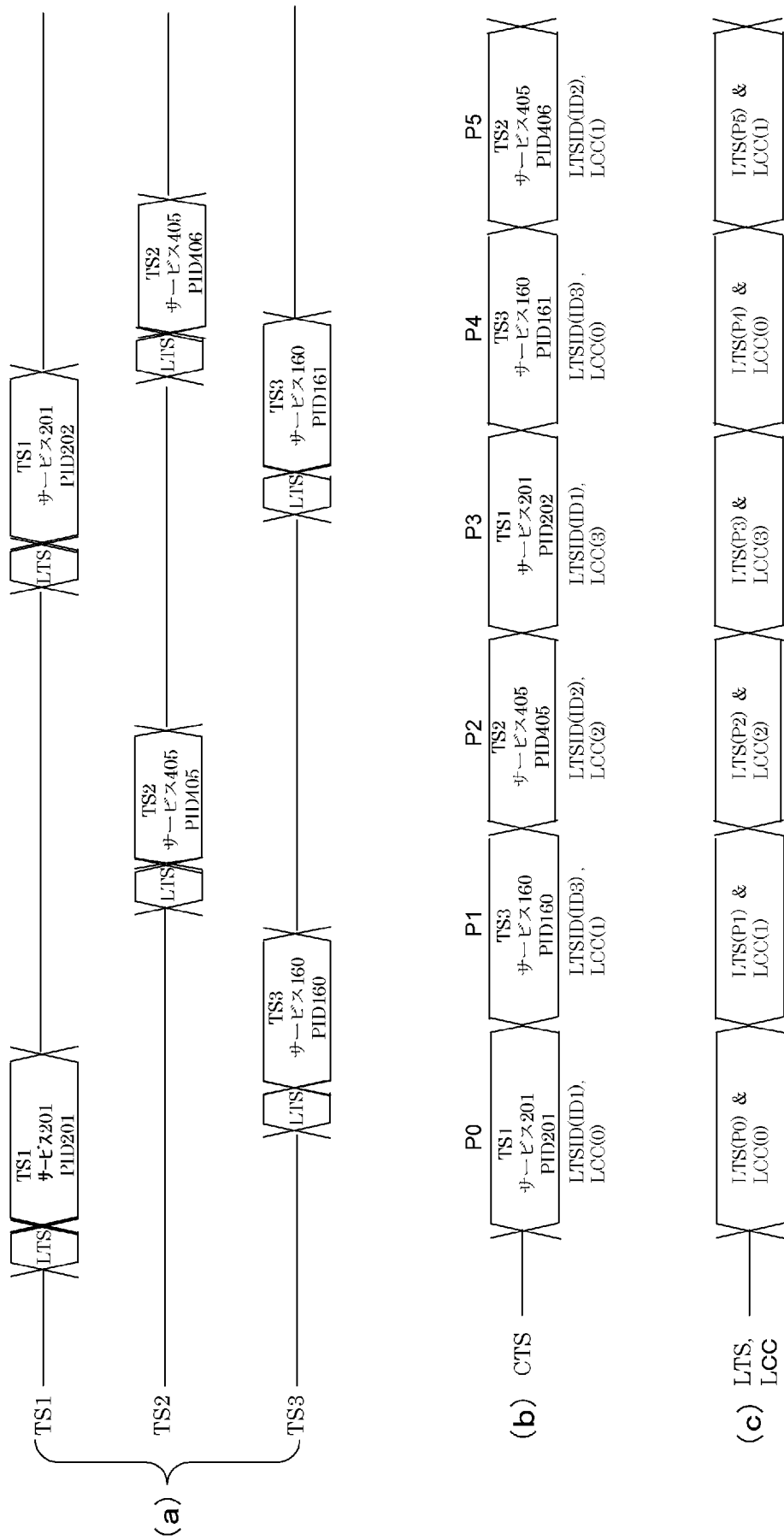
(a) TS入力



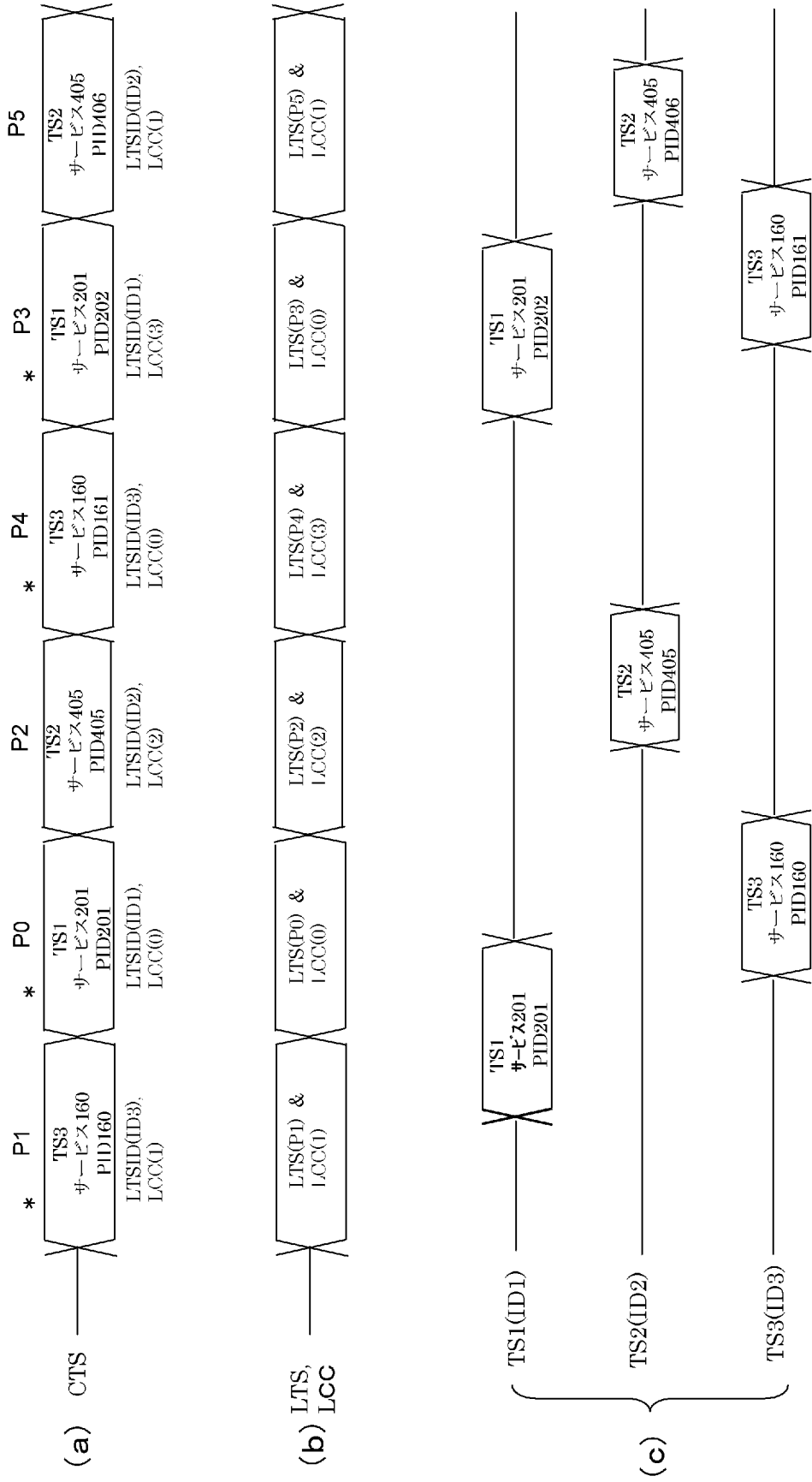
(b) 不要でないPIDパケット



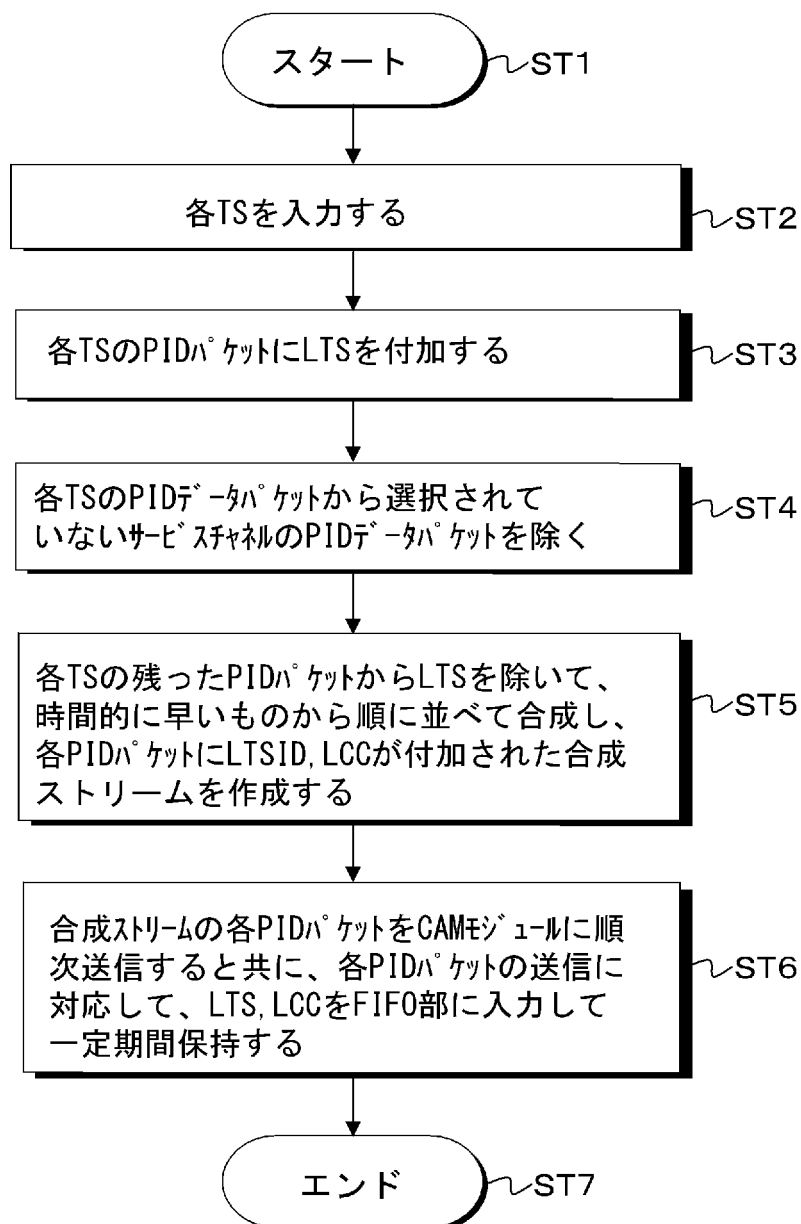
[図15]



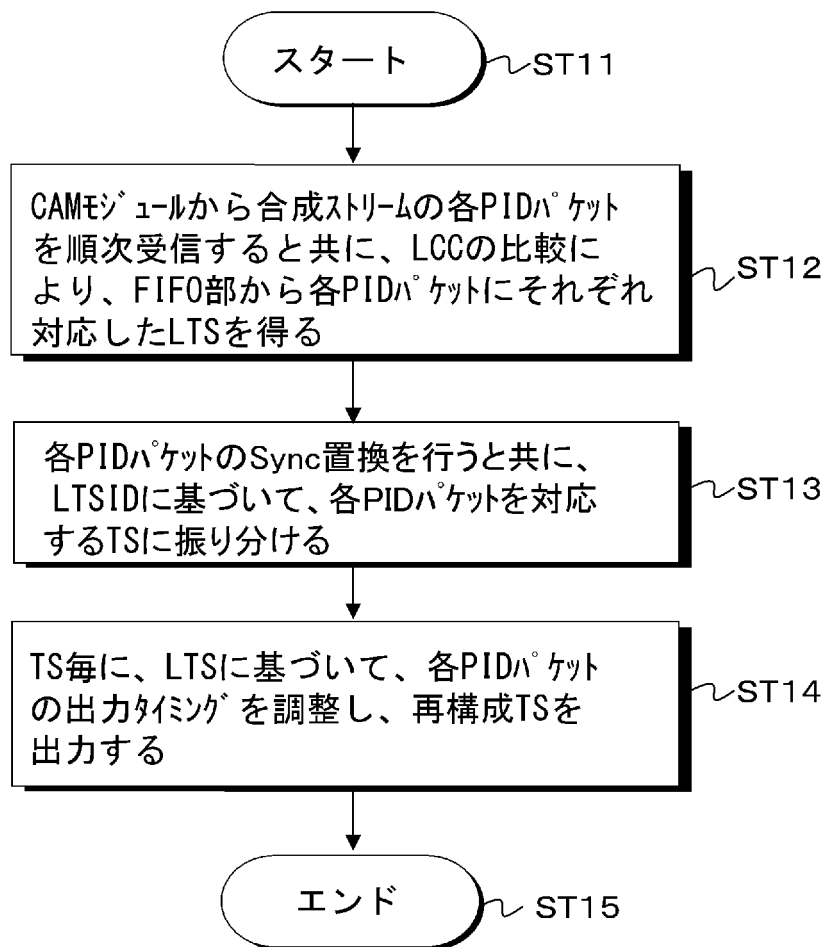
[図16]



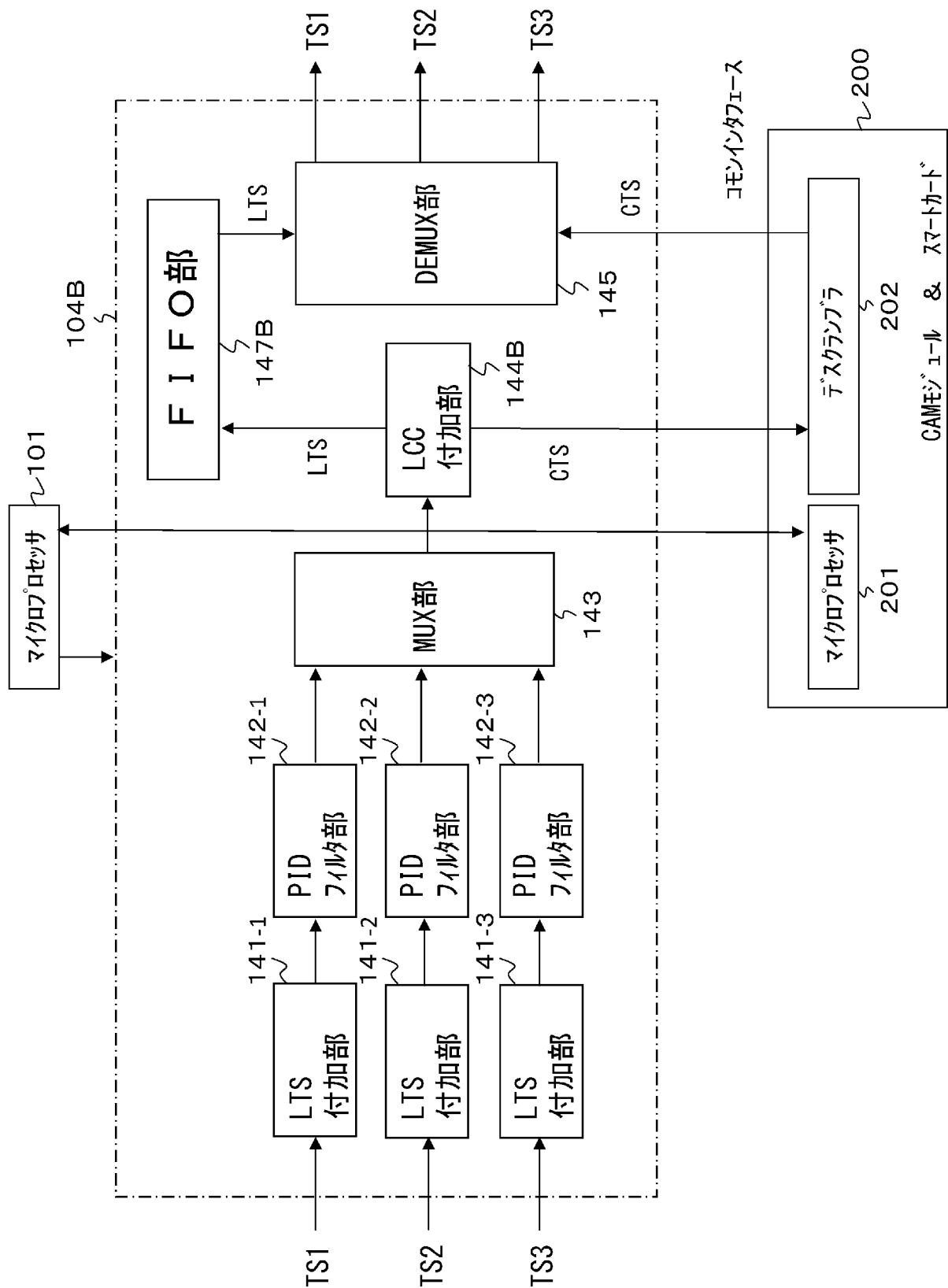
[図17]



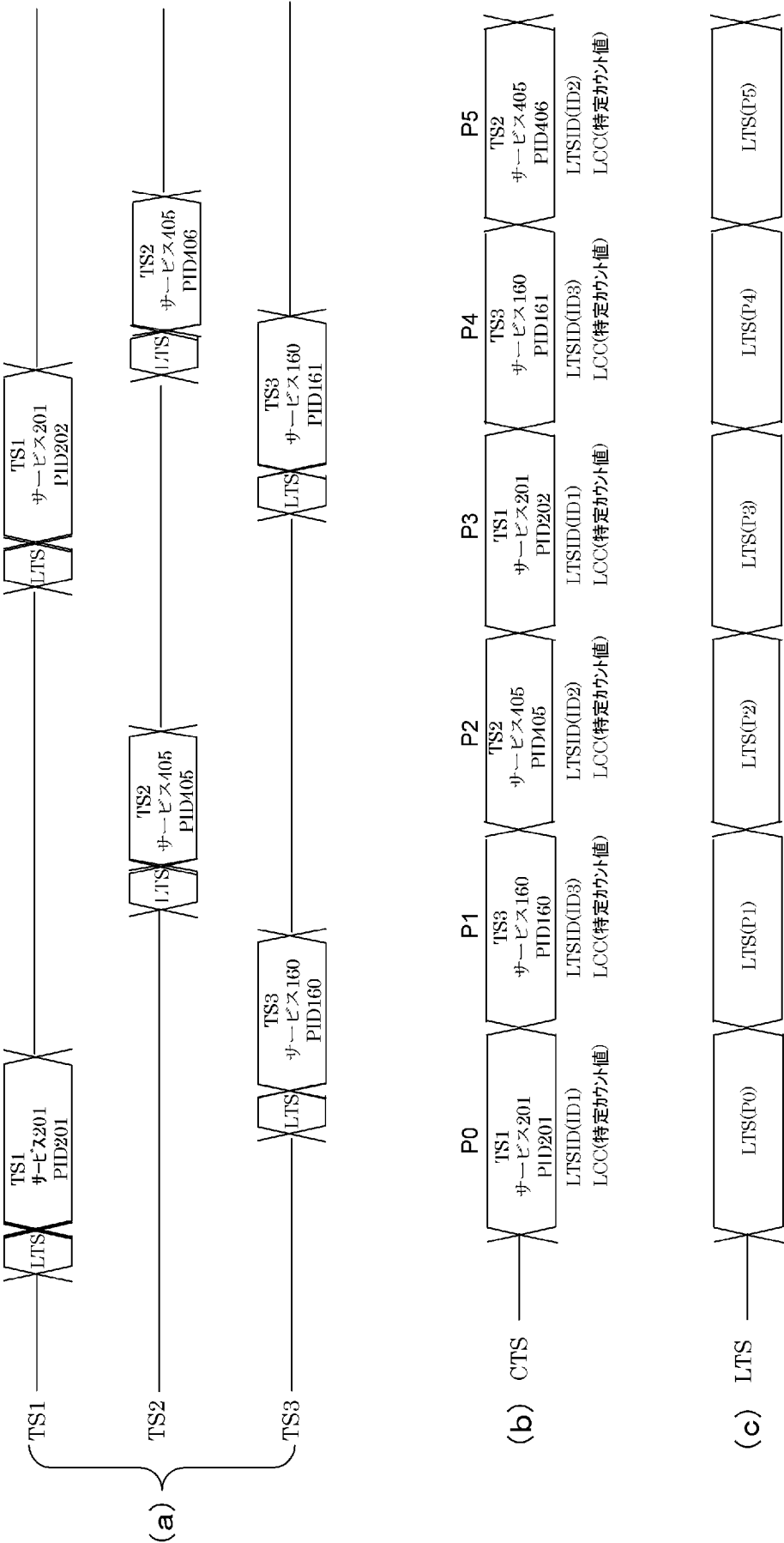
[図18]



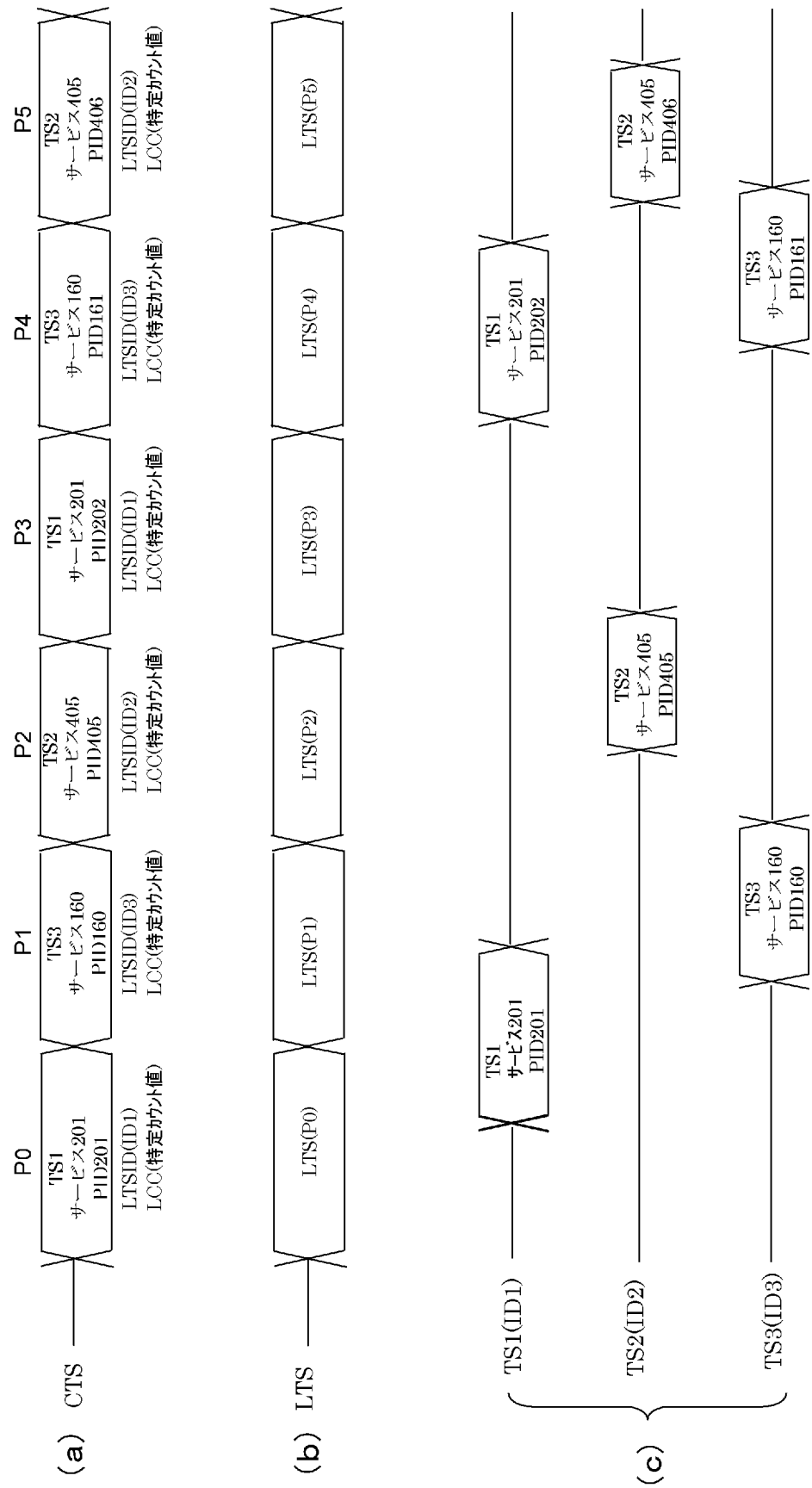
[図19]



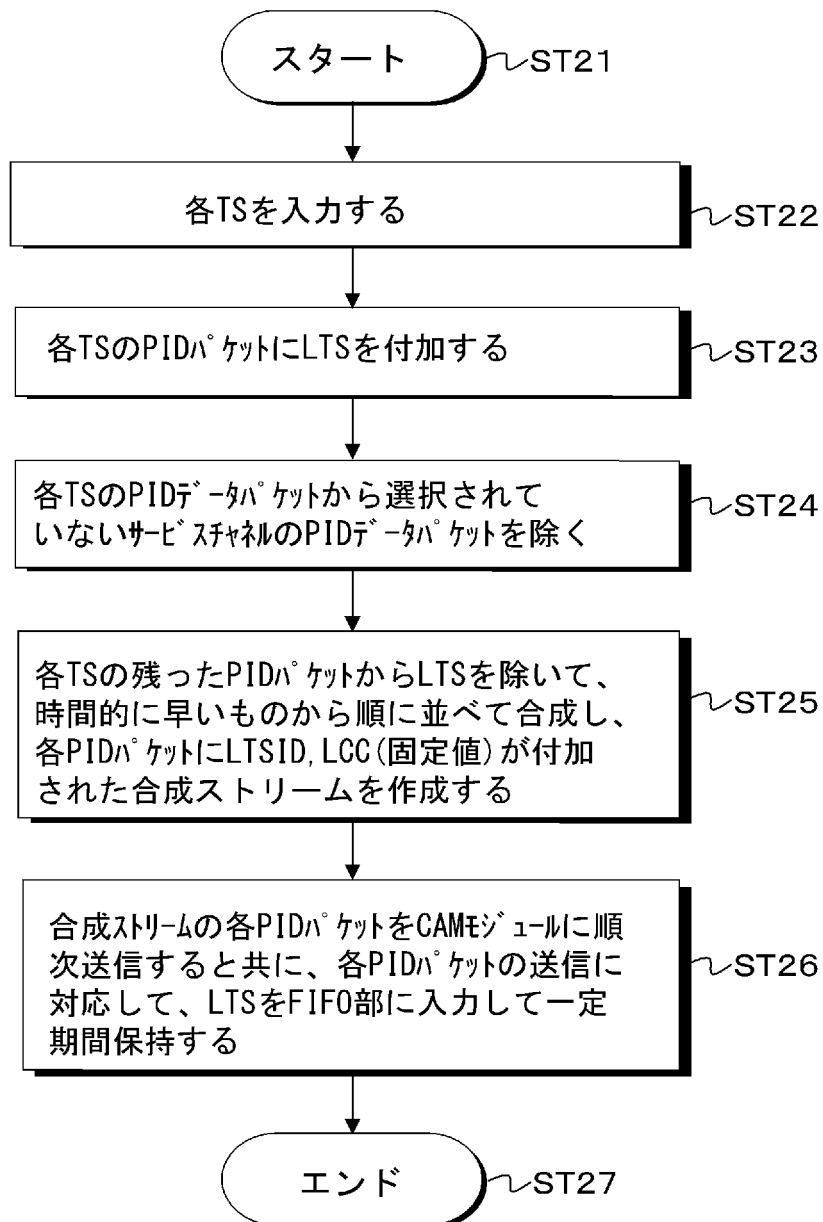
[図20]



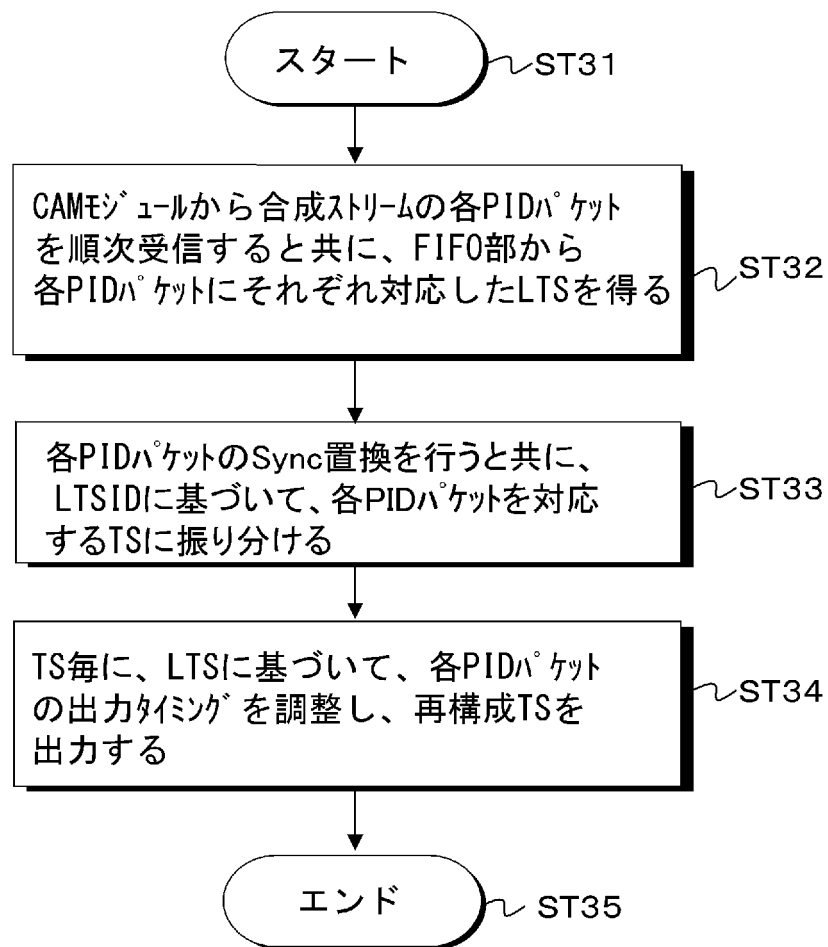
[図21]



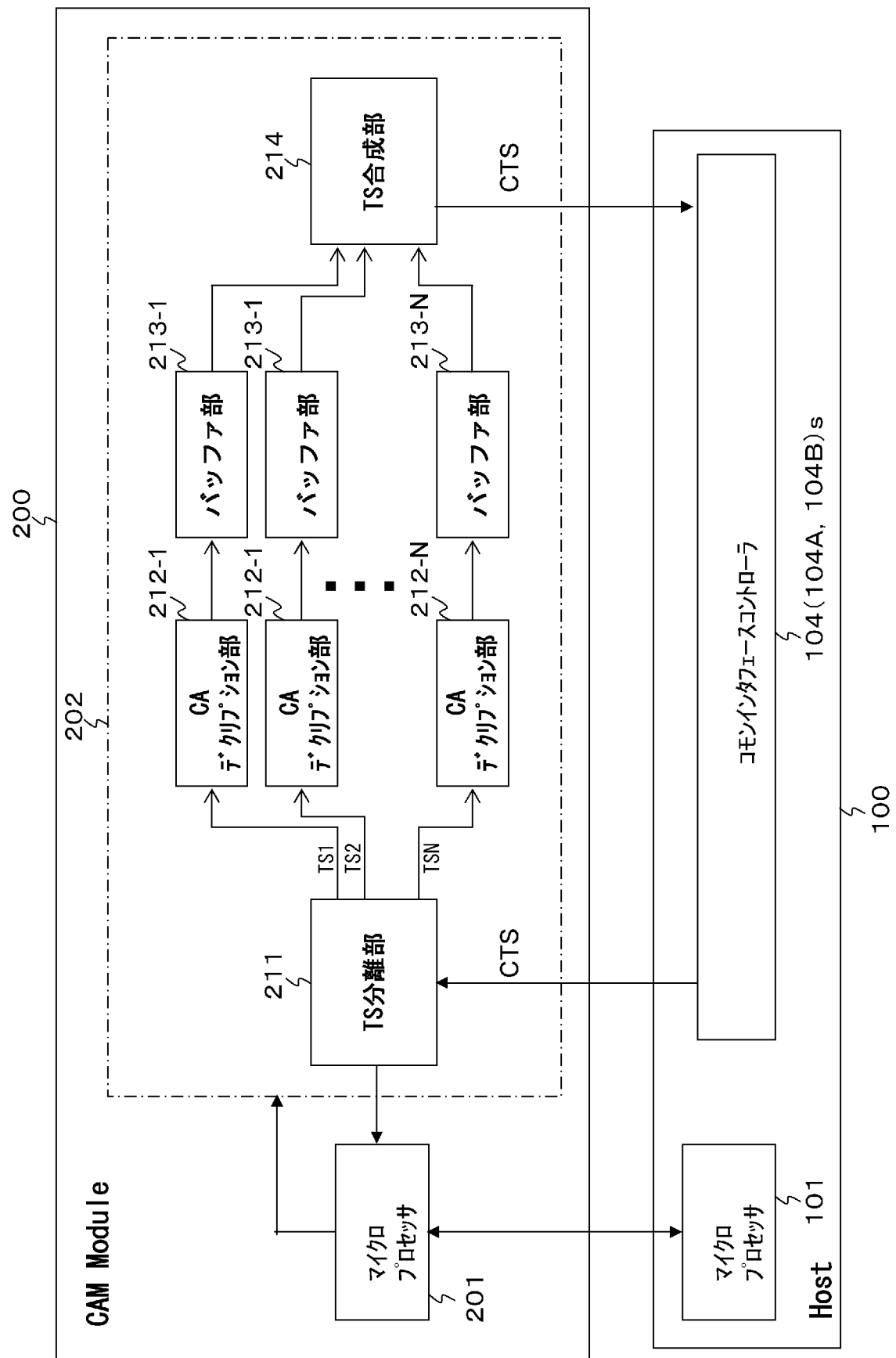
[図22]



[図23]



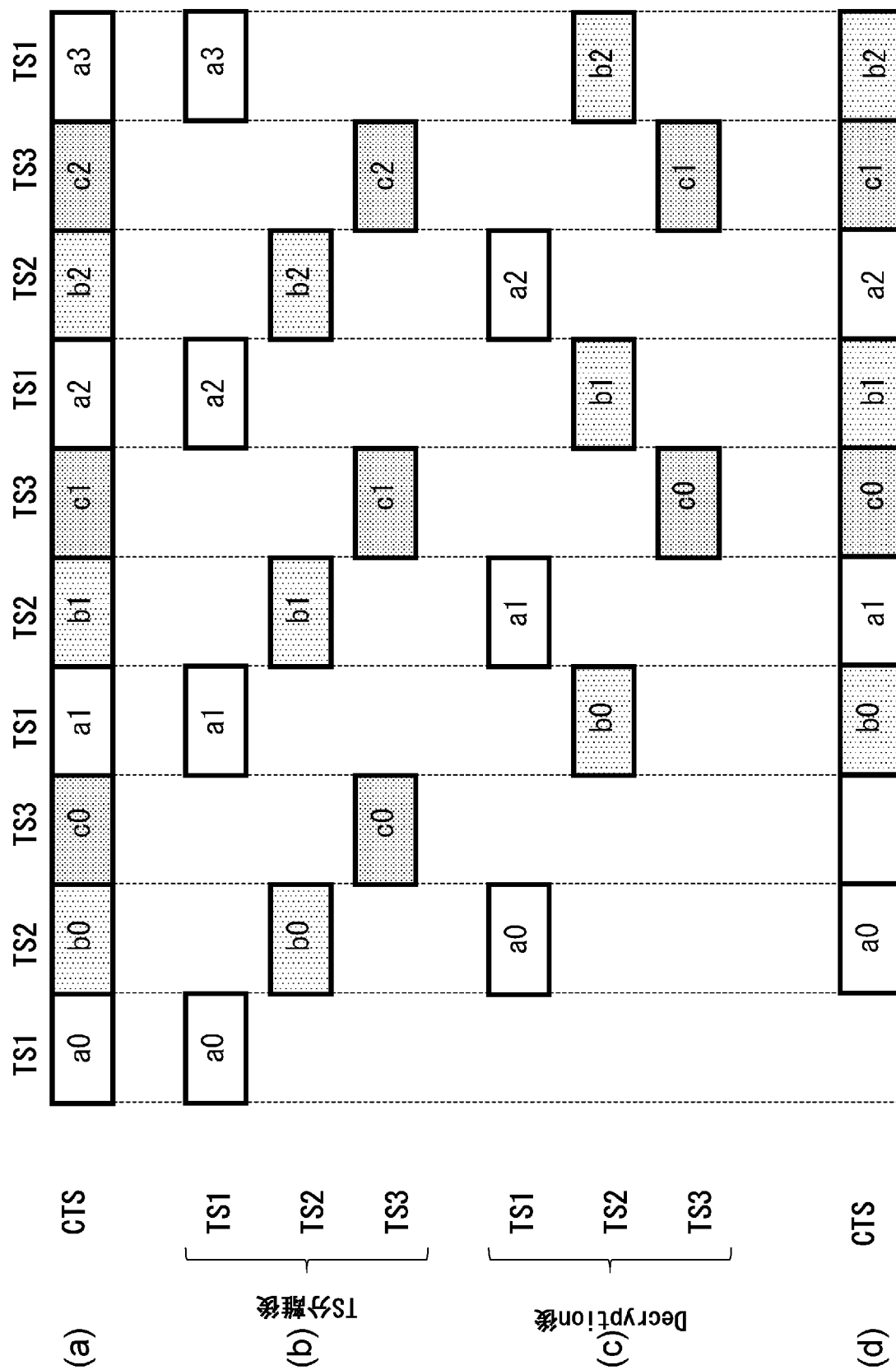
[図24]



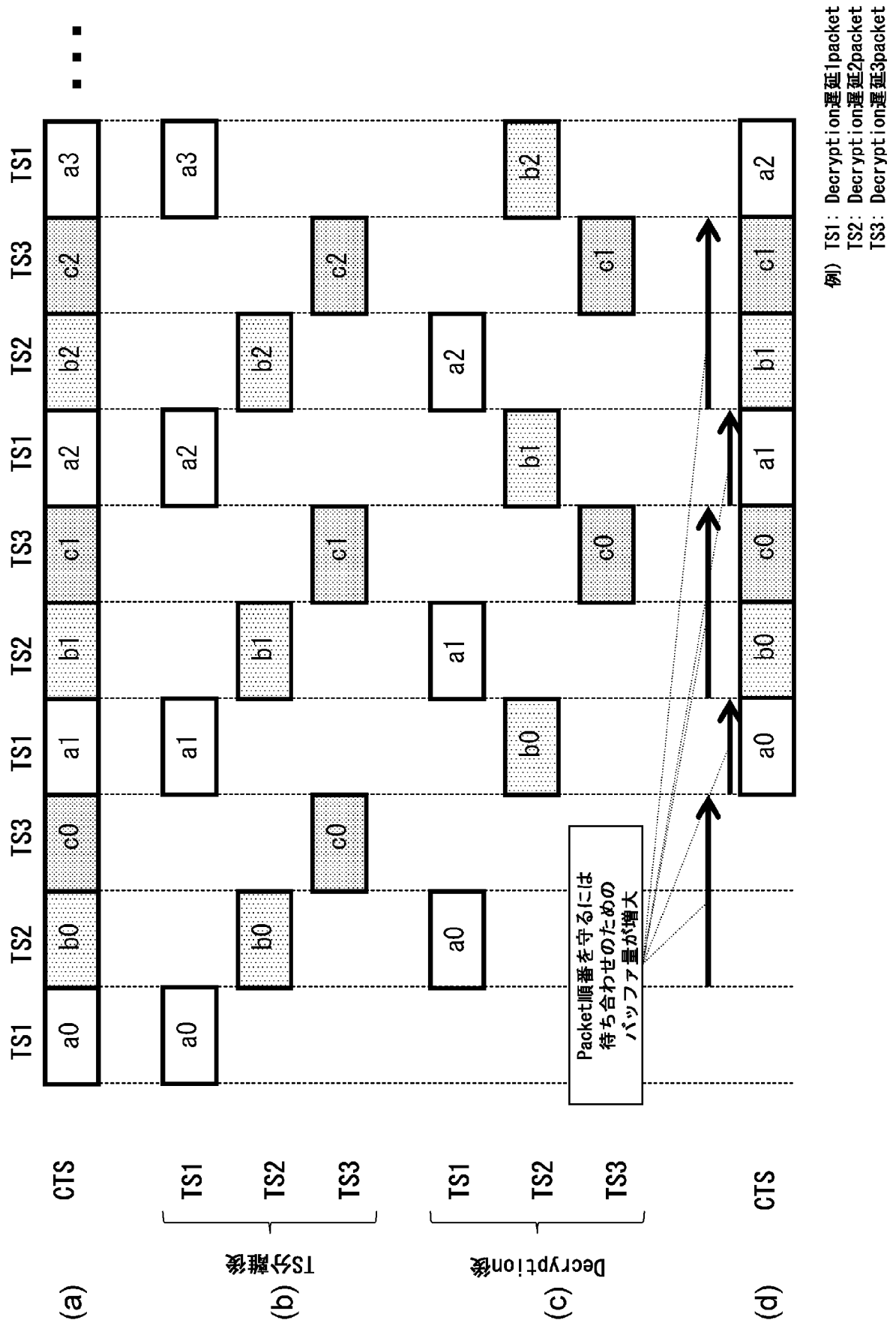
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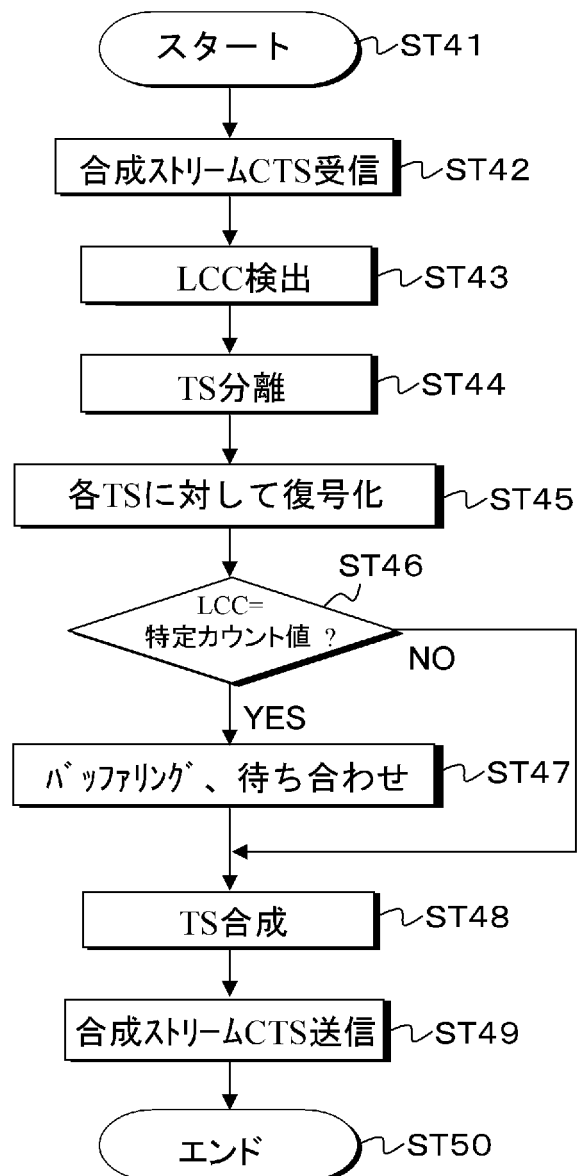
•



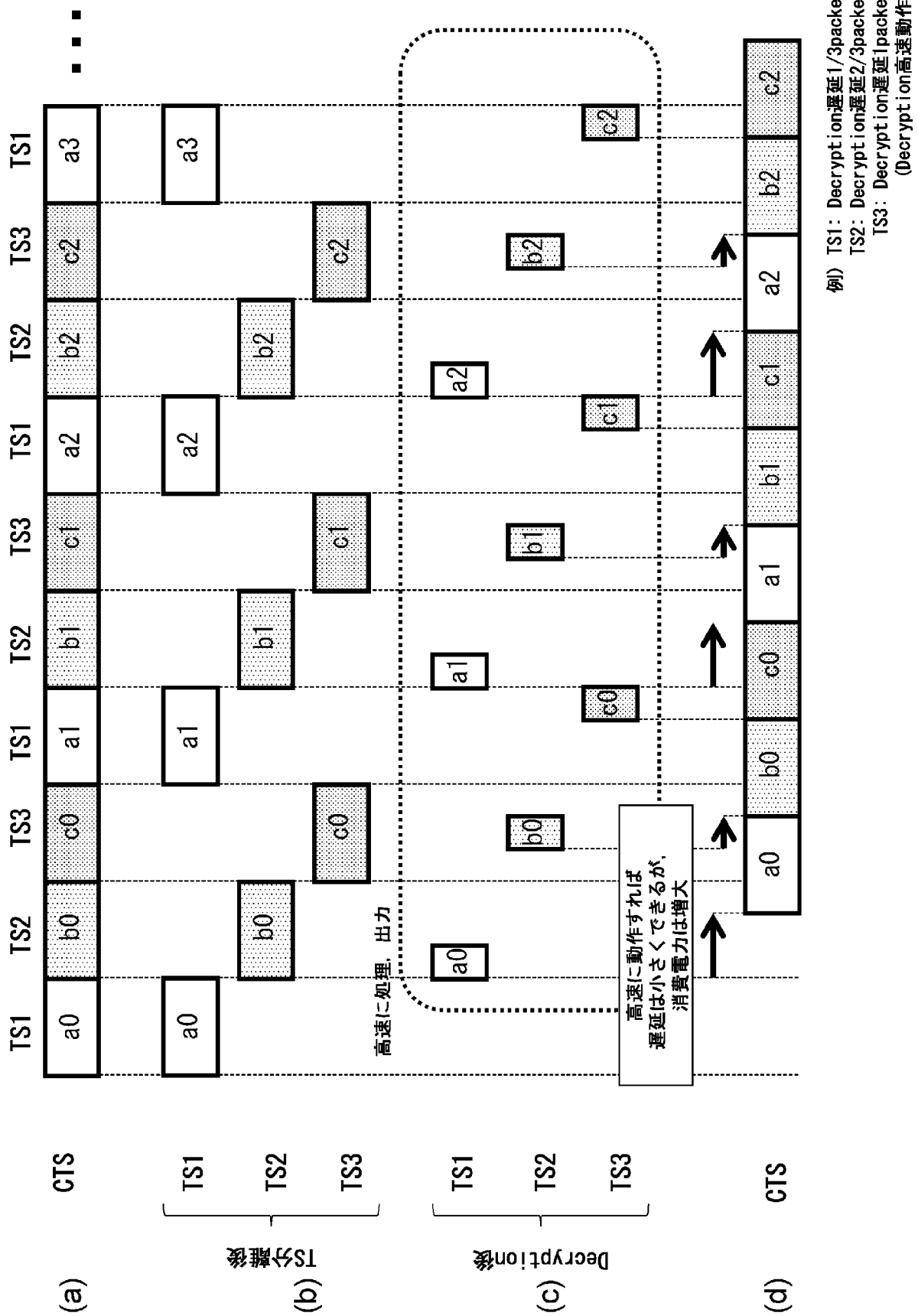
[図26]



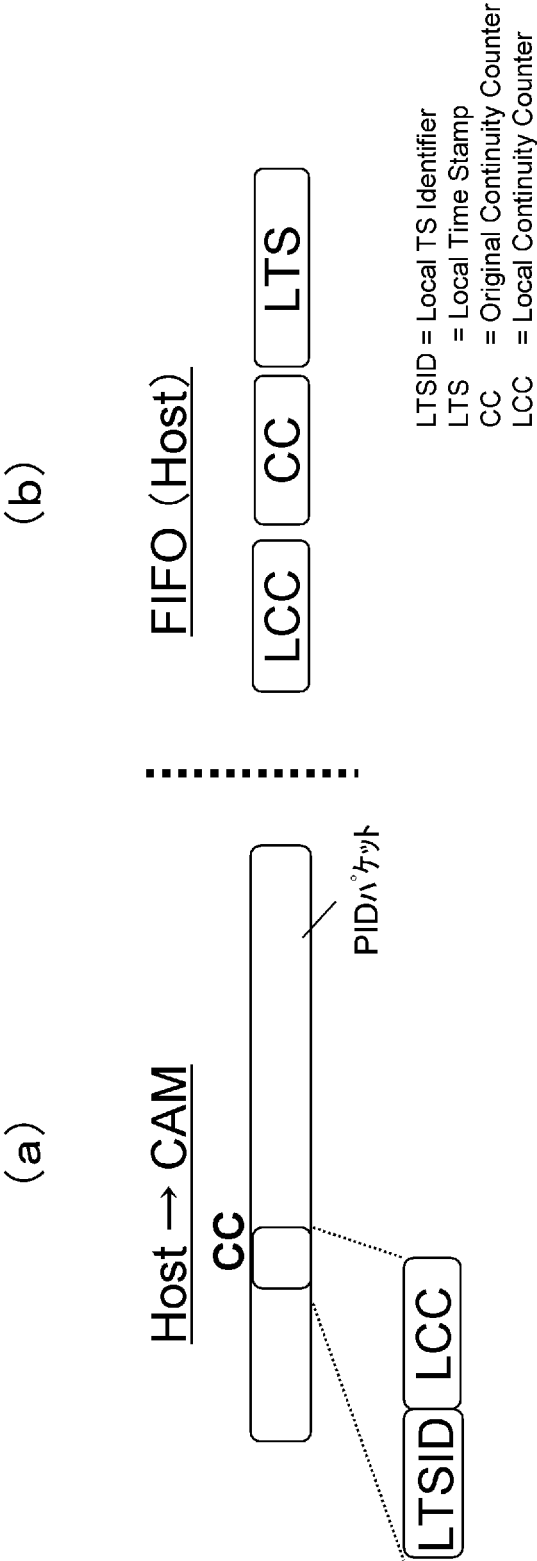
[図27]



[図28]



[図29]



(b)

[図30]

	LTSID	LCC	Recover range of packet order change
1	2bit	2bit	4TS / recover less than 2 packets order change
2	1bit	3bit	2TS / recover less than 6 packets order change