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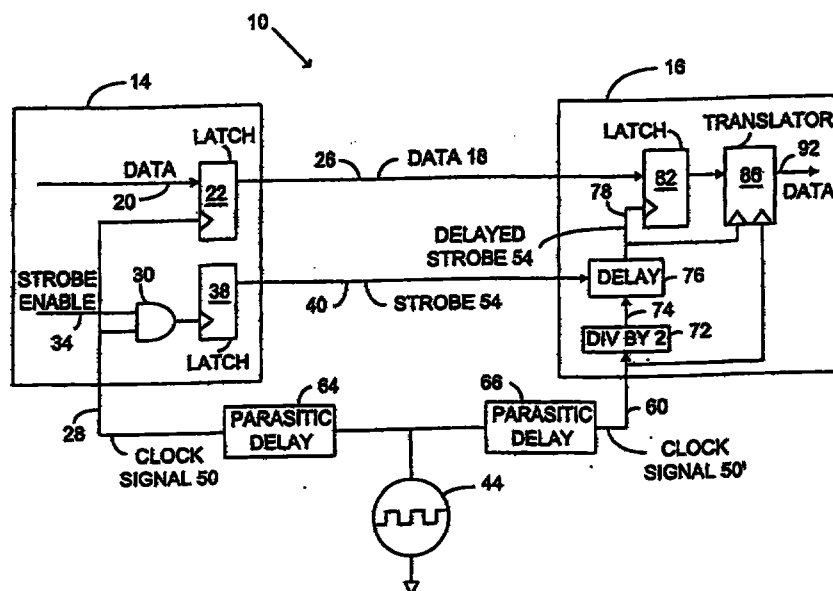
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(54) Title: A MASTER-SLAVE DELAY LOCKED LOOP FOR ACCURATE DELAY OF NON-PERIODIC SIGNALS



(57) Abstract

The present invention involves an electrical component interface system (10). The system includes clocking circuitry (44) to provide a clock signal (50). A sending component (14) provides a non-periodic strobe signal (54) and a data signal (18) responsive to the clocking signal (50). A receiving component (16) receives the strobe signal (54) and data signal (50). The receiving component includes delay circuitry (76) to delay the strobe signal so as to position edges of the strobe signal with respect to data cells of the data signal. The delay circuitry (76) includes loaded delay elements and DC level restoration circuitry (166A) to control the load of the loaded delay element. The delay elements may be a series of inverters loaded with RC loads. The DC level restoration circuitry may be pulse generation circuitry.