

CALIBRATION CIRCUIT, MEMORY AND CALIBRATION METHOD

ABSTRACT

Embodiments of the disclosure provide a calibration circuit, a memory and a calibration method. The calibration circuit includes: a differential input circuit, configured to receive a first oscillation signal and a second oscillation signal, the first oscillation signal and the second oscillation signal having the same frequency and opposite phases, the duty cycle of the first oscillation signal and the duty cycle of the second oscillation signal being in a first preset range, and the differential input circuit being configured to output a first internal signal and a second internal signal; a comparison unit, connected to an output end of the differential input circuit, and configured to compare the duty cycle of the first internal signal and/or the duty cycle of the second internal signal; and a logical unit, connected to the comparison unit and the differential input circuit, and configured to control the differential input circuit according to an output result of the comparison unit, such that the duty cycle of the first internal signal and/or the duty cycle of the second internal signal reaches a second preset range. According to the embodiments of the disclosure, the calibration circuit has a Duty Cycle Monitor (DCM) function and a Duty Cycle Adjust (DCA) function, and also can achieve detection and calibration on the differential input circuit.

[Fig. 2]