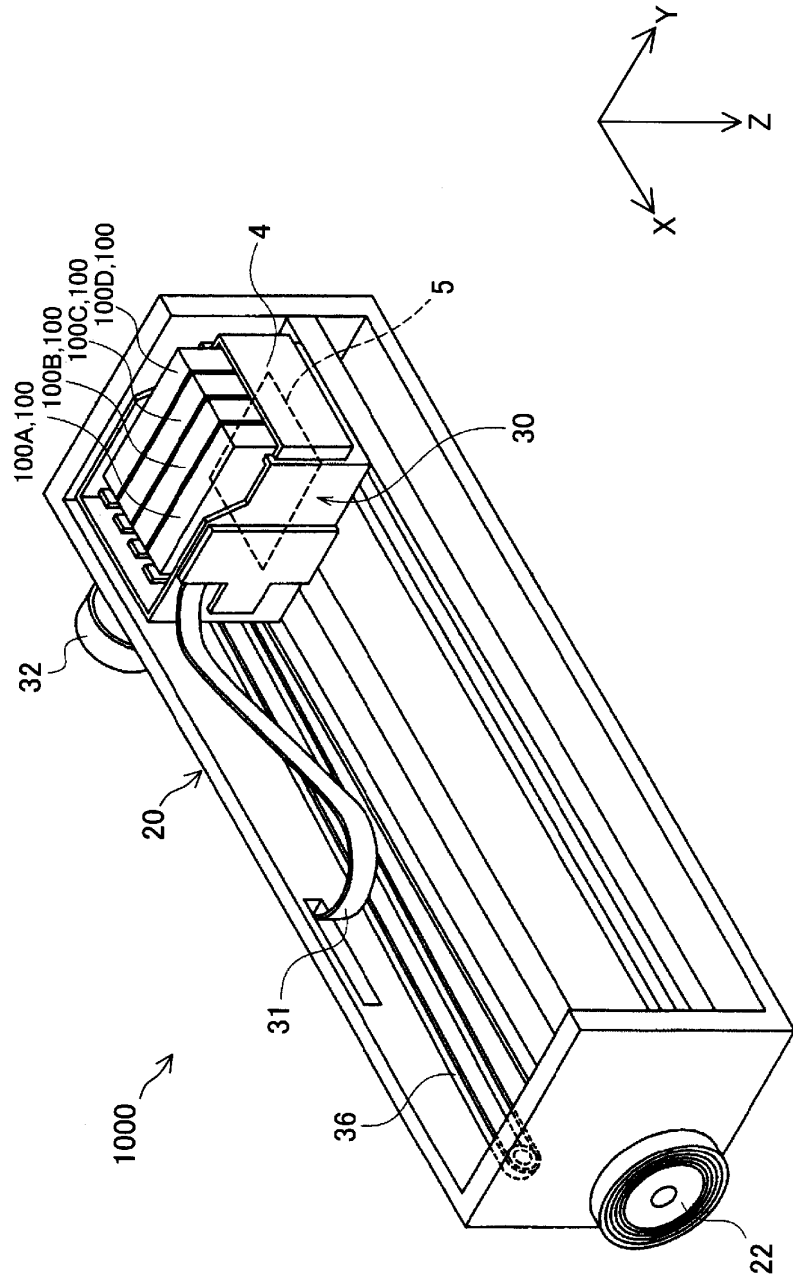


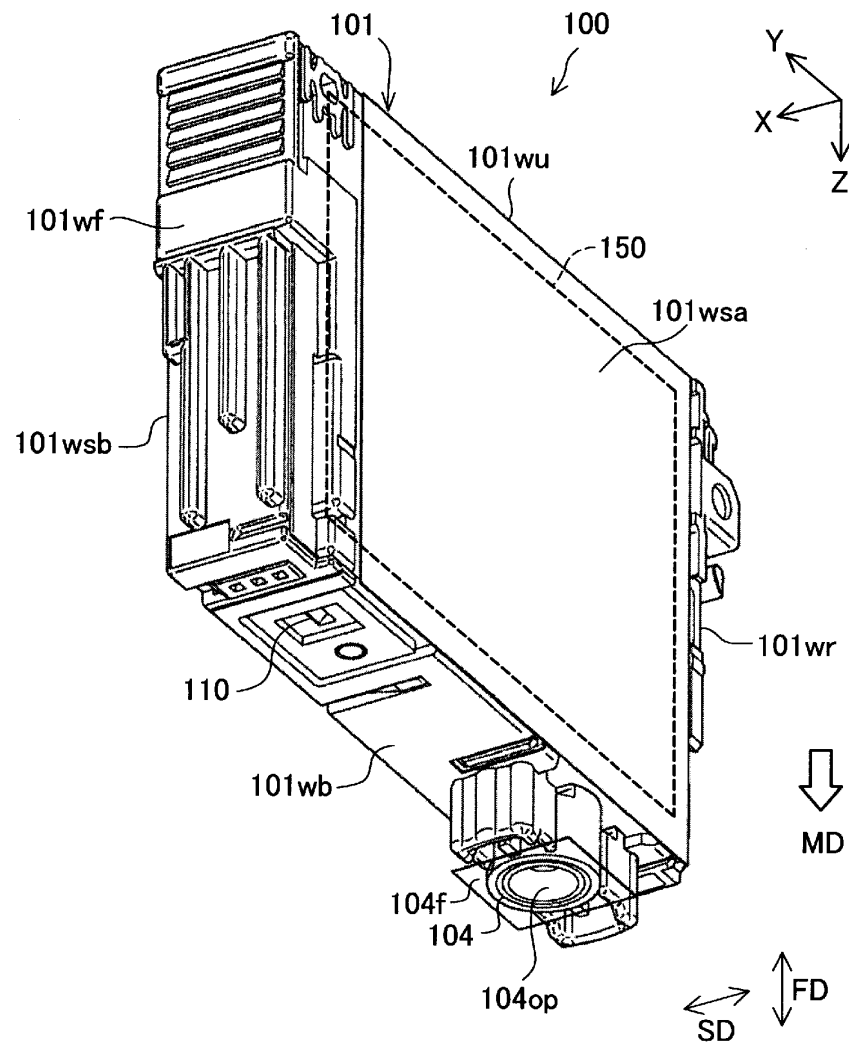
[図1]

Fig.1



[図3]

Fig.3



[図4]

Fig.4

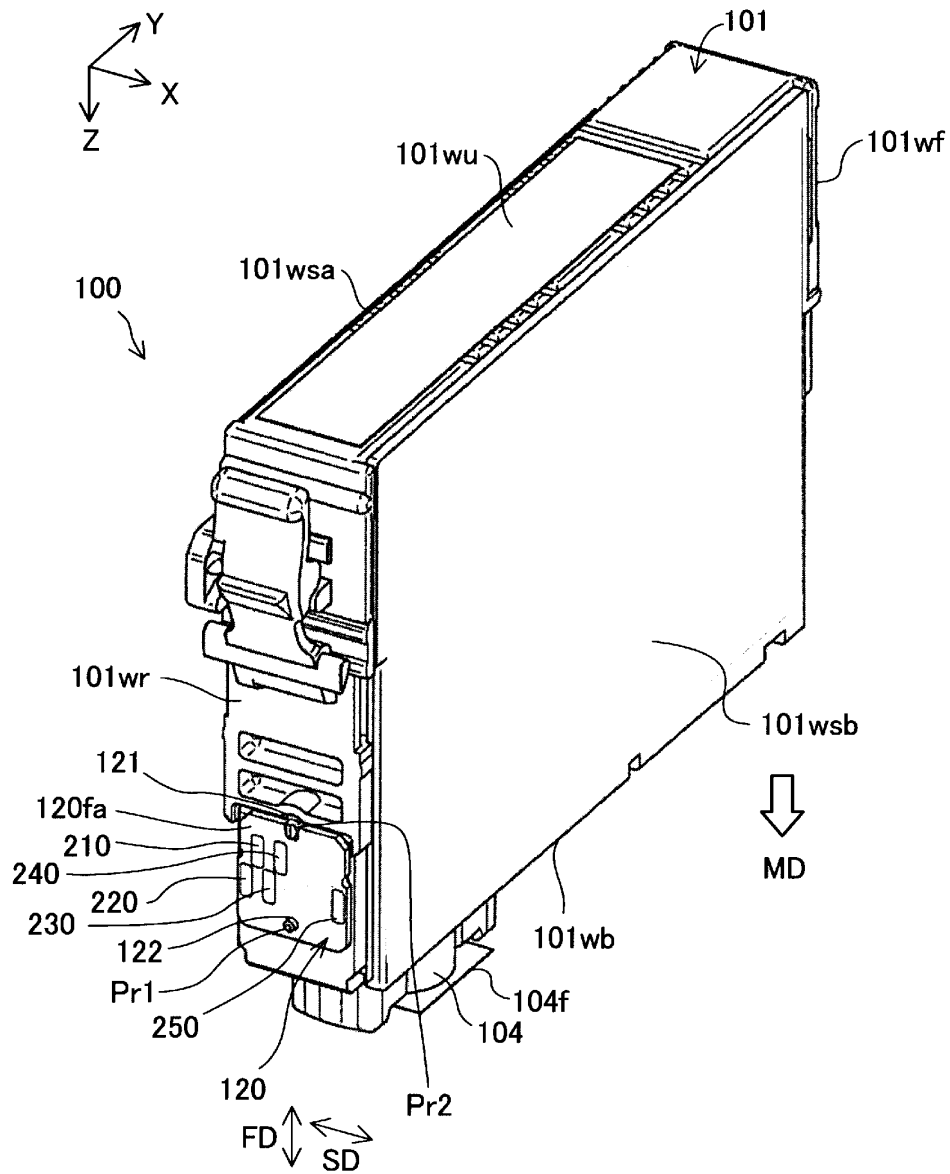
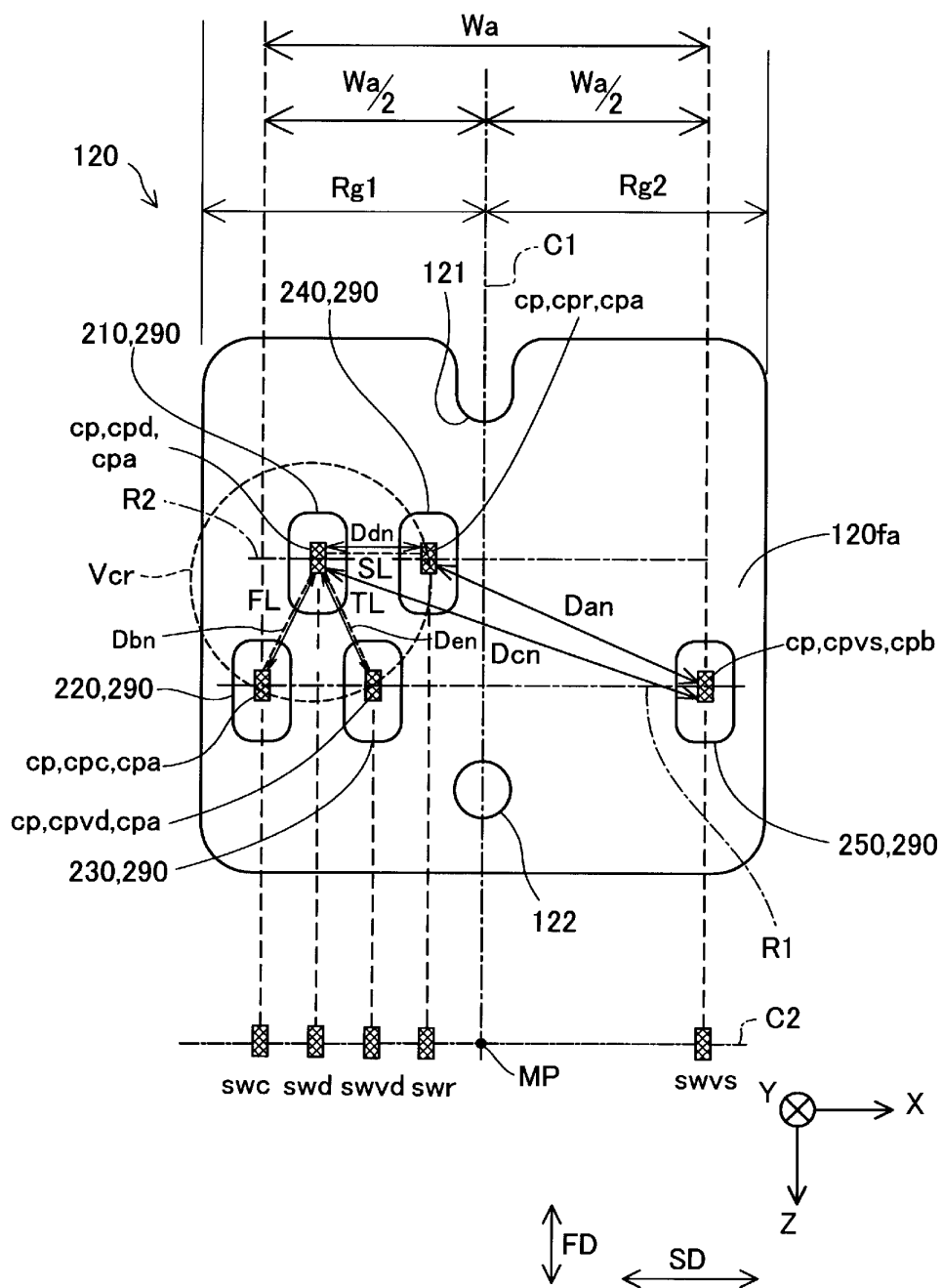
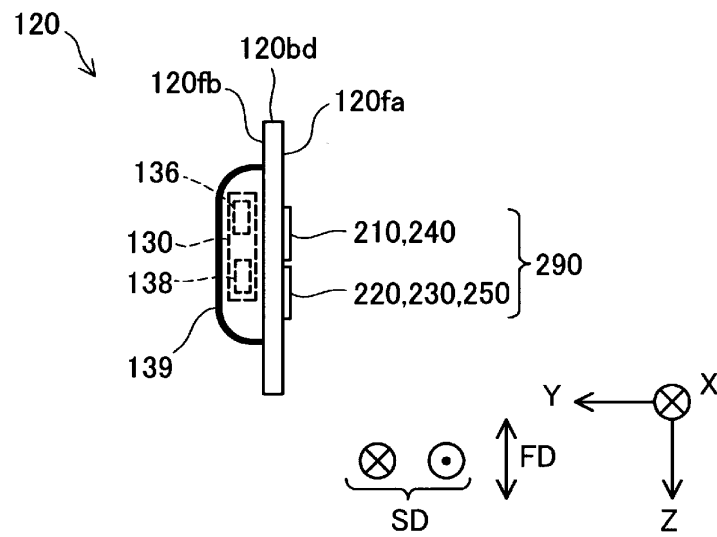


Fig.5



[図6]

Fig.6



[図7A]

Fig.7A

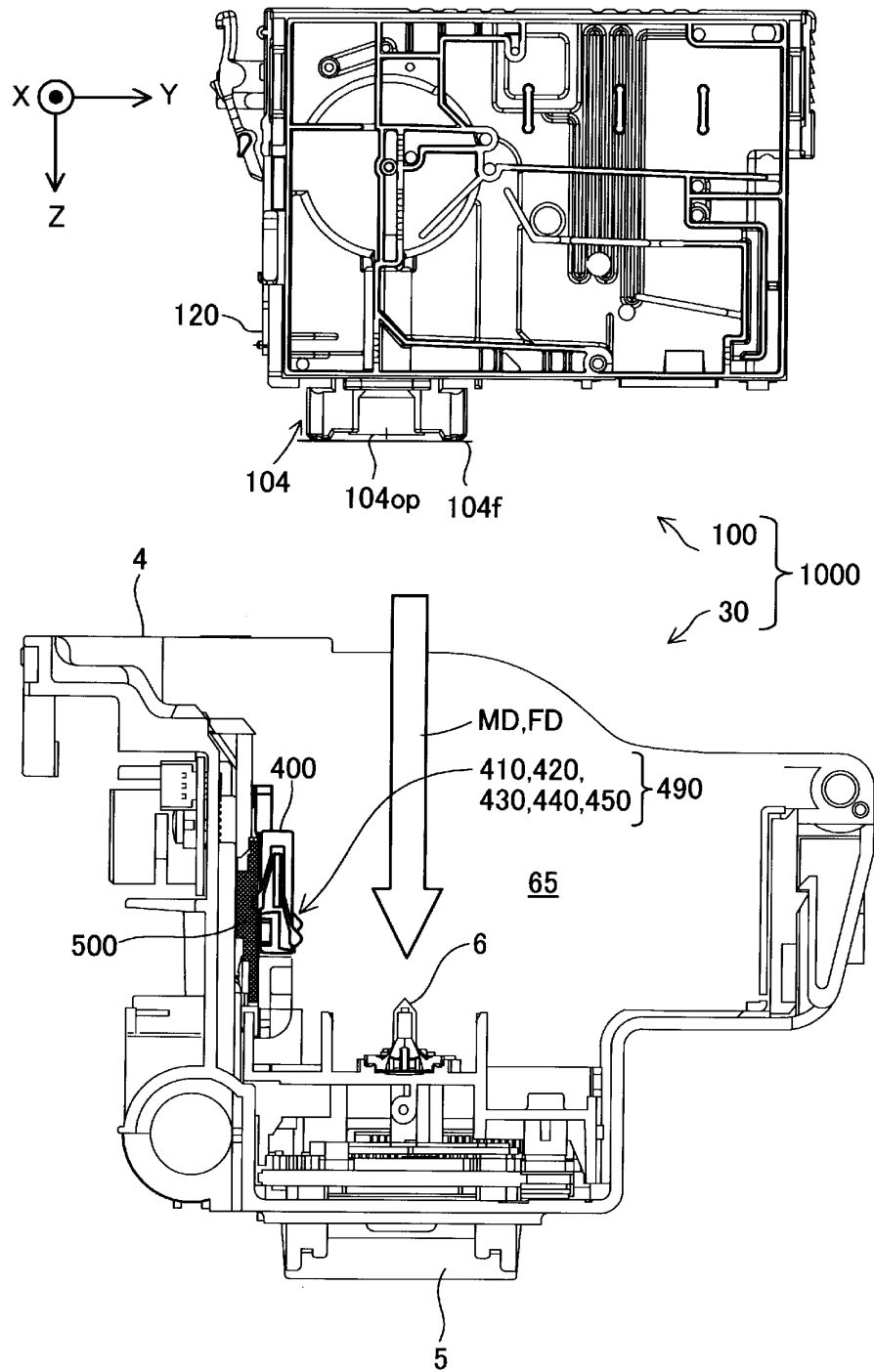
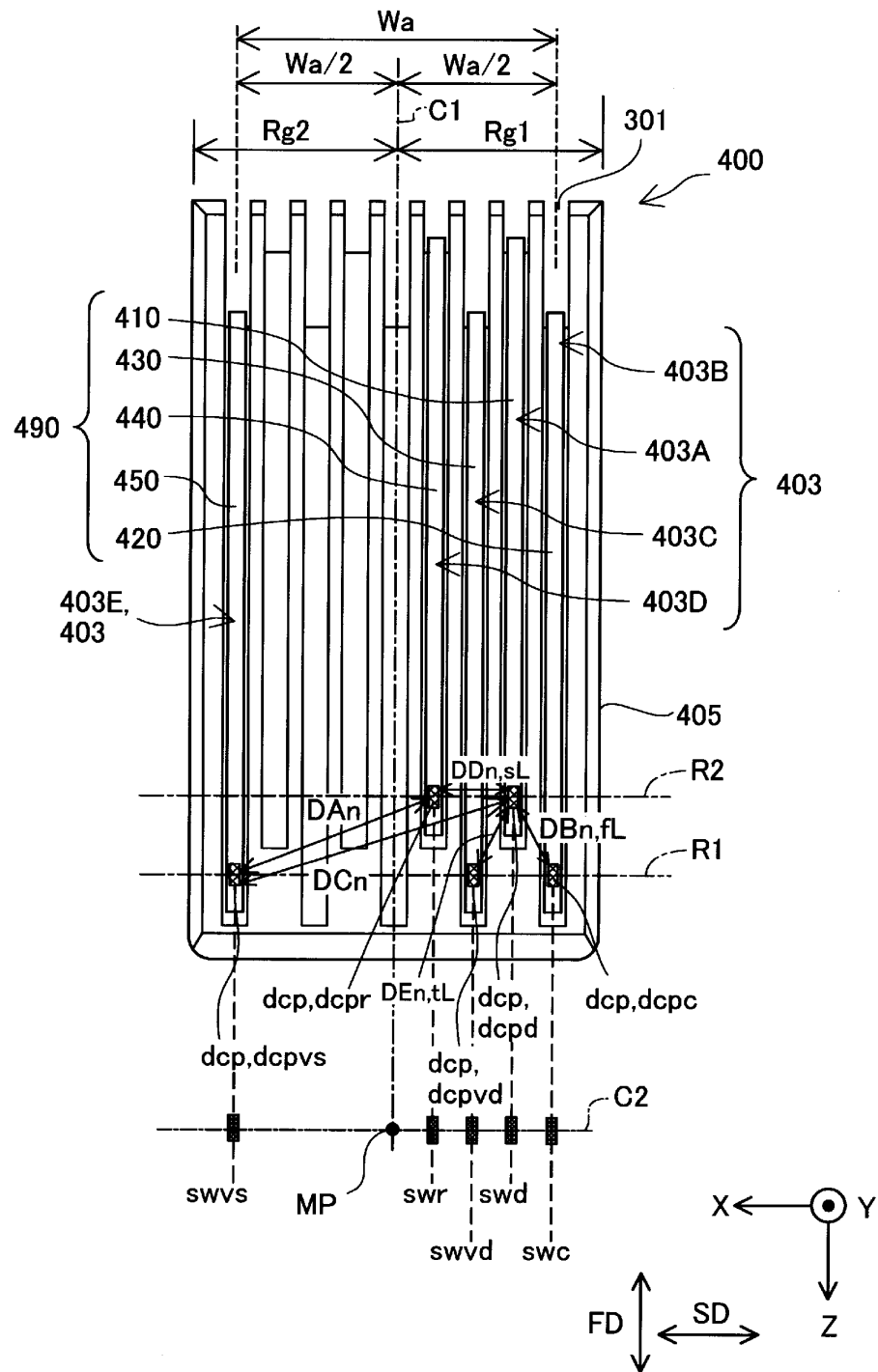
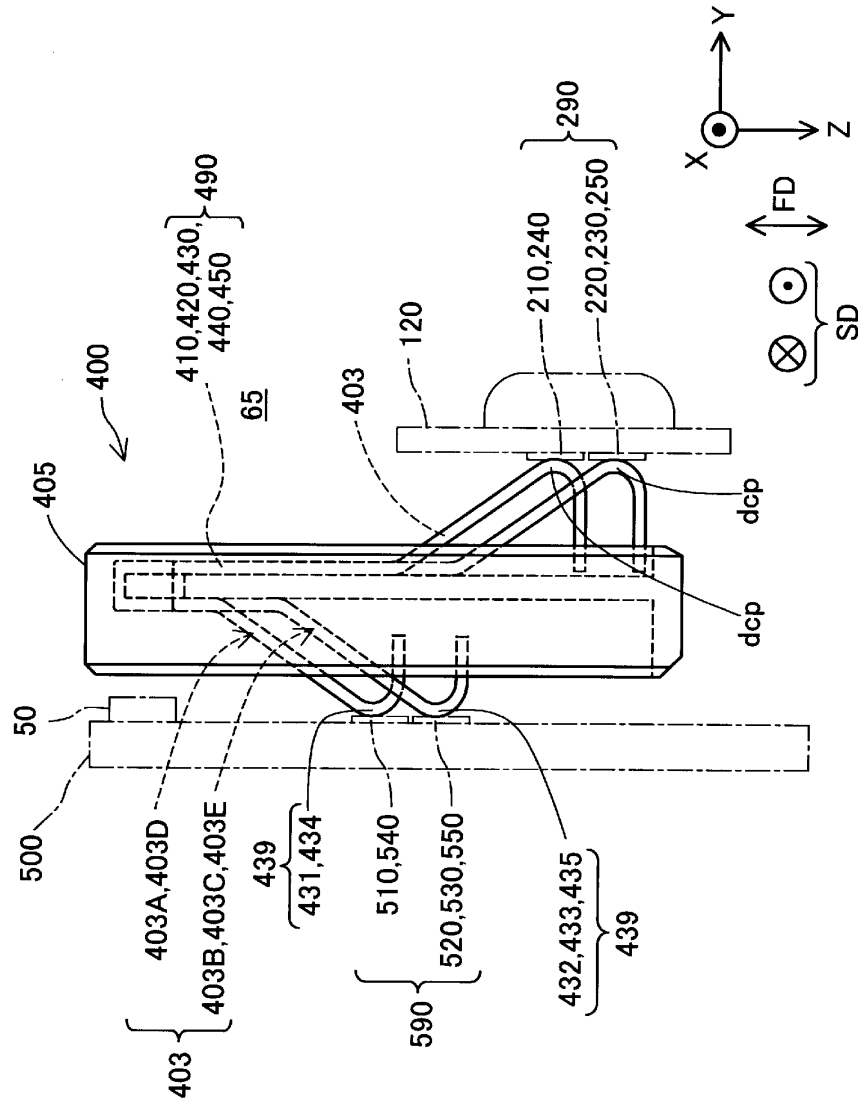


Fig.7B



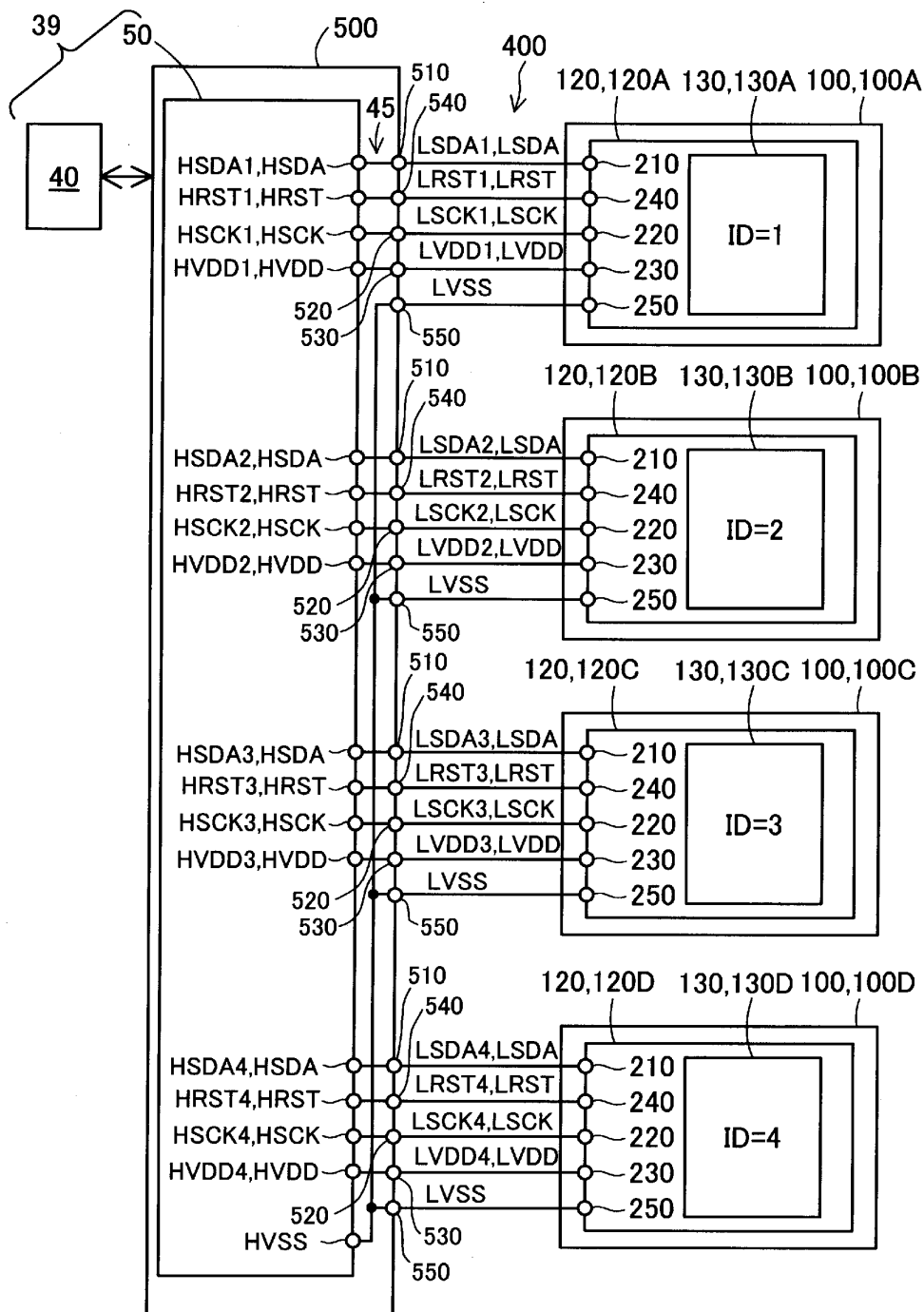
[図7C]

Fig.7C



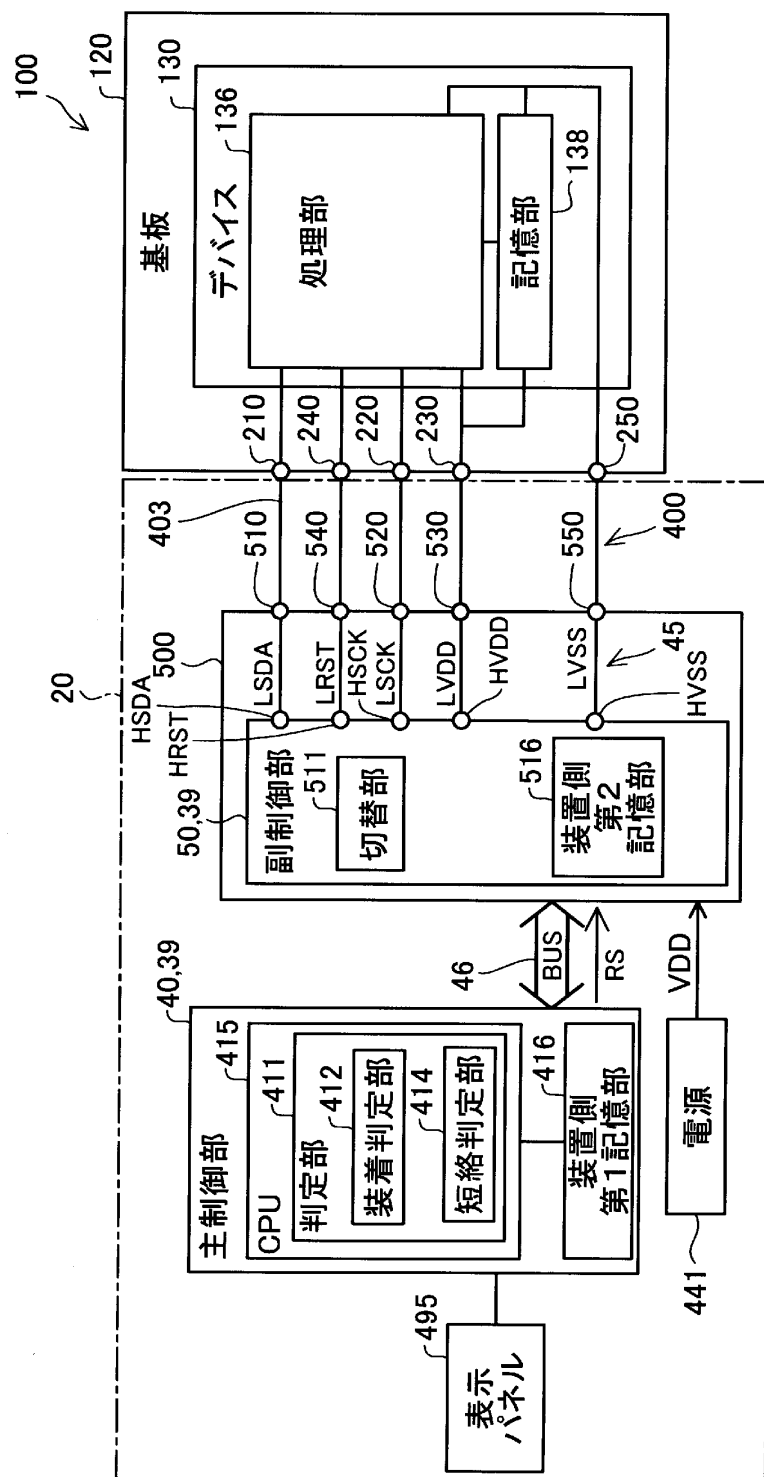
[図8]

Fig.8



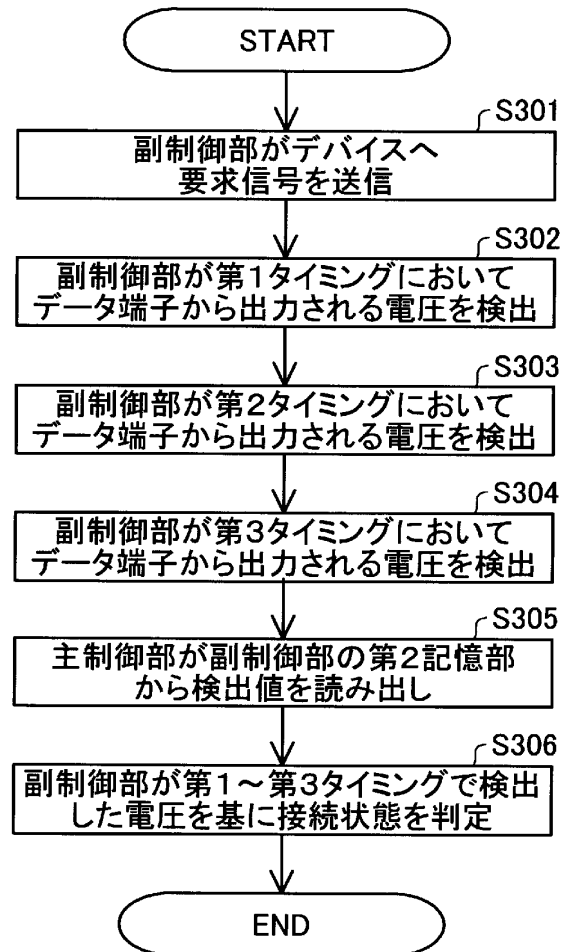
[図9]

Fig.9



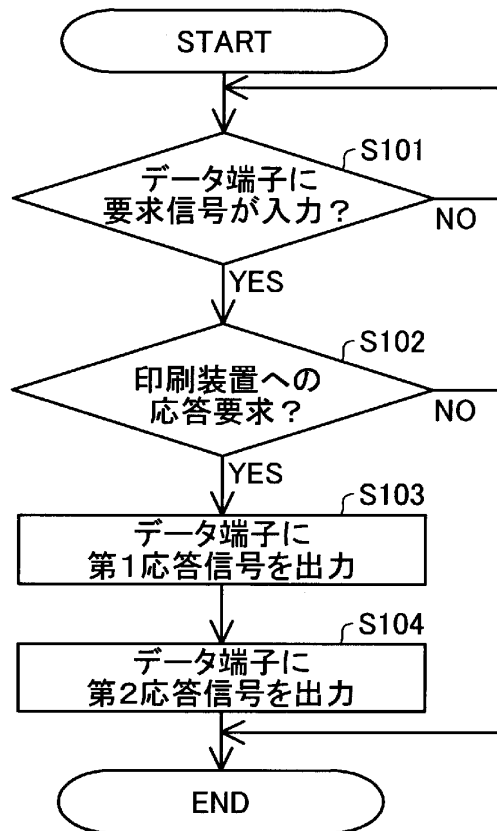
[図10A]

Fig.10A

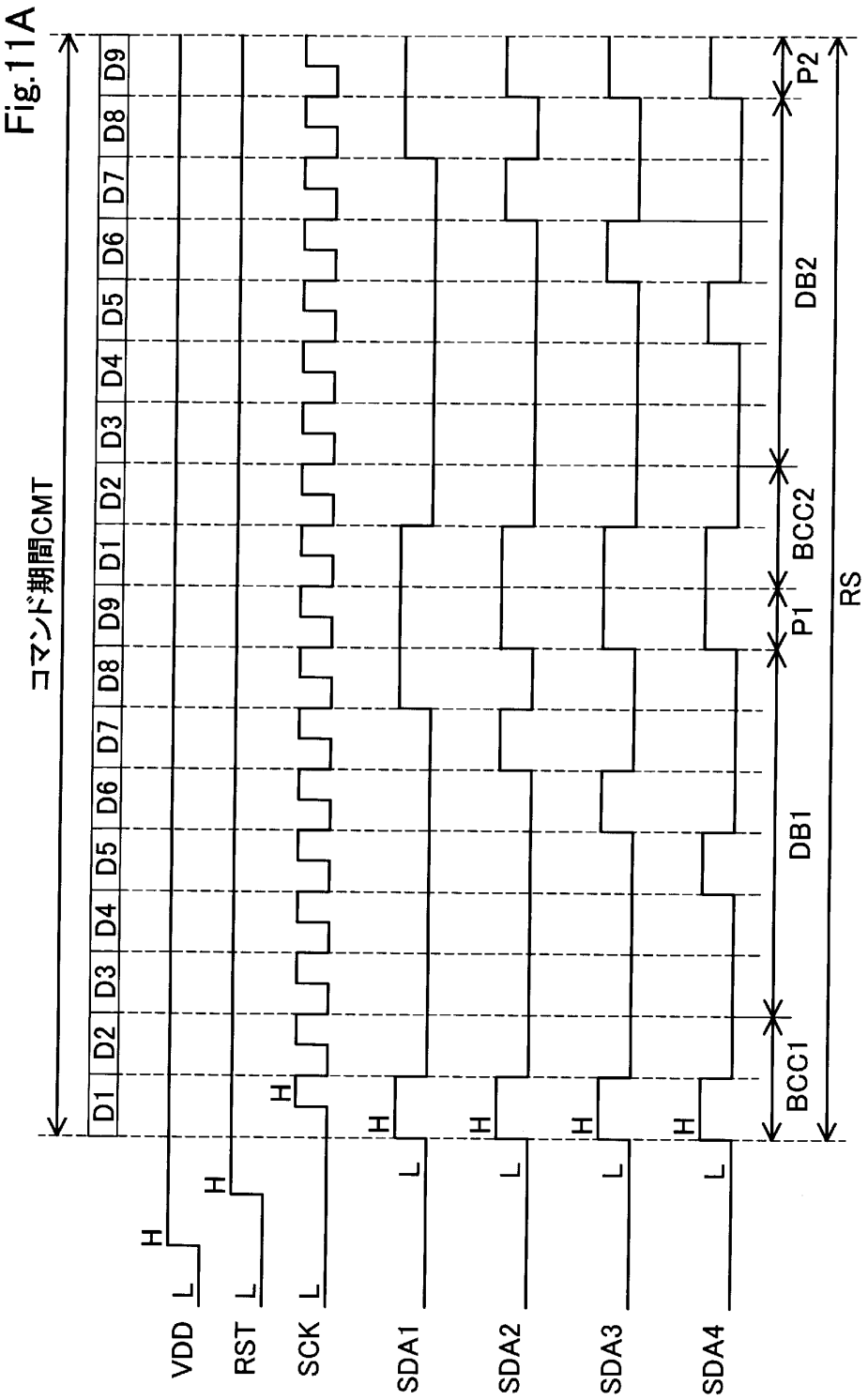


[図10B]

Fig.10B

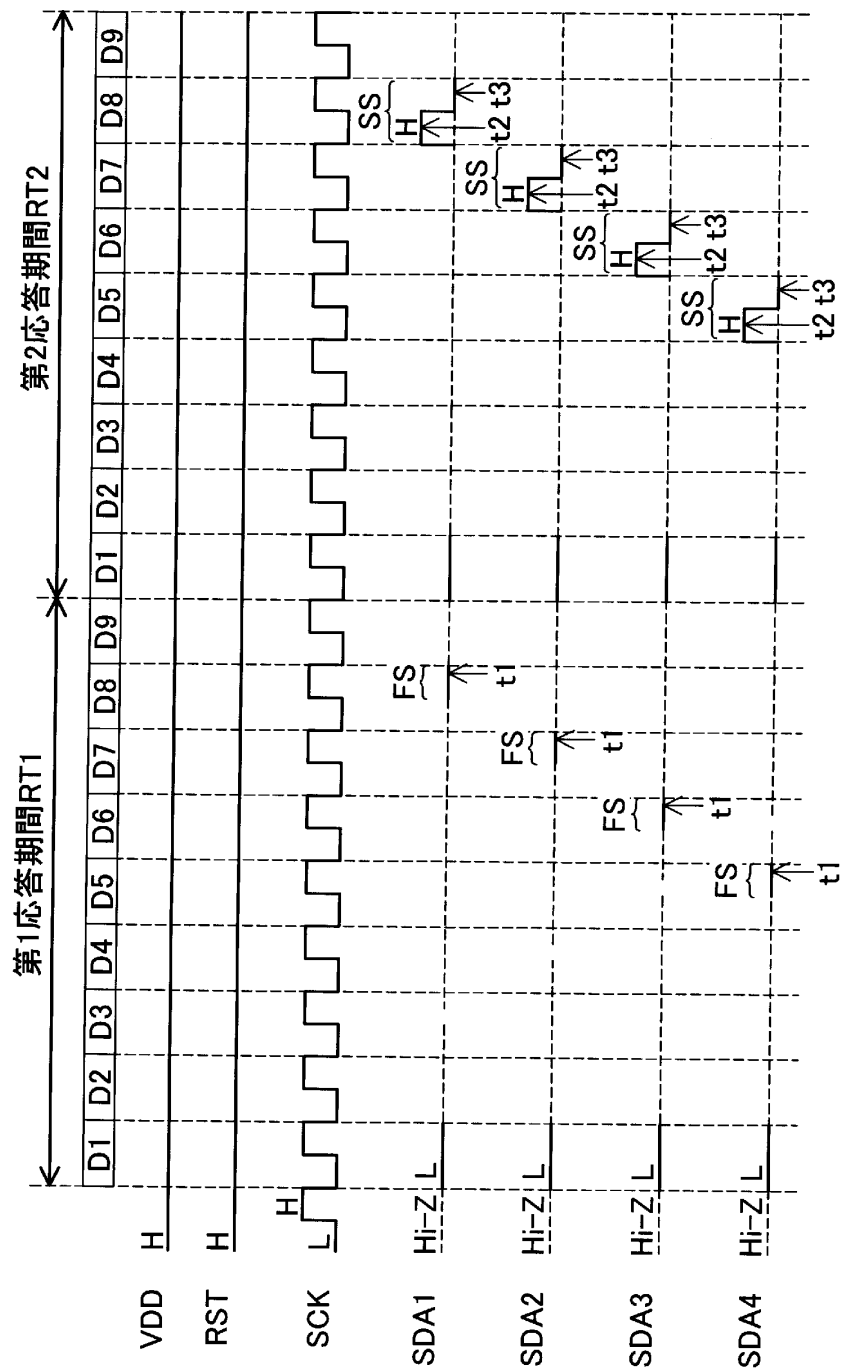


[図11A]



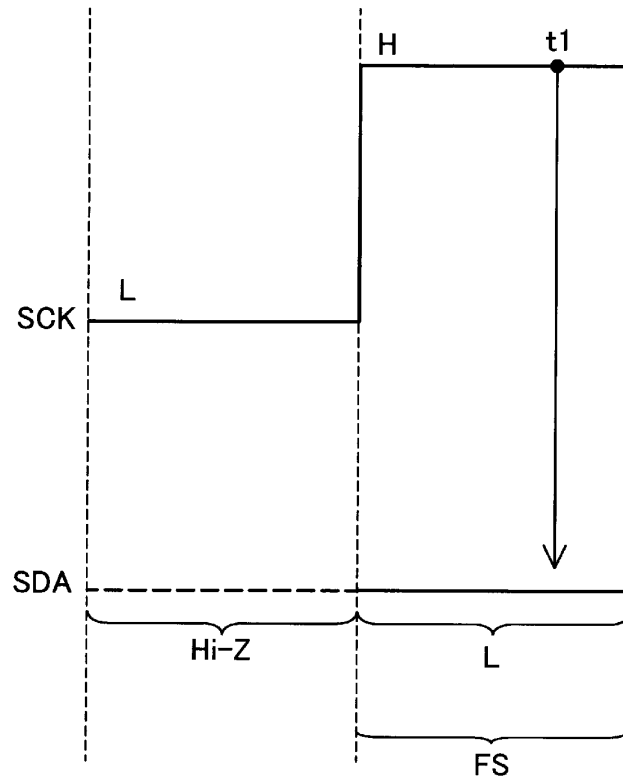
[図11B]

Fig.11B



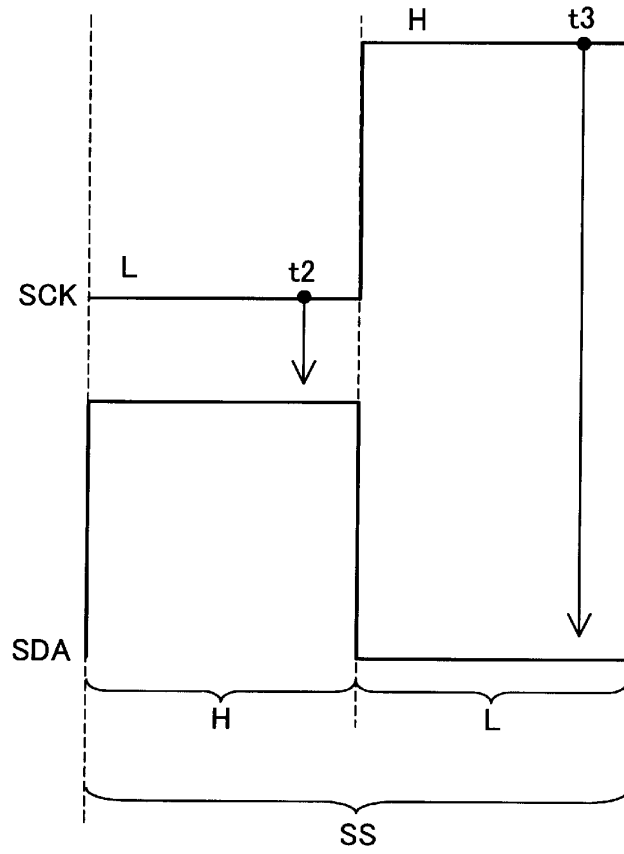
[図11C]

Fig.11C



[図11D]

Fig.11D



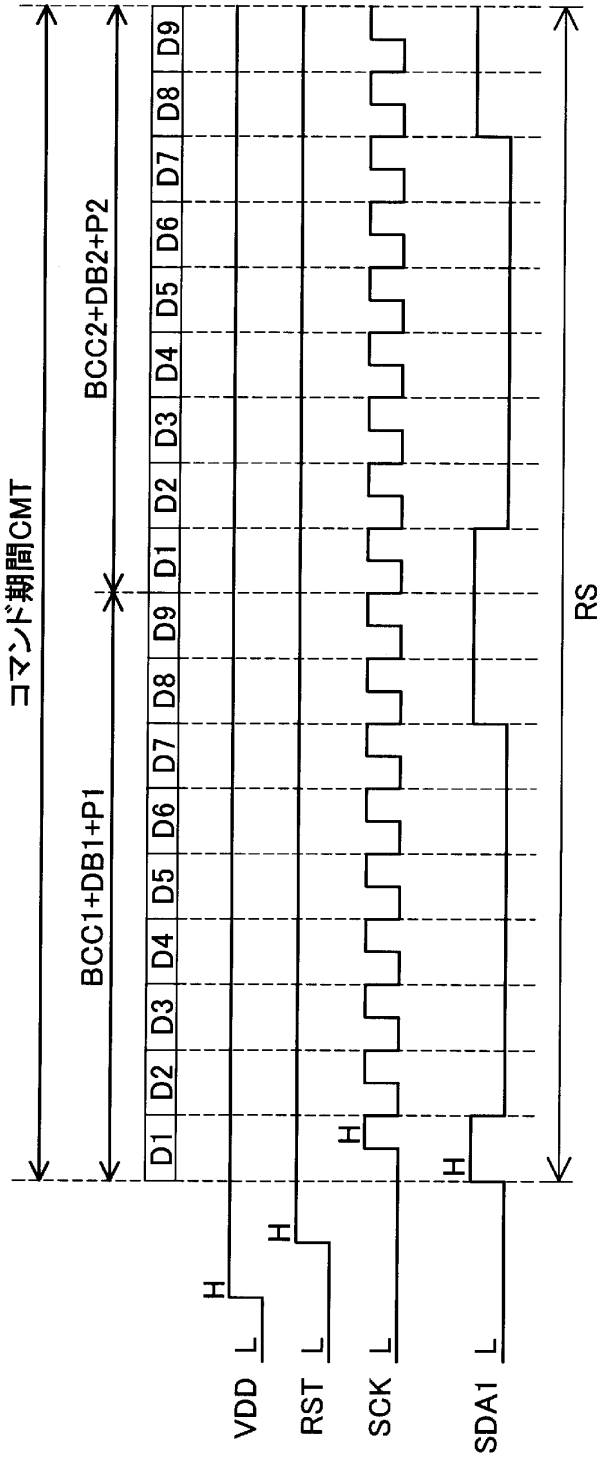
[図12]

Fig.12

	第1 タイミング t ₁	第2 タイミング t ₂	第3 タイミング t ₃	接続状態	判定結果
期待値	L (第1期待値)	H (第2期待値)	L (第3期待値)	—	—
検出値	L	H	L	装着完了状態	容器有り
	L	L	L	非装着完了状態	容器無し
	H	L	H	データ端子とクロック端子 が短絡状態	短絡有り
	H	H	H	データ端子と 電源端子が 短絡状態 又は データ端子と リセット端子が 短絡状態	短絡有り

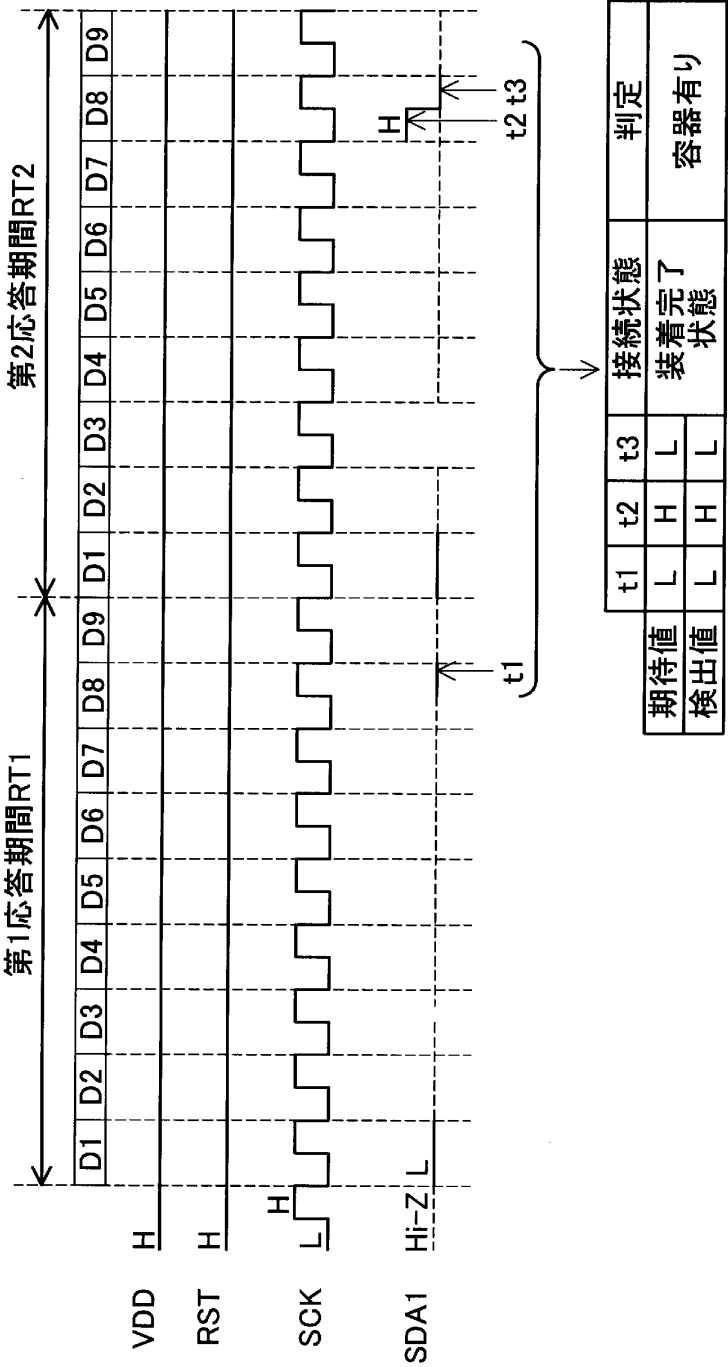
[図13A]

Fig. 13A

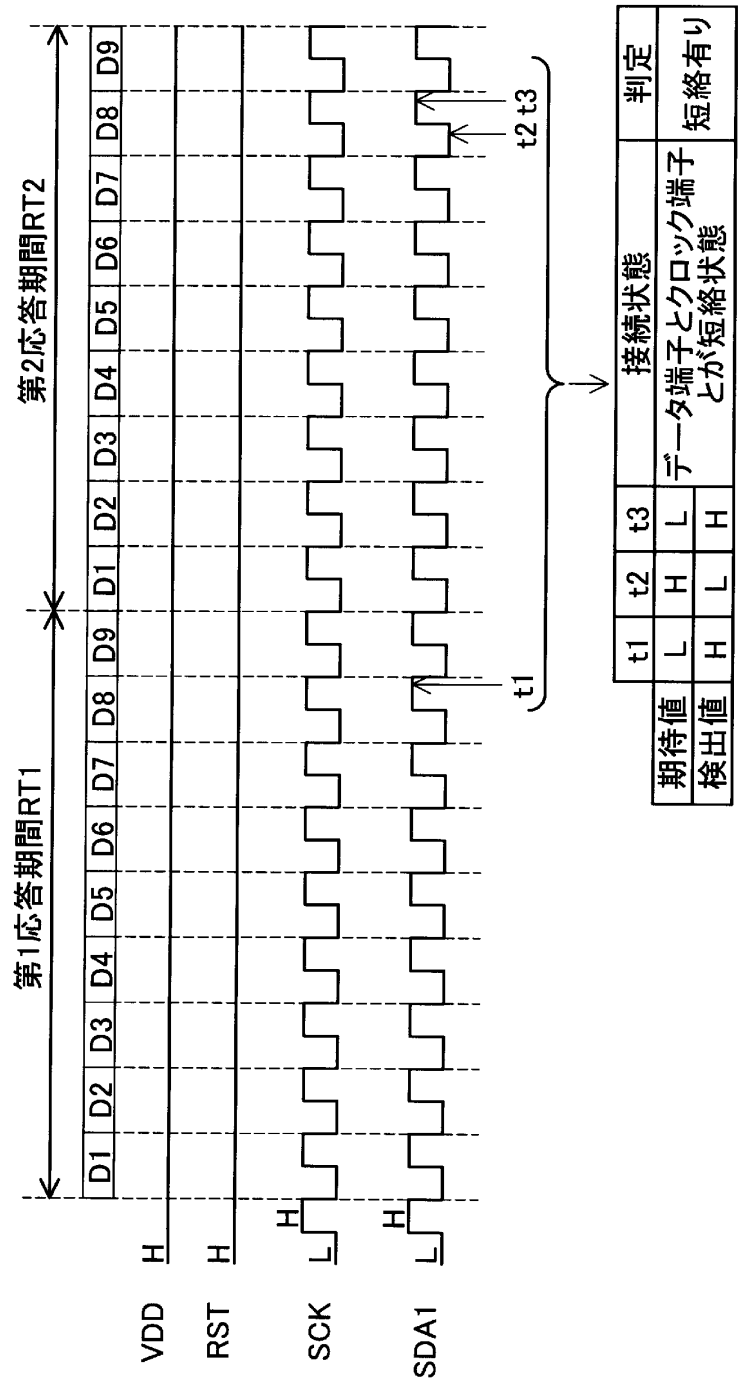


[図13B]

Fig. 13B

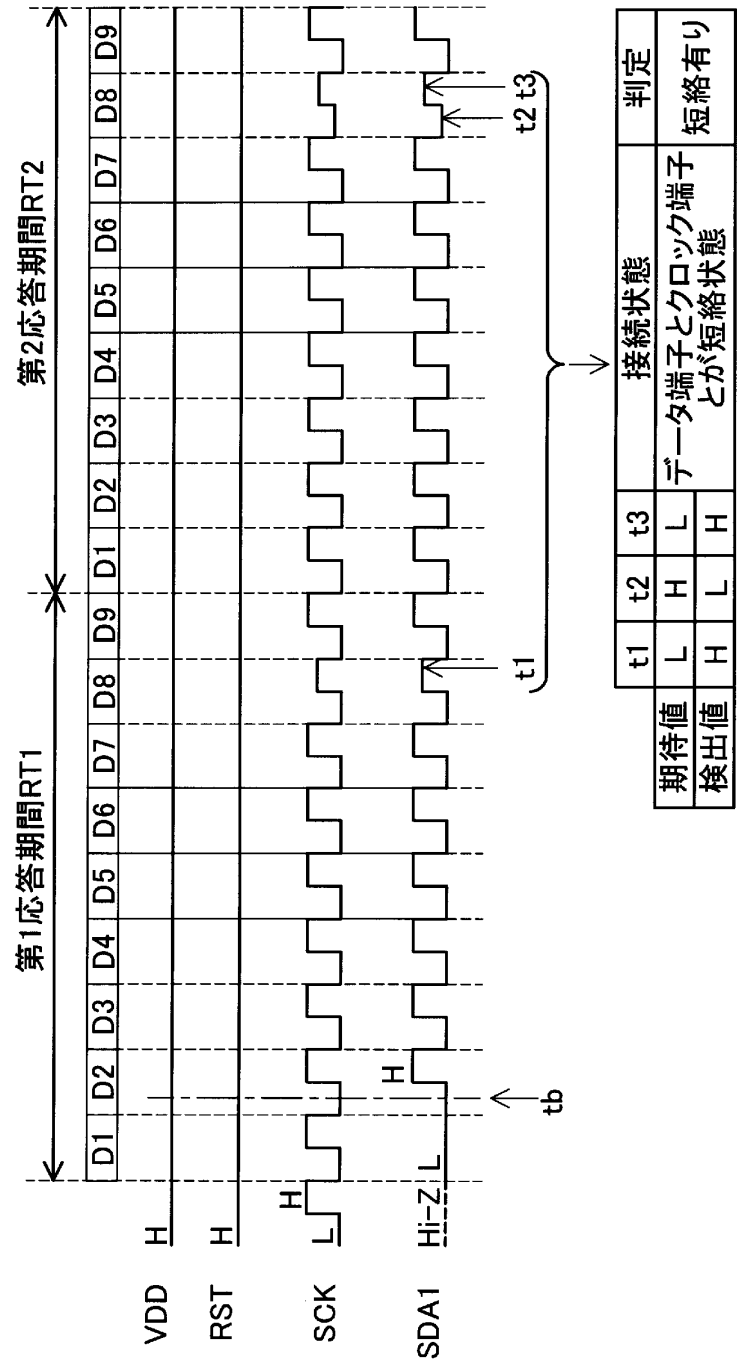


[図14B]
Fig.14B

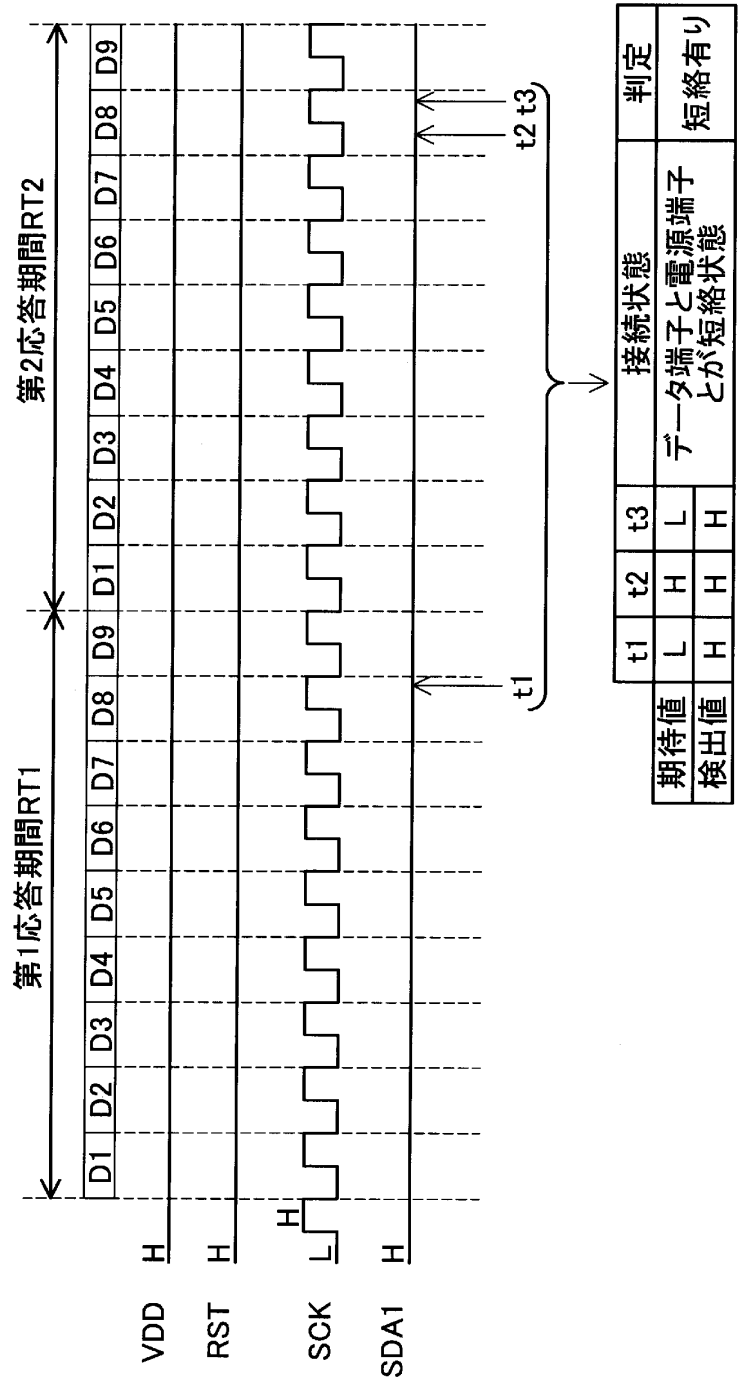


[図15]

Fig.15

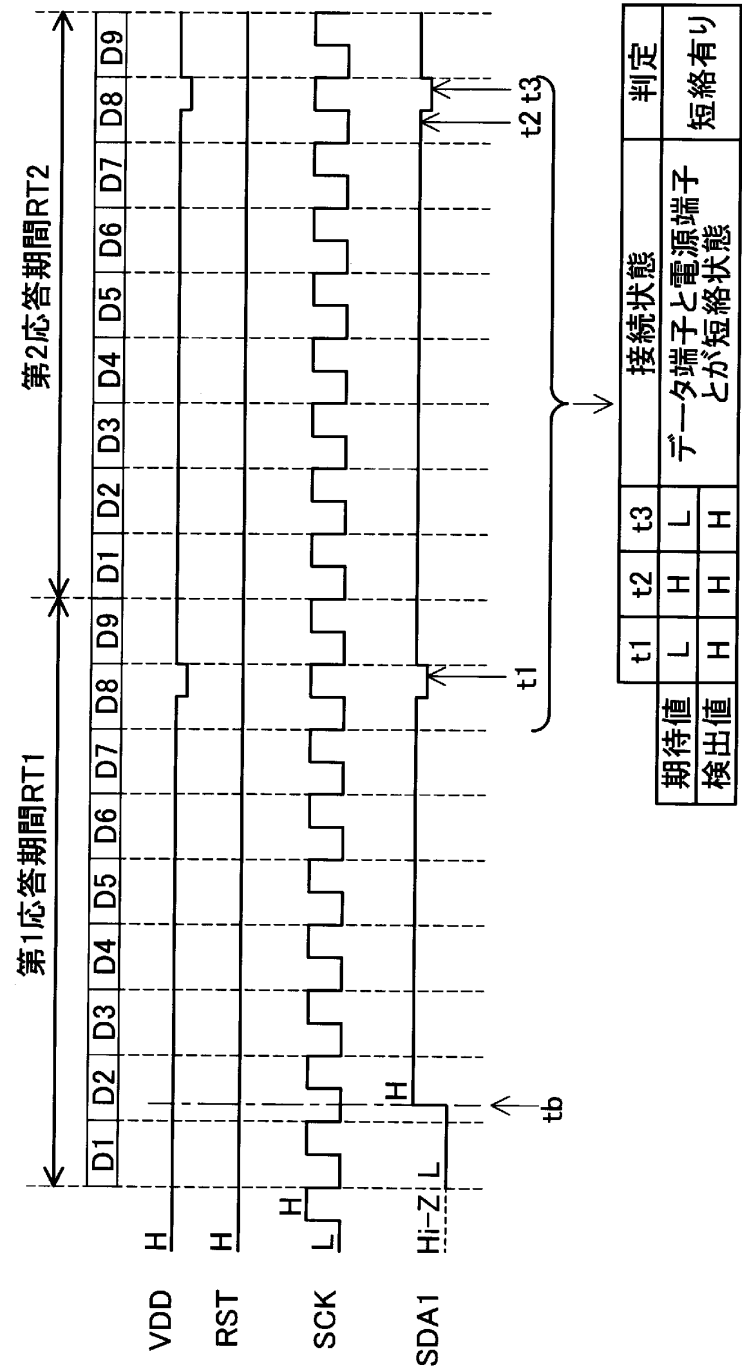


[図16B]
Fig. 16B



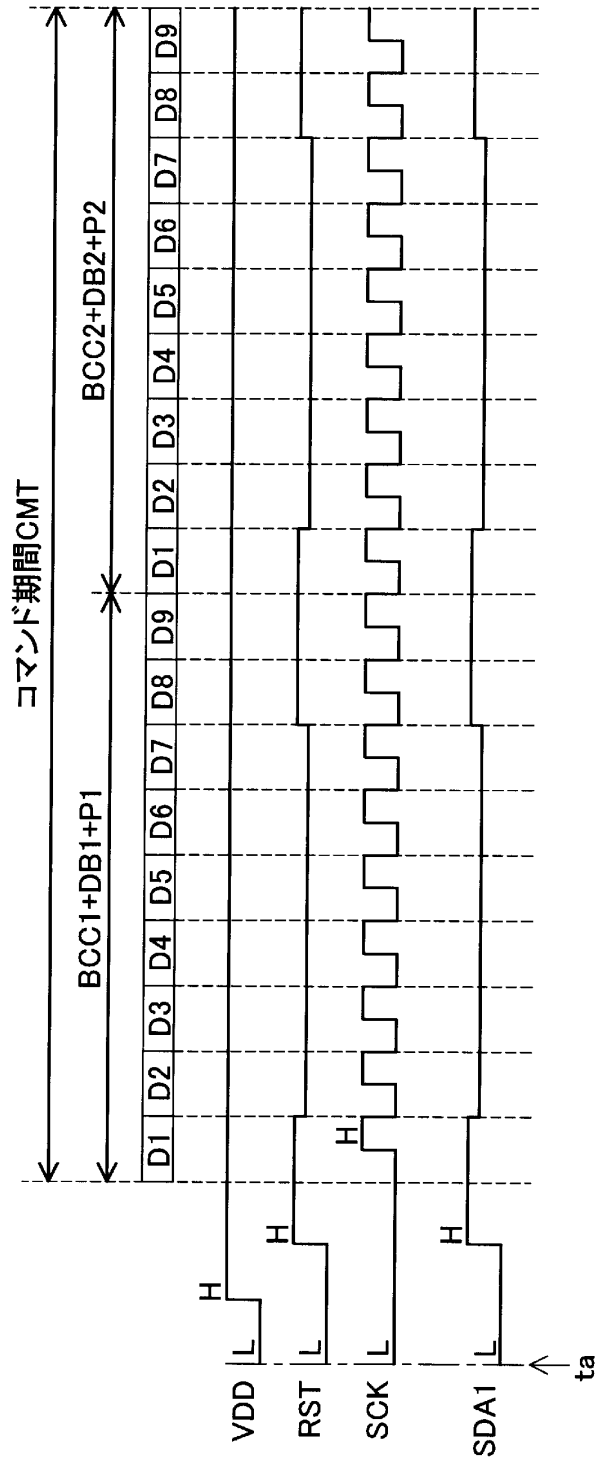
[図17]

Fig.17

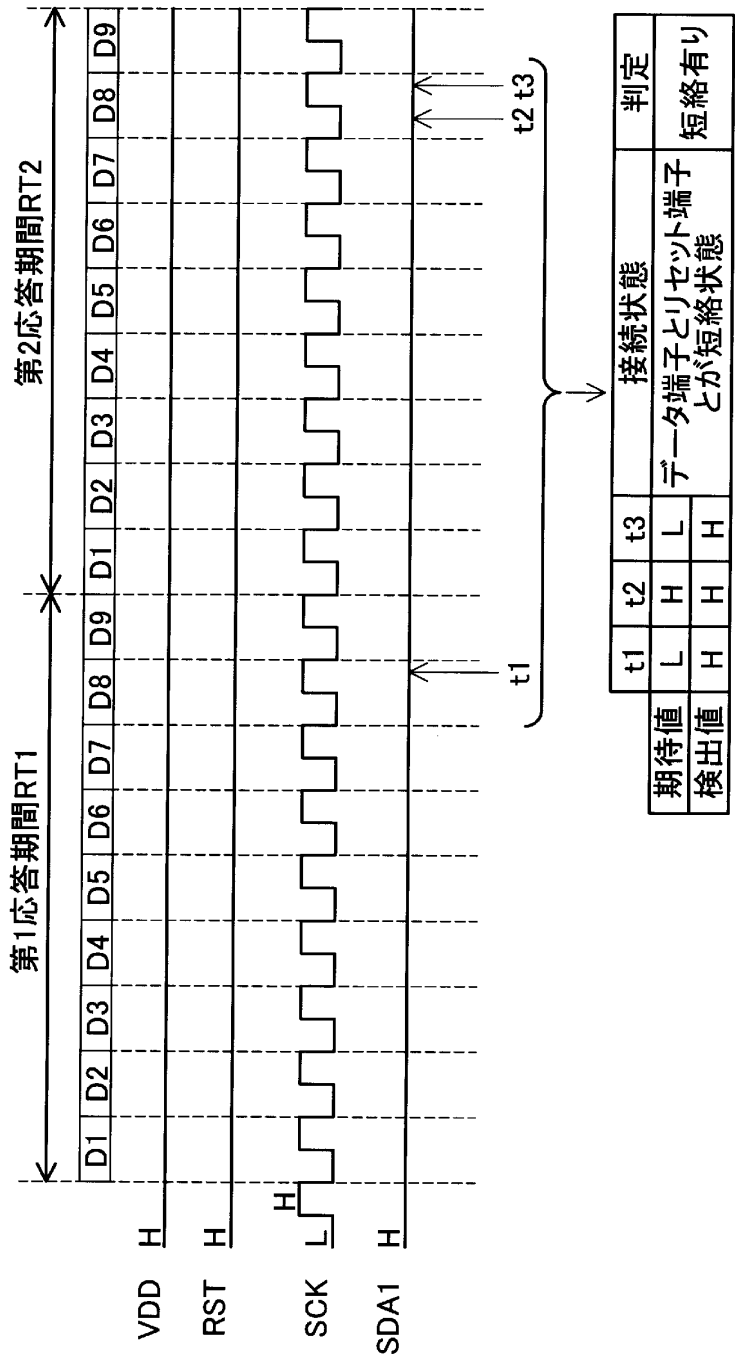


[図18A]

Fig.18A

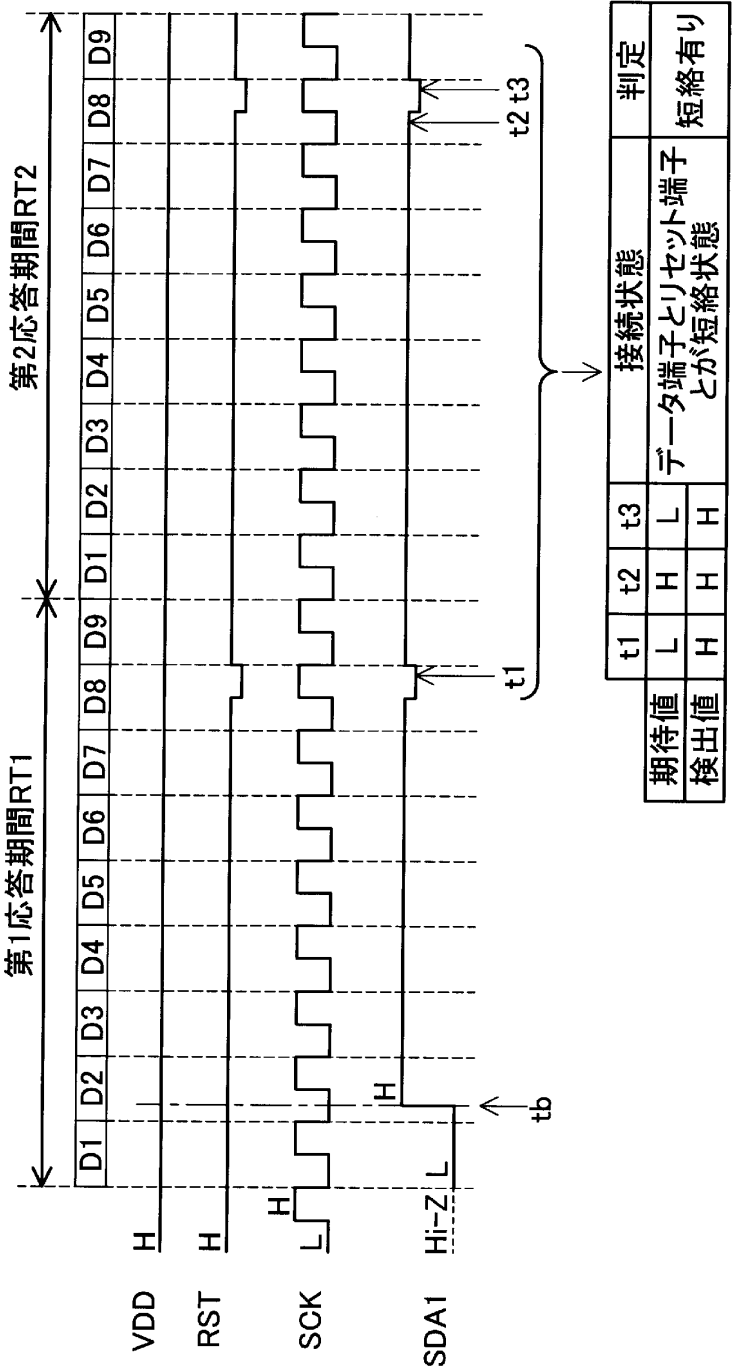


[図18B]
Fig. 18B



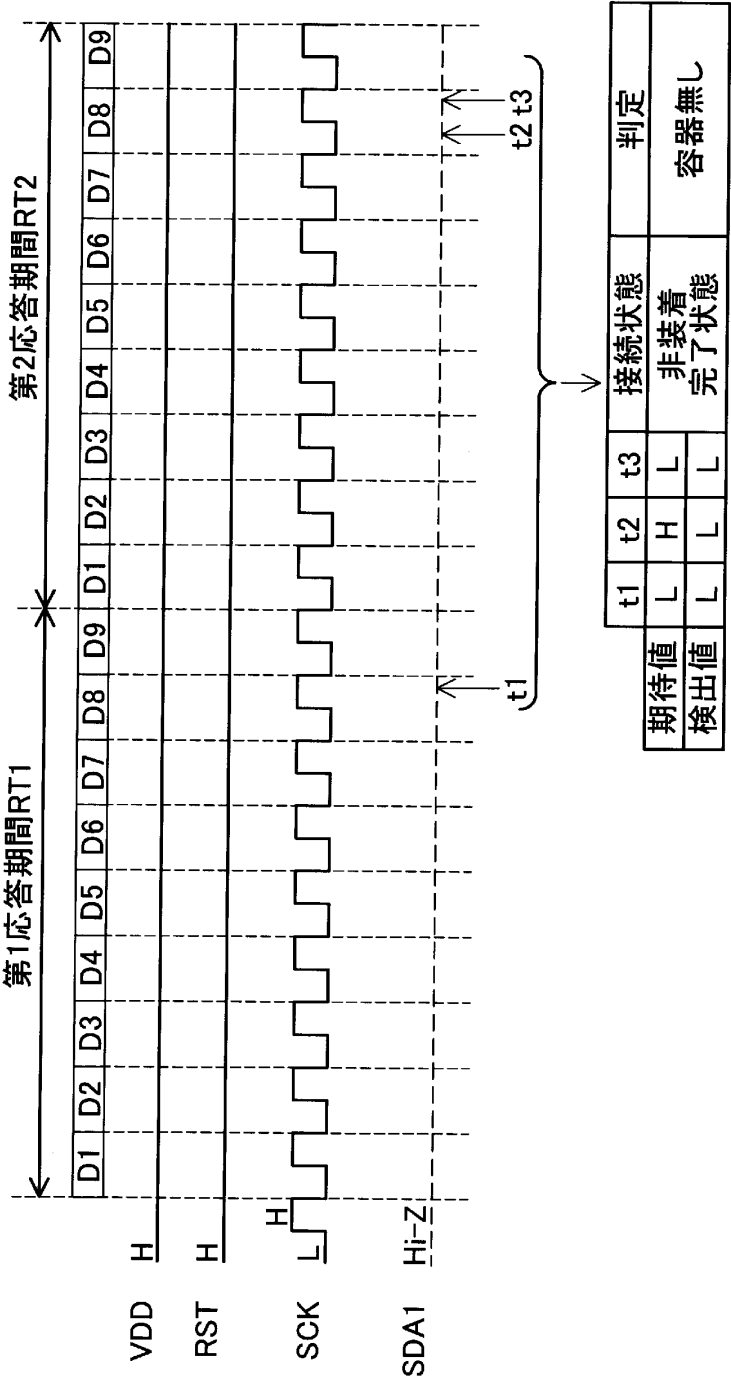
[図19]

Fig.19



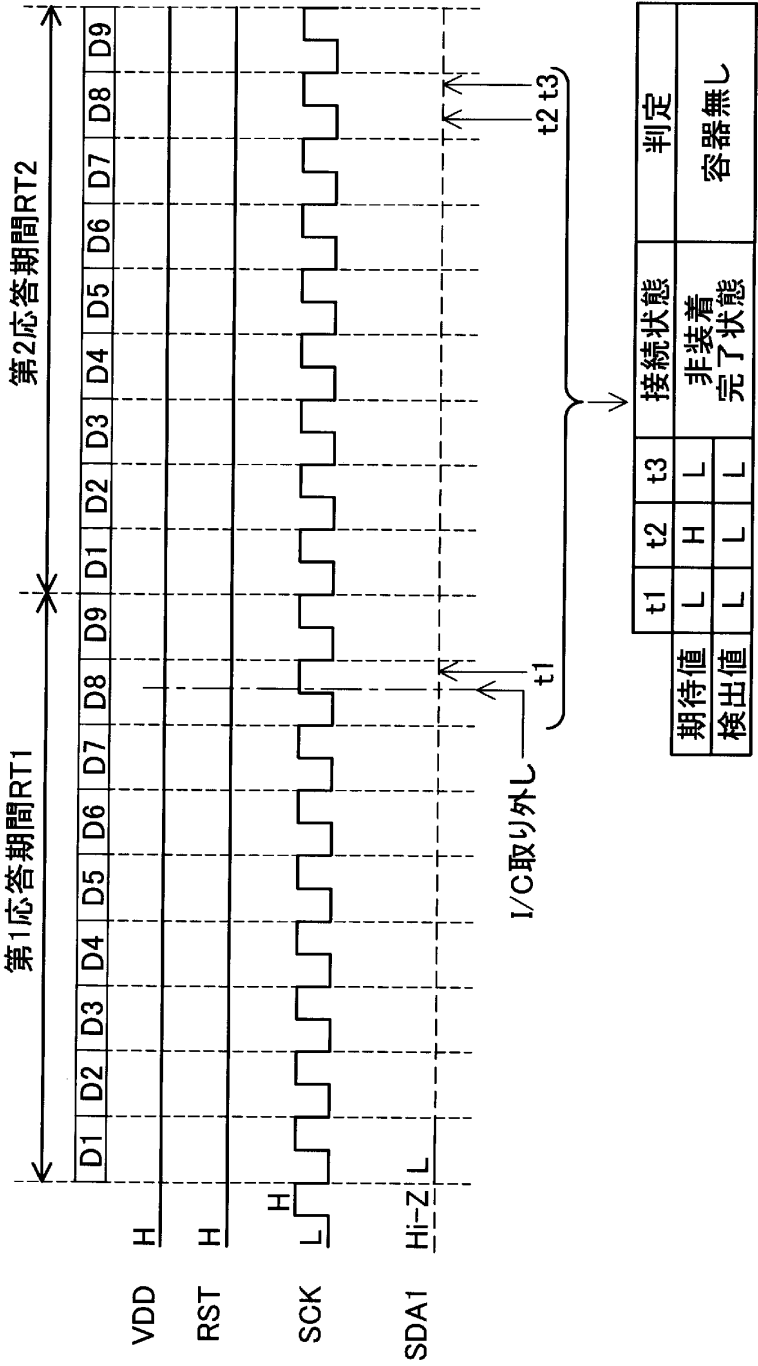
[図20A]

Fig.20A



[図20B]

Fig.20B



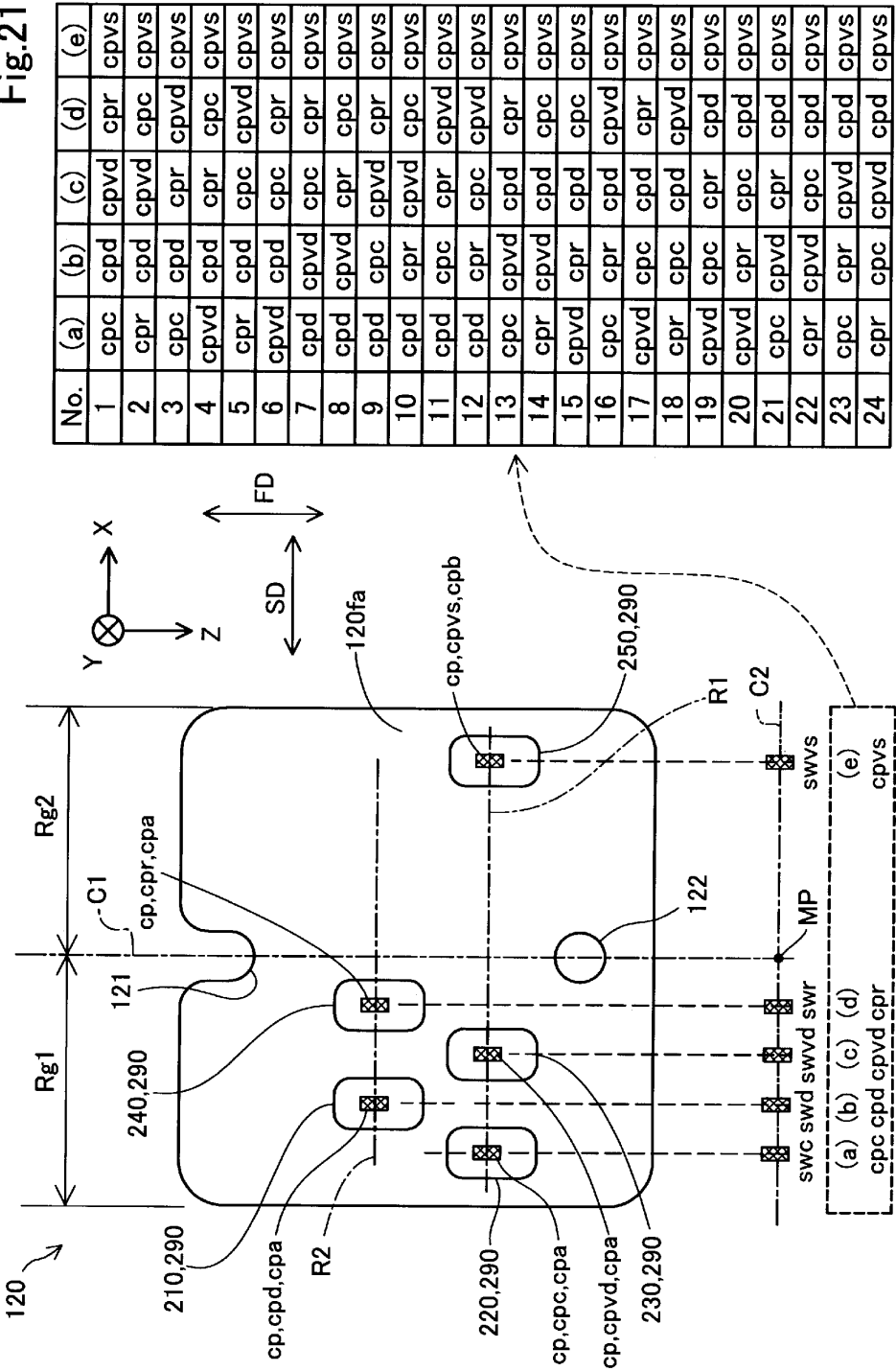
[図20C]

Fig.20C

	タイミング			接続状態	判定	No.
	t1	t2	t3			
期待値	L	H	L	—		
検出値	H	L	H	t<t1で データ端子とクロック端子 の短絡状態発生	短絡 有り	1
	L	L	H	t1<t<t2で データ端子とクロック端子 の短絡状態発生	短絡 有り	2
	L	H	H	t2<t<t3で データ端子とクロック端子 の短絡状態発生	短絡 有り	3
	H	H	L	t1<t<t2で データ端子とクロック端子 の短絡状態解消	短絡 有り	4
	H	L	L	t2<t<t3で データ端子とクロック端子 の短絡状態解消	短絡 有り	5
	H	H	H	t<t1で データ端子と電源端子or データ端子とリセット端子 の短絡状態発生	短絡 有り	6
	L	H	H	t1<t<t2で データ端子と電源端子or データ端子とリセット端子 の短絡状態発生	短絡 有り	7
	L	H	H	t2<t<t3で データ端子と電源端子or データ端子とリセット端子 の短絡状態発生	短絡 有り	8
	H	H	L	t1<t<t2で データ端子と電源端子、 データ端子とリセット端子 の短絡状態解消	短絡 有り	9
	H	H	L	t2<t<t3で データ端子と電源端子、 データ端子とリセット端子 の短絡状態解消	短絡 有り	10

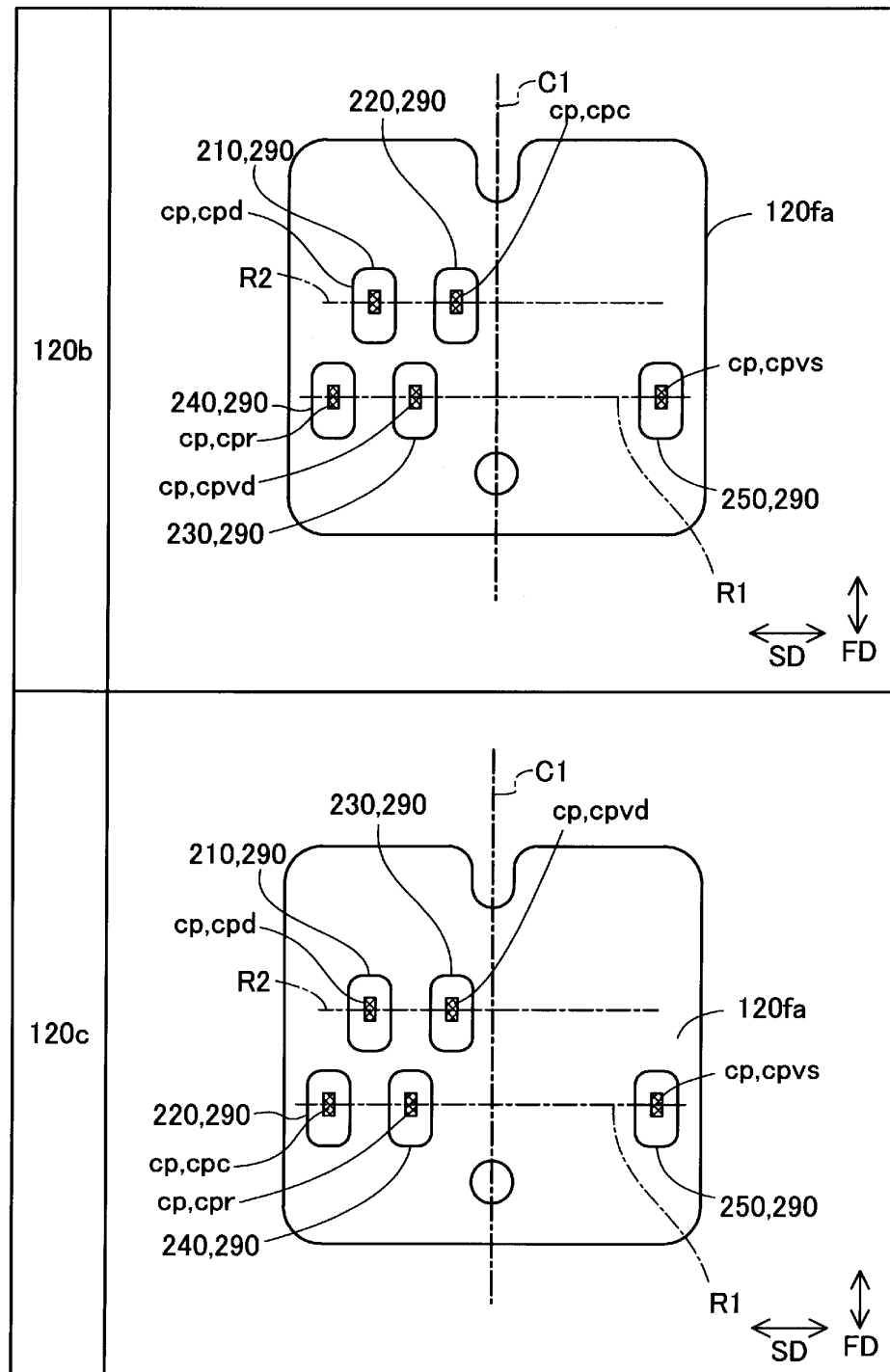
[図21A]

Fig.21A



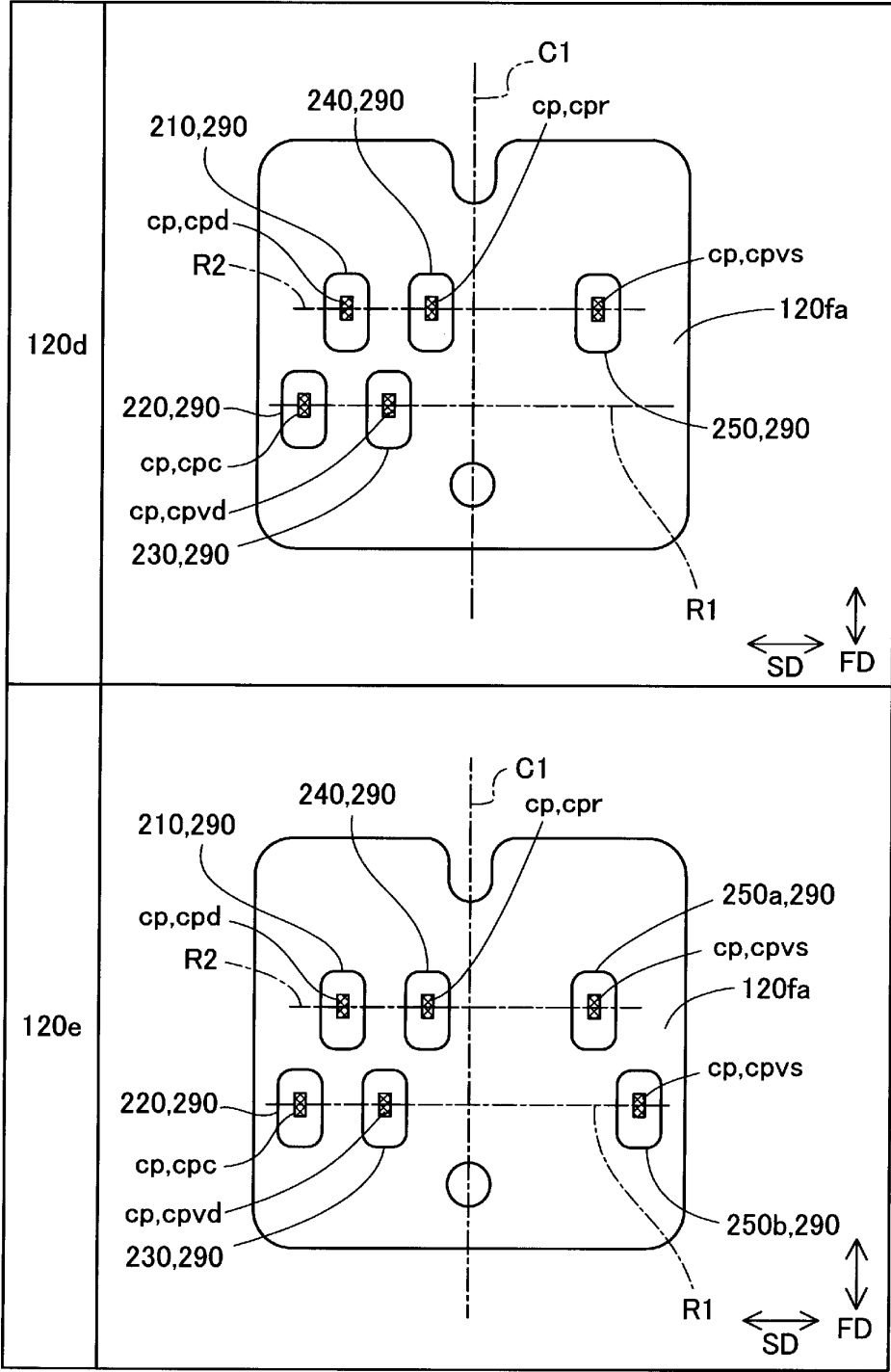
[図21B]

Fig.21B



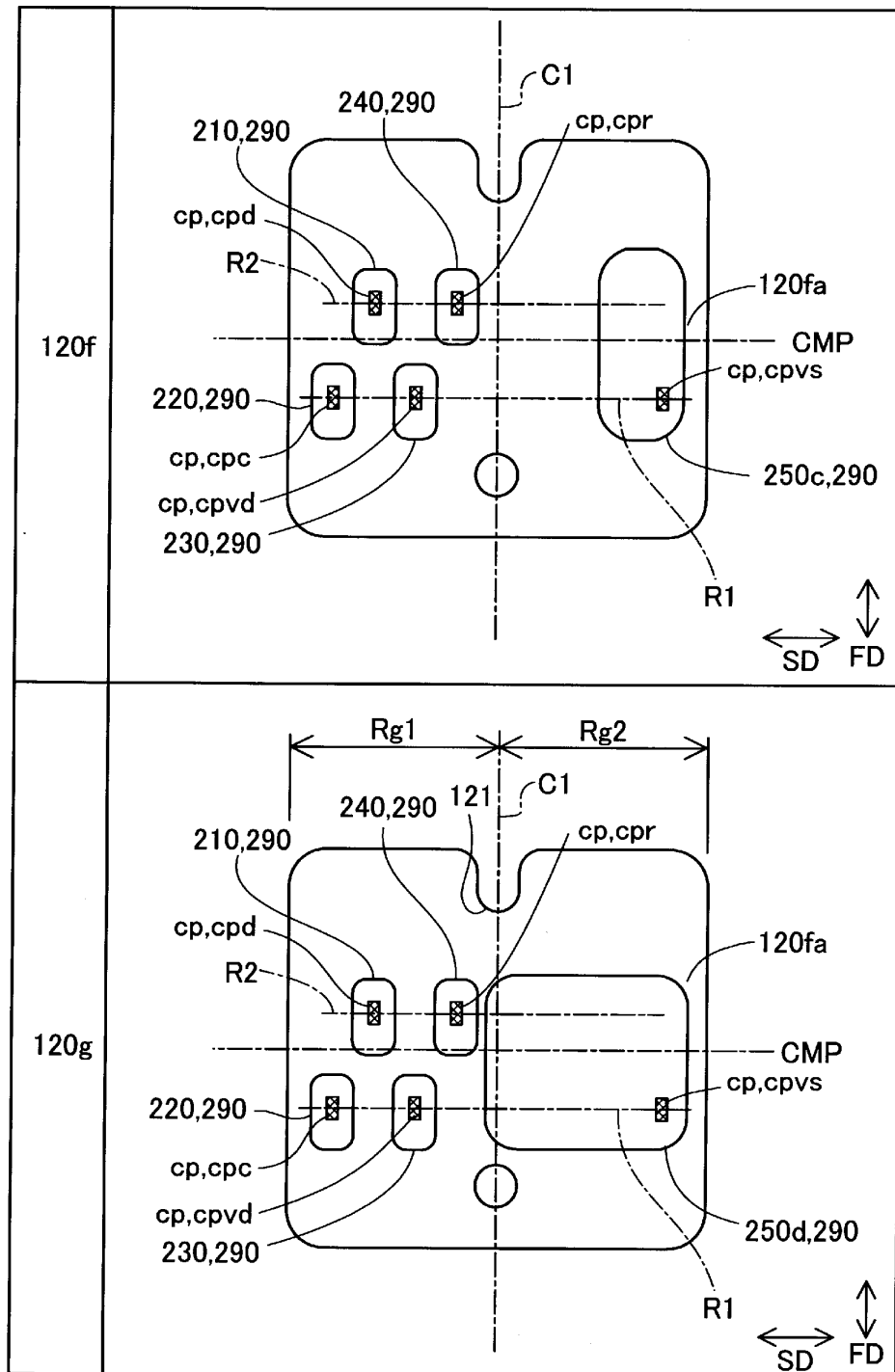
[図22]

Fig.22



[図23]

Fig.23



[図24]

Fig.24

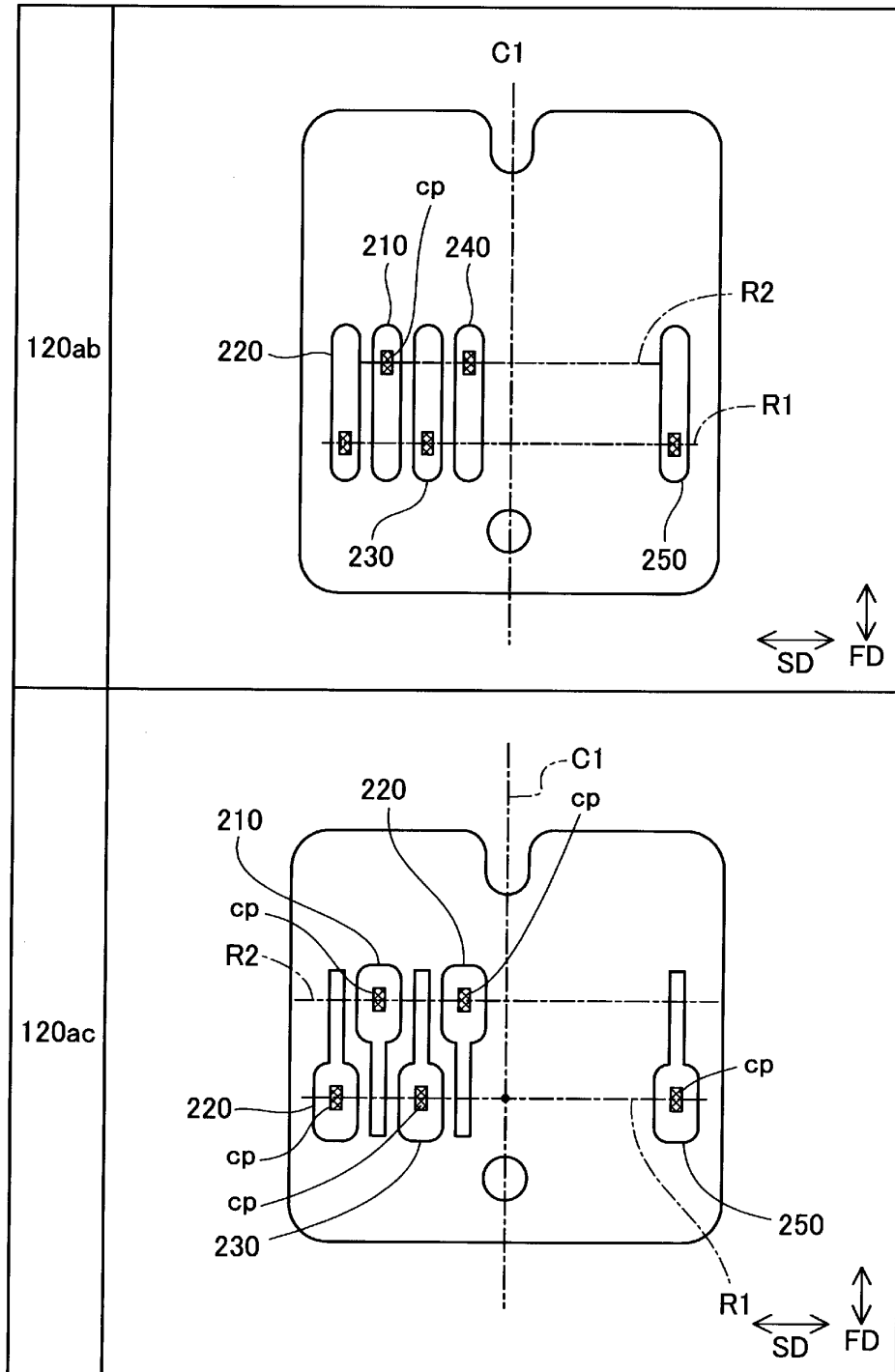


Fig.25

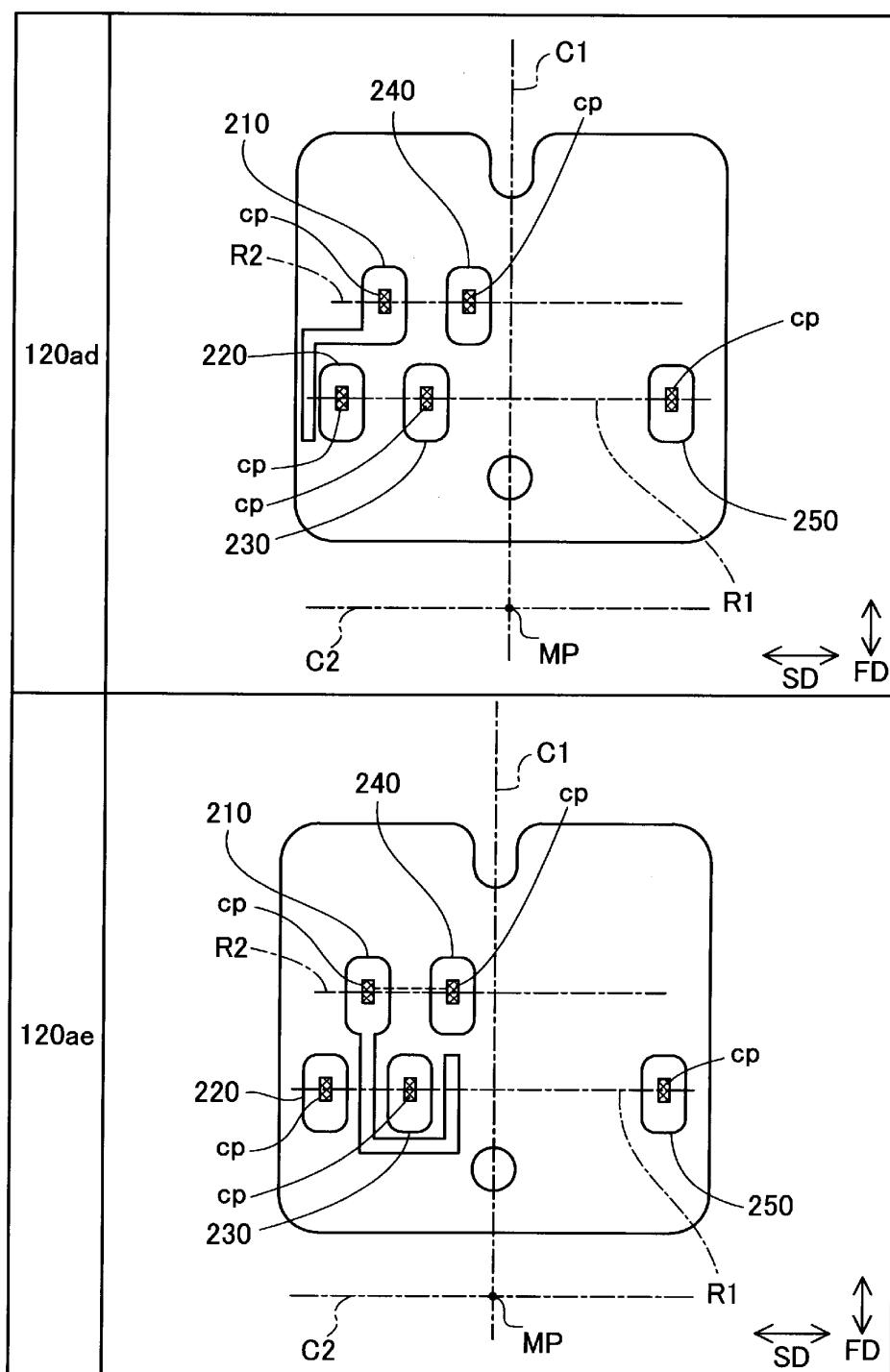
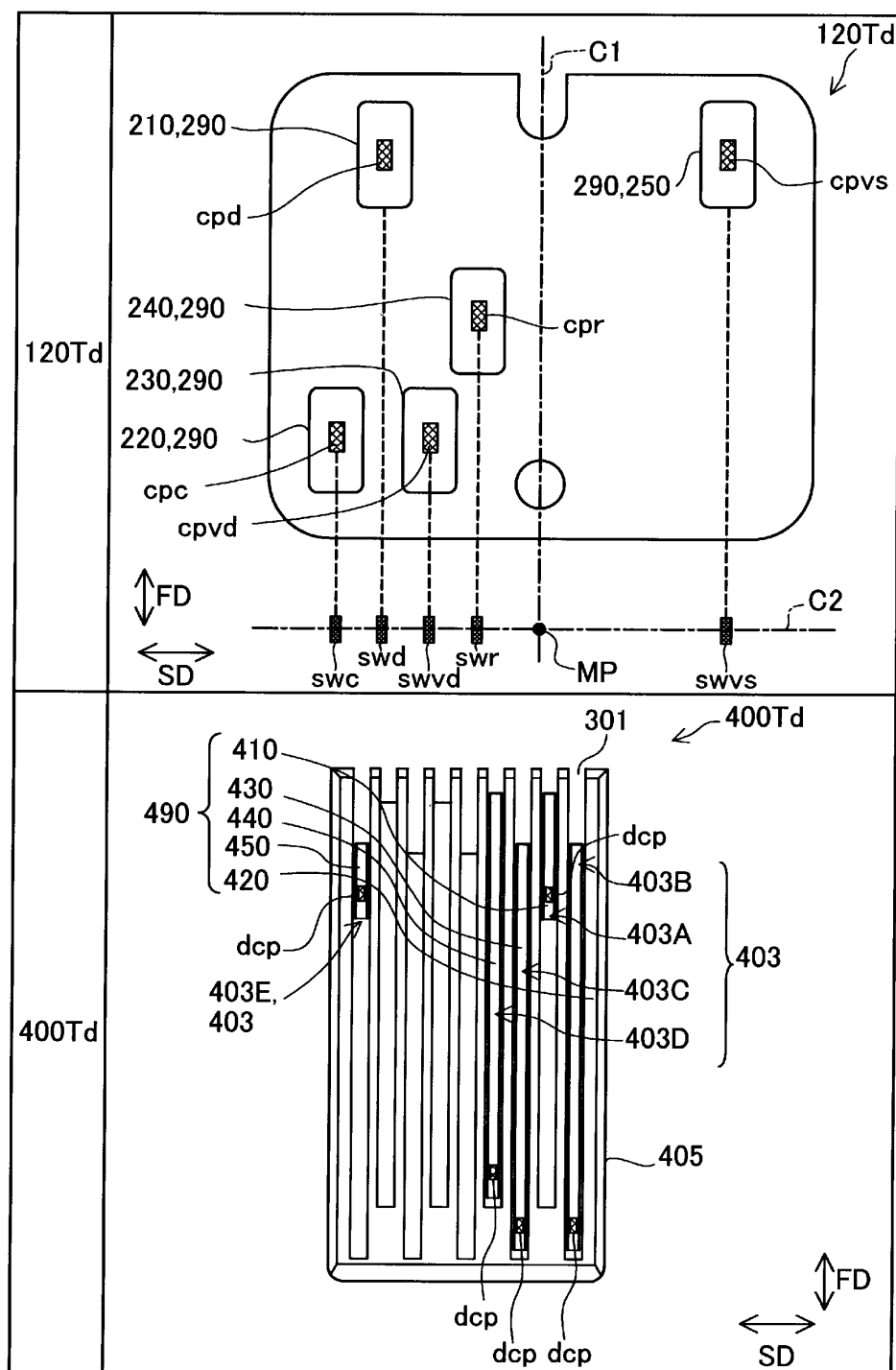
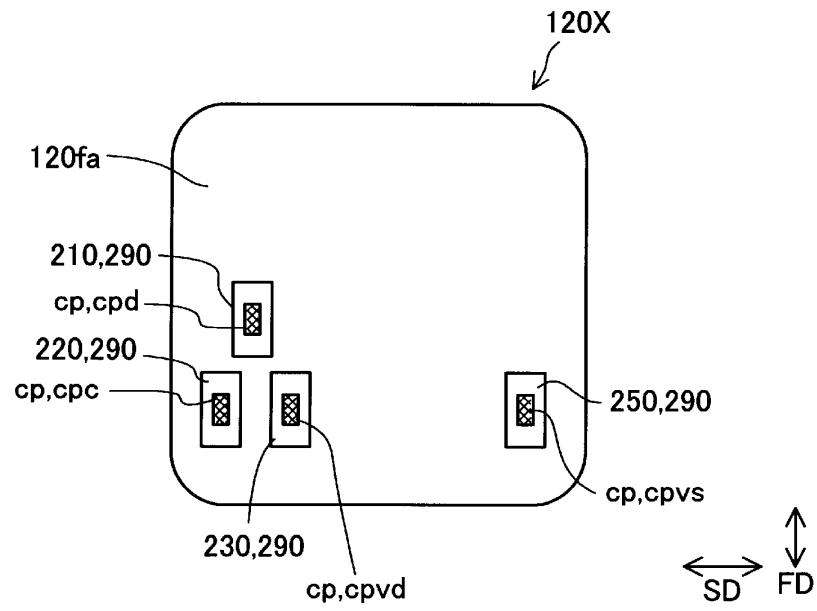


Fig.26

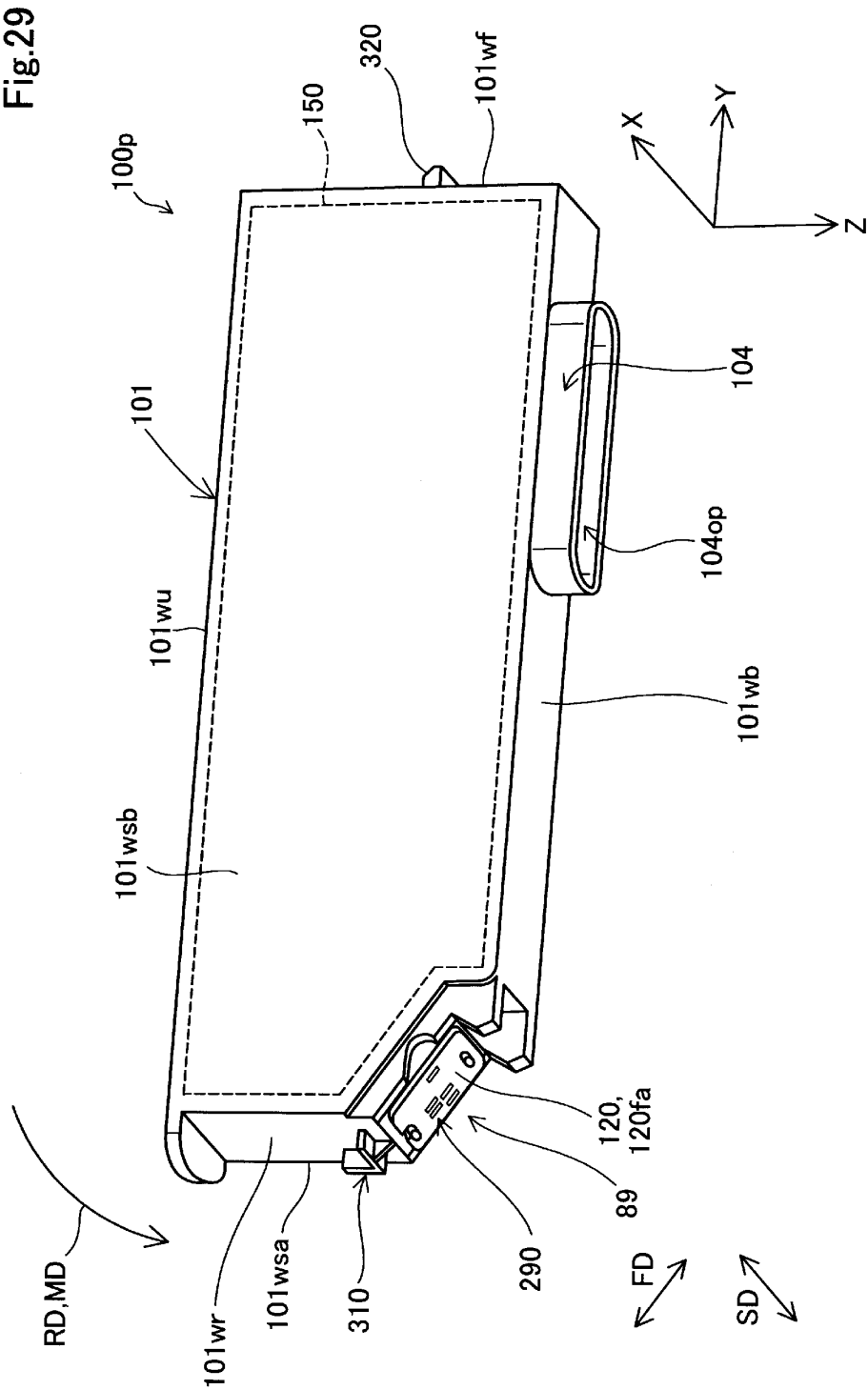


[図28]

Fig.28

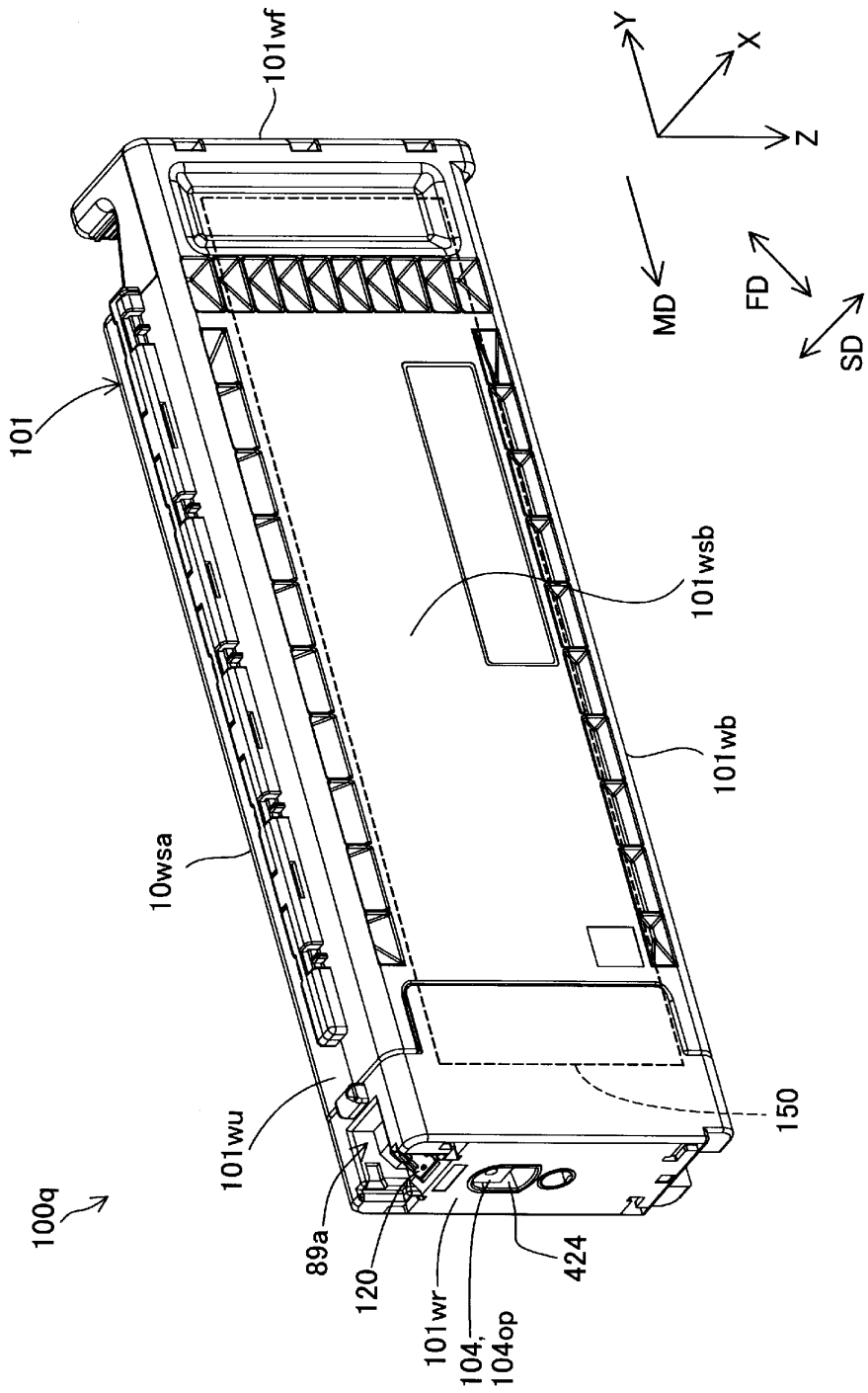


[図29]



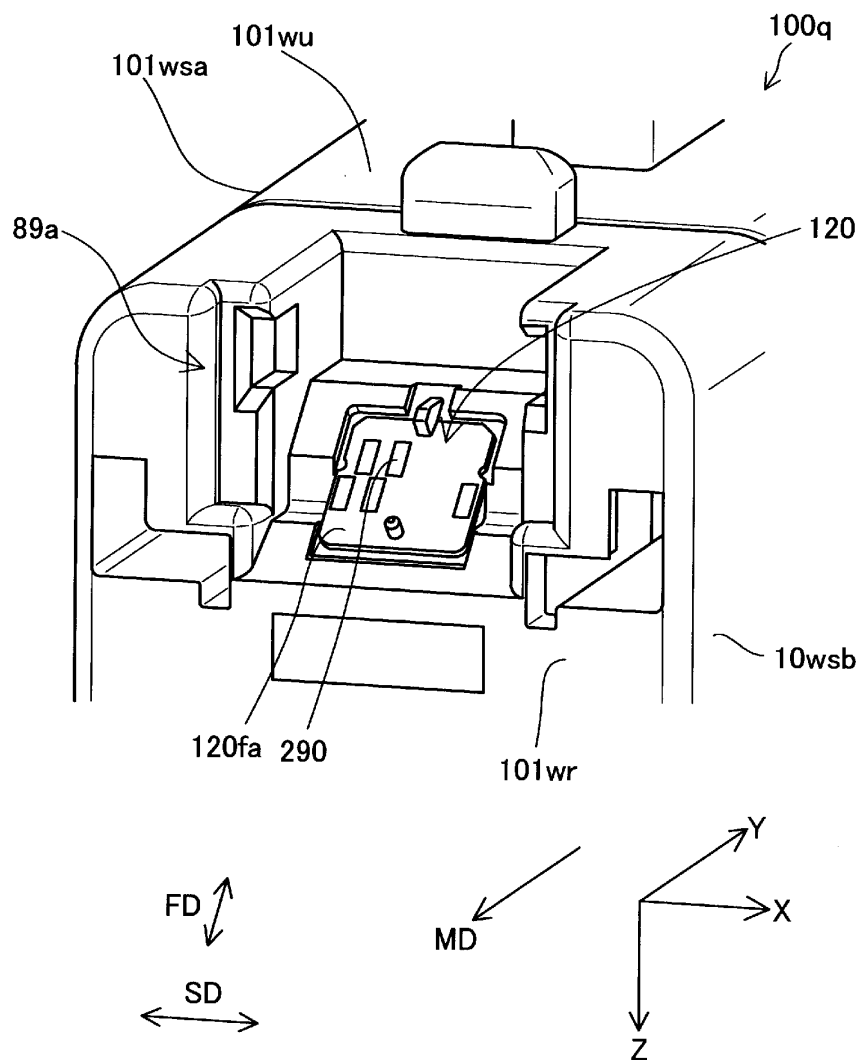
[図30]

Fig.30



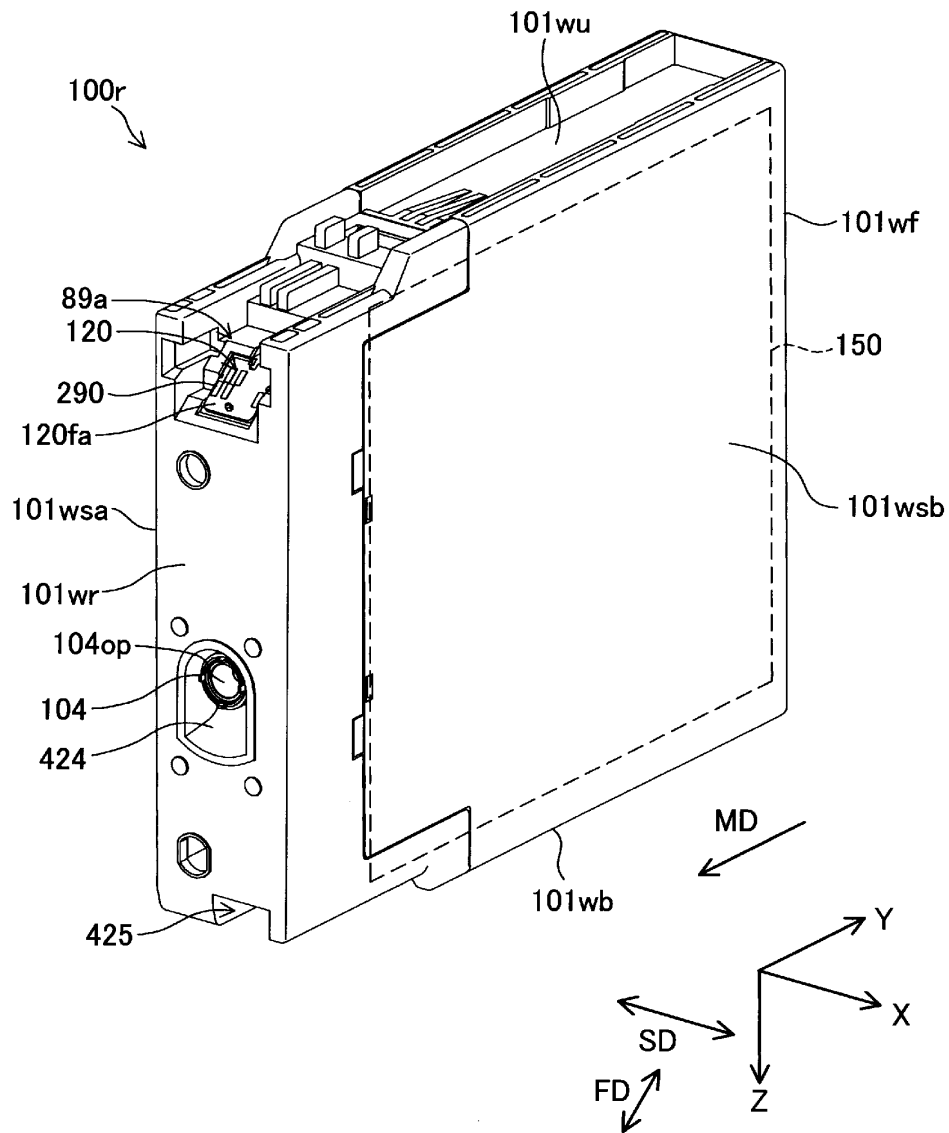
[図31]

Fig.31



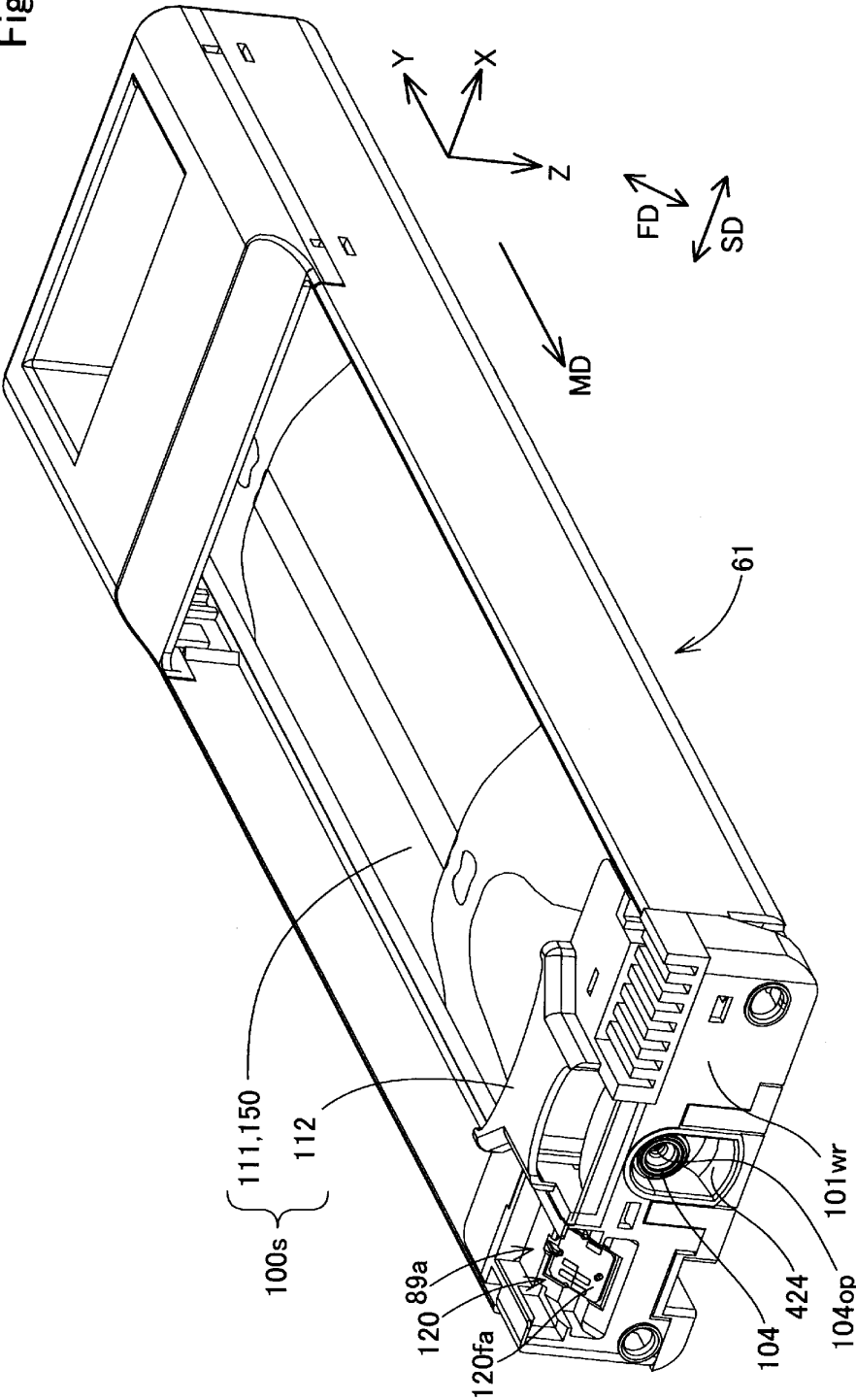
[図32]

Fig.32



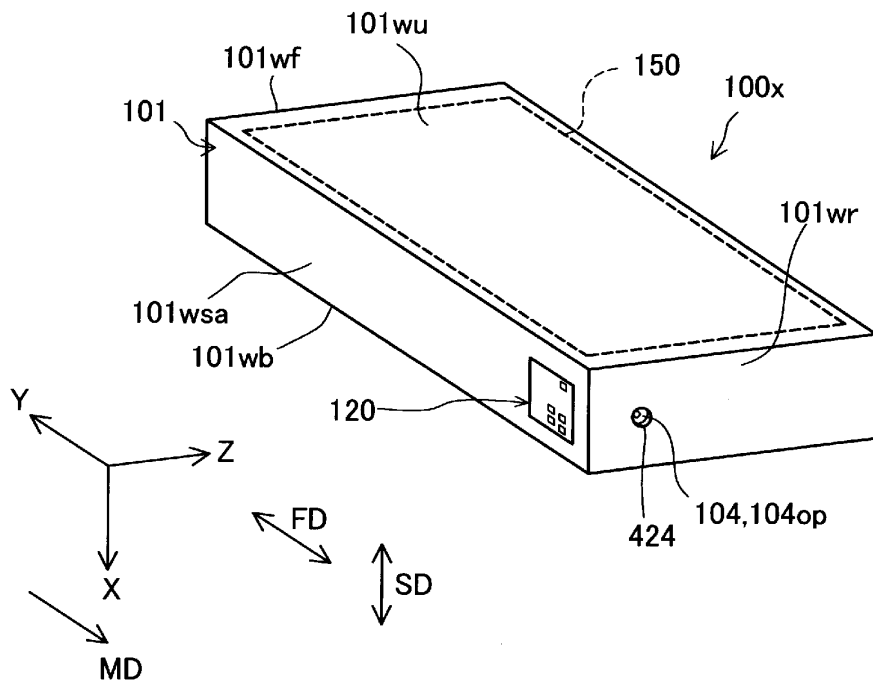
[図33]

Fig.33



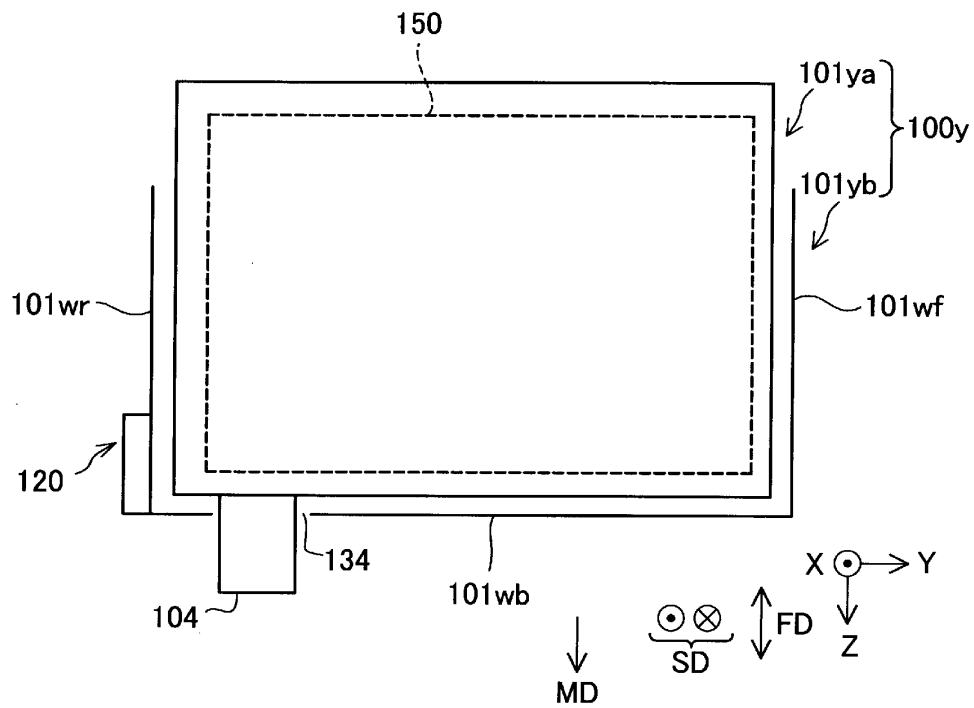
[図35]

Fig.35



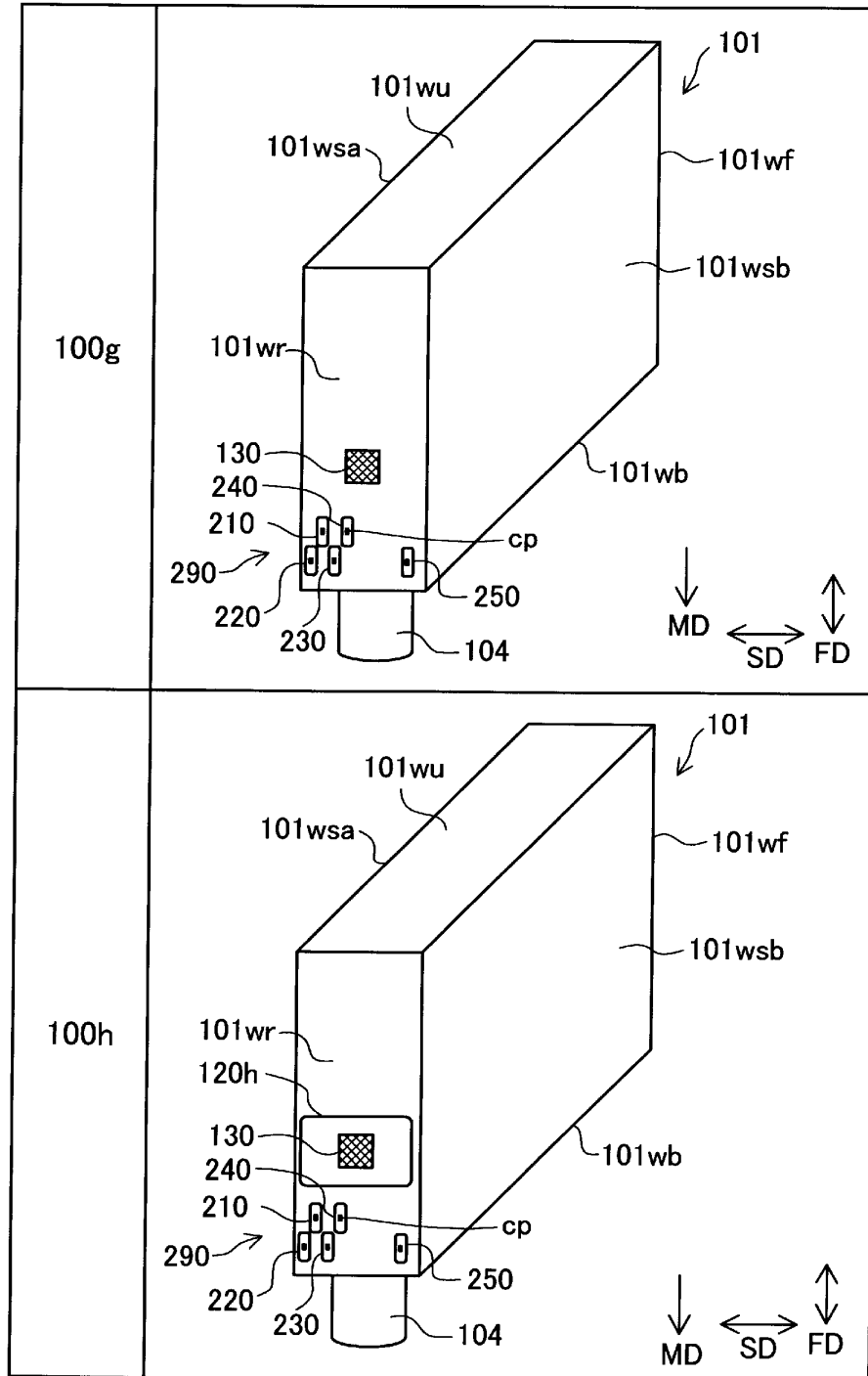
[図36]

Fig.36



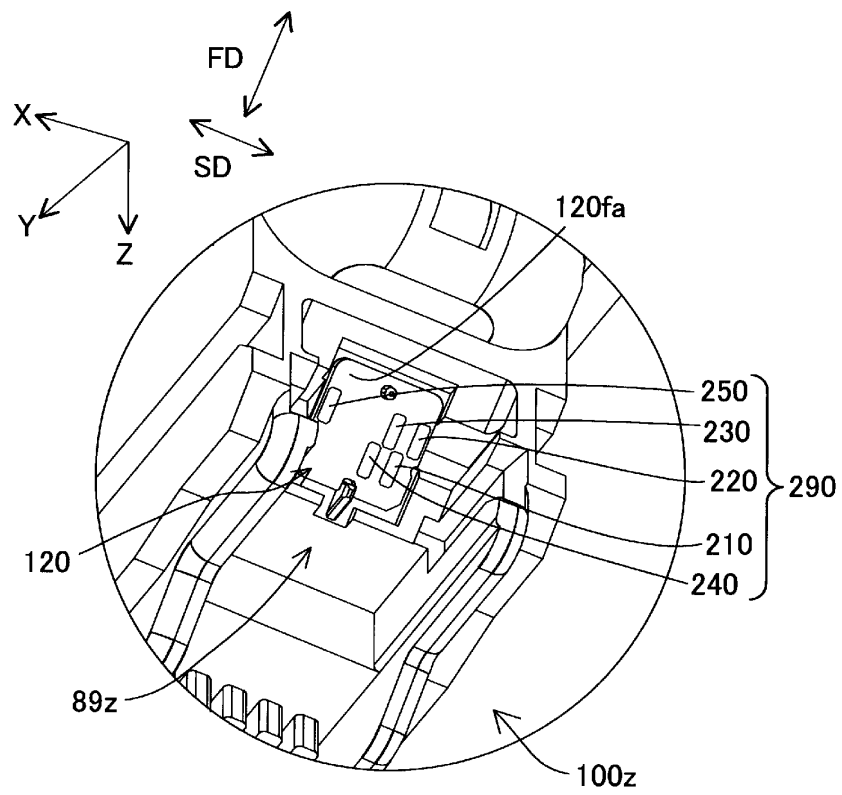
[図37]

Fig.37



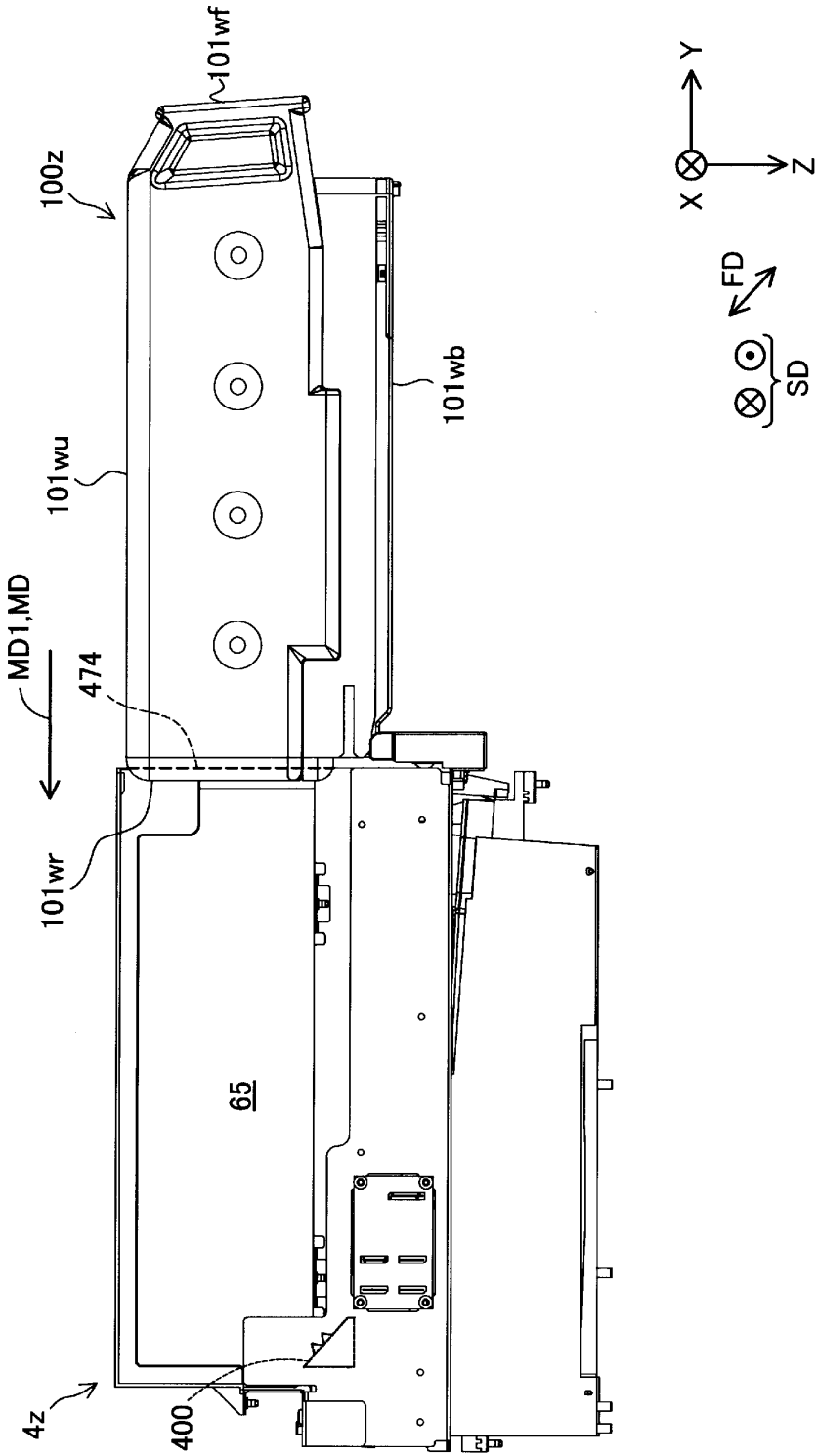
[図39]

Fig.39

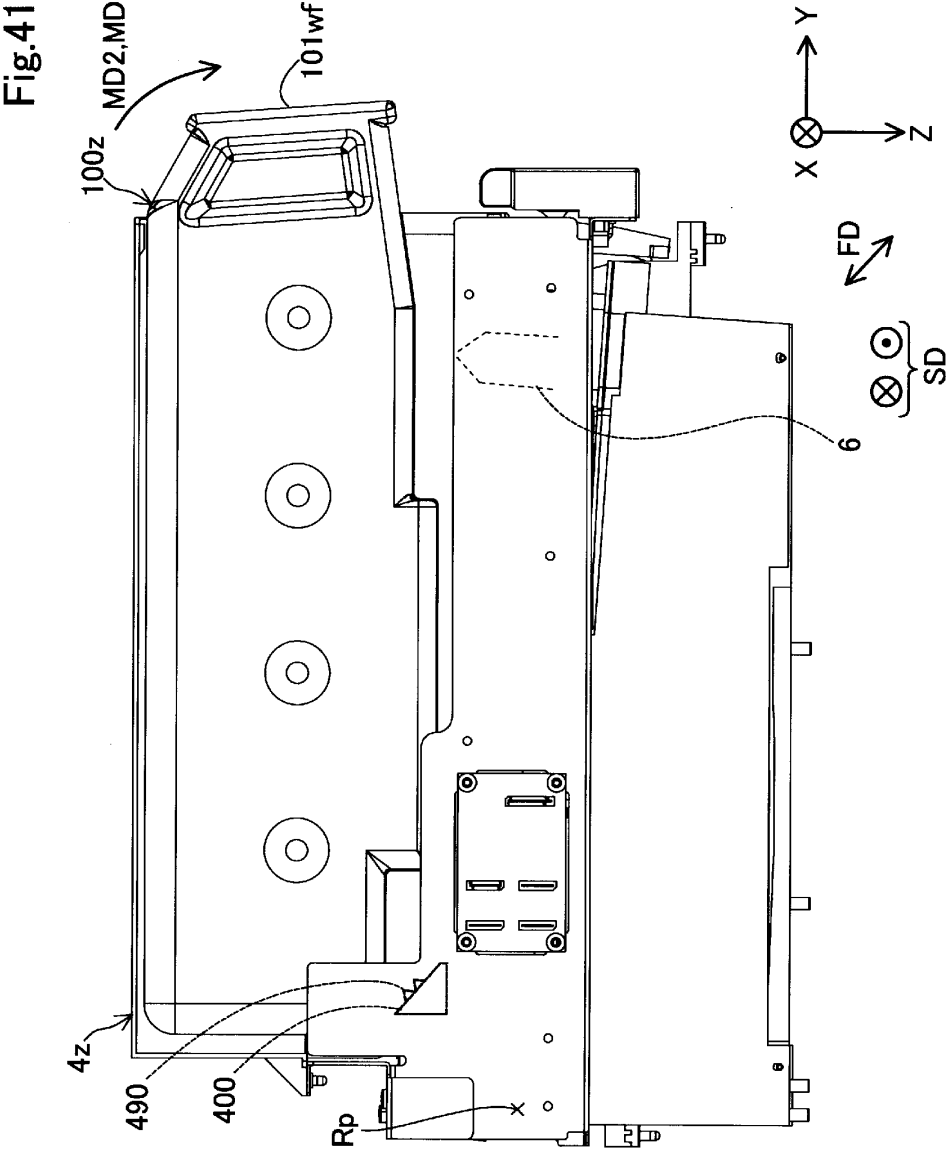


[図40]

Fig.40

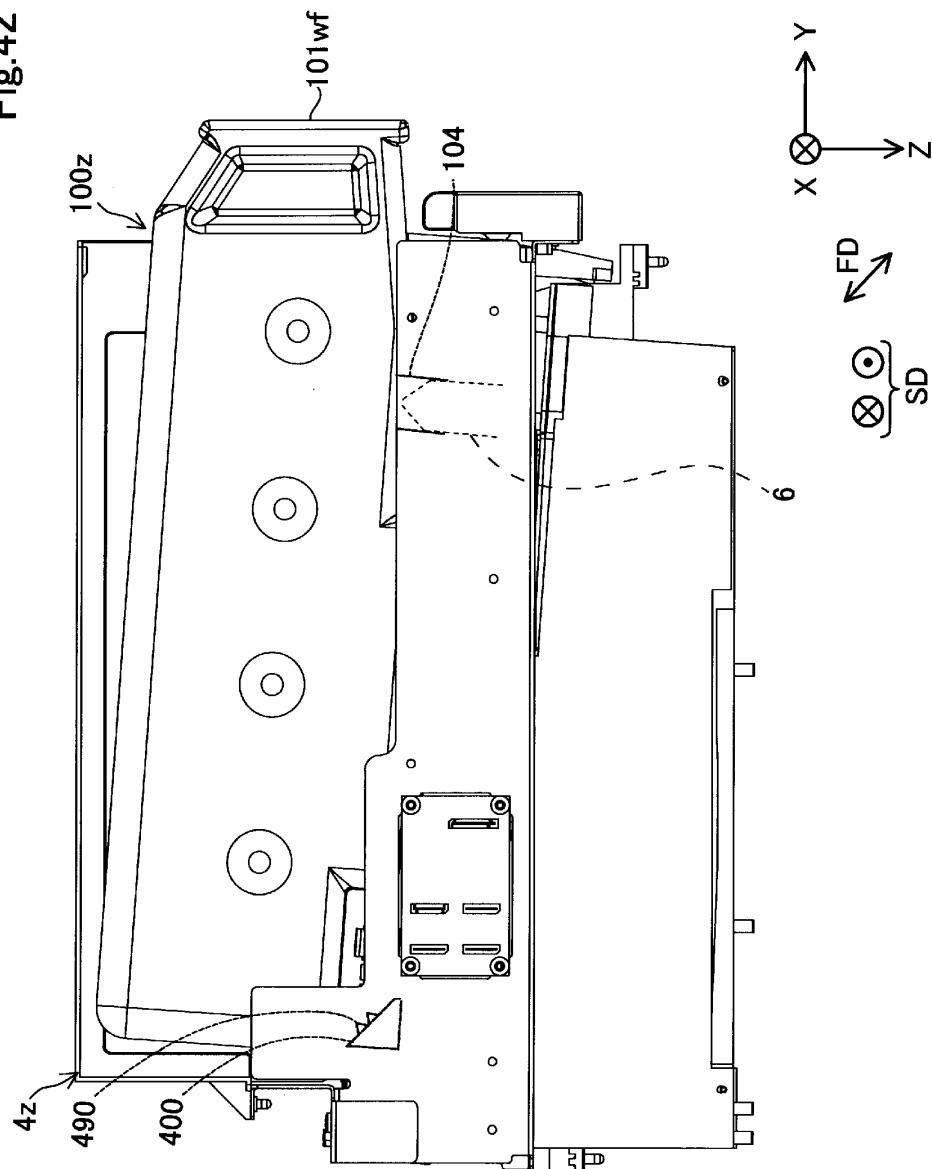


[図41]



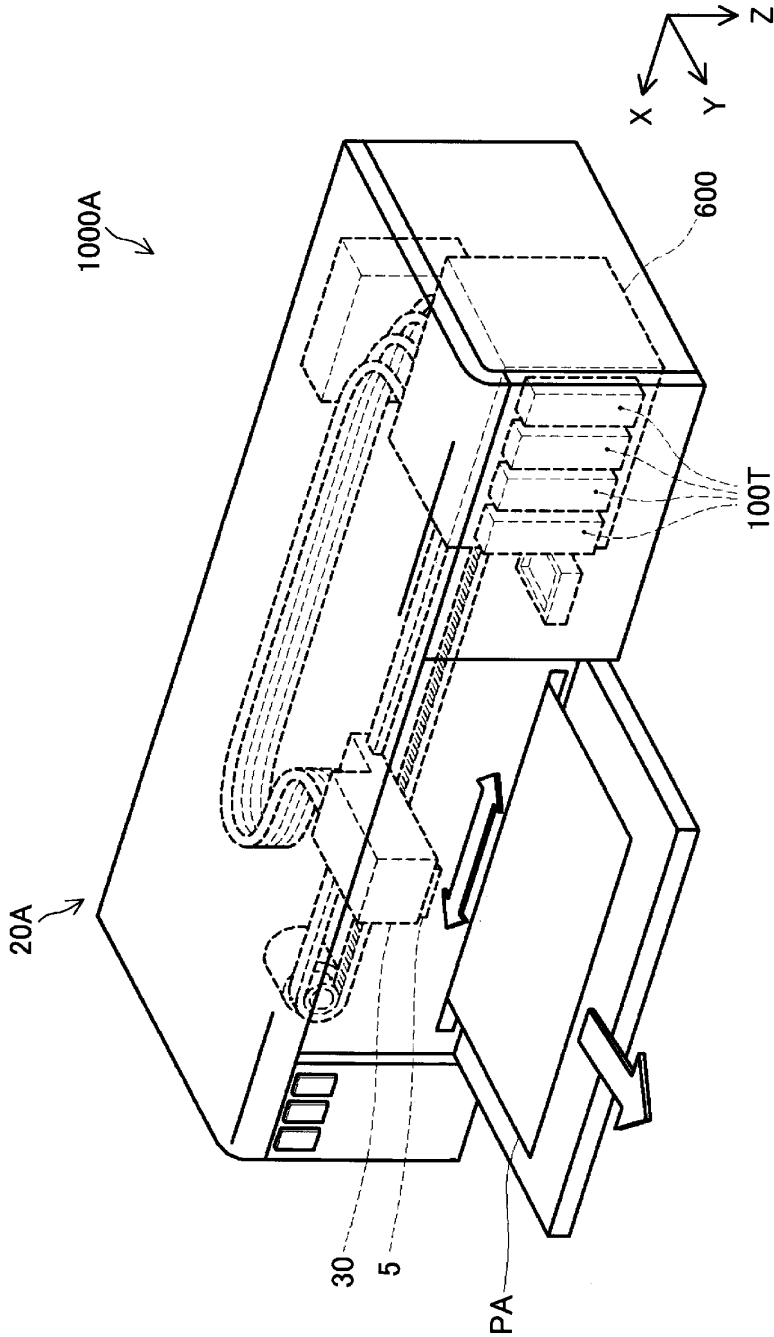
[図42]

Fig.42



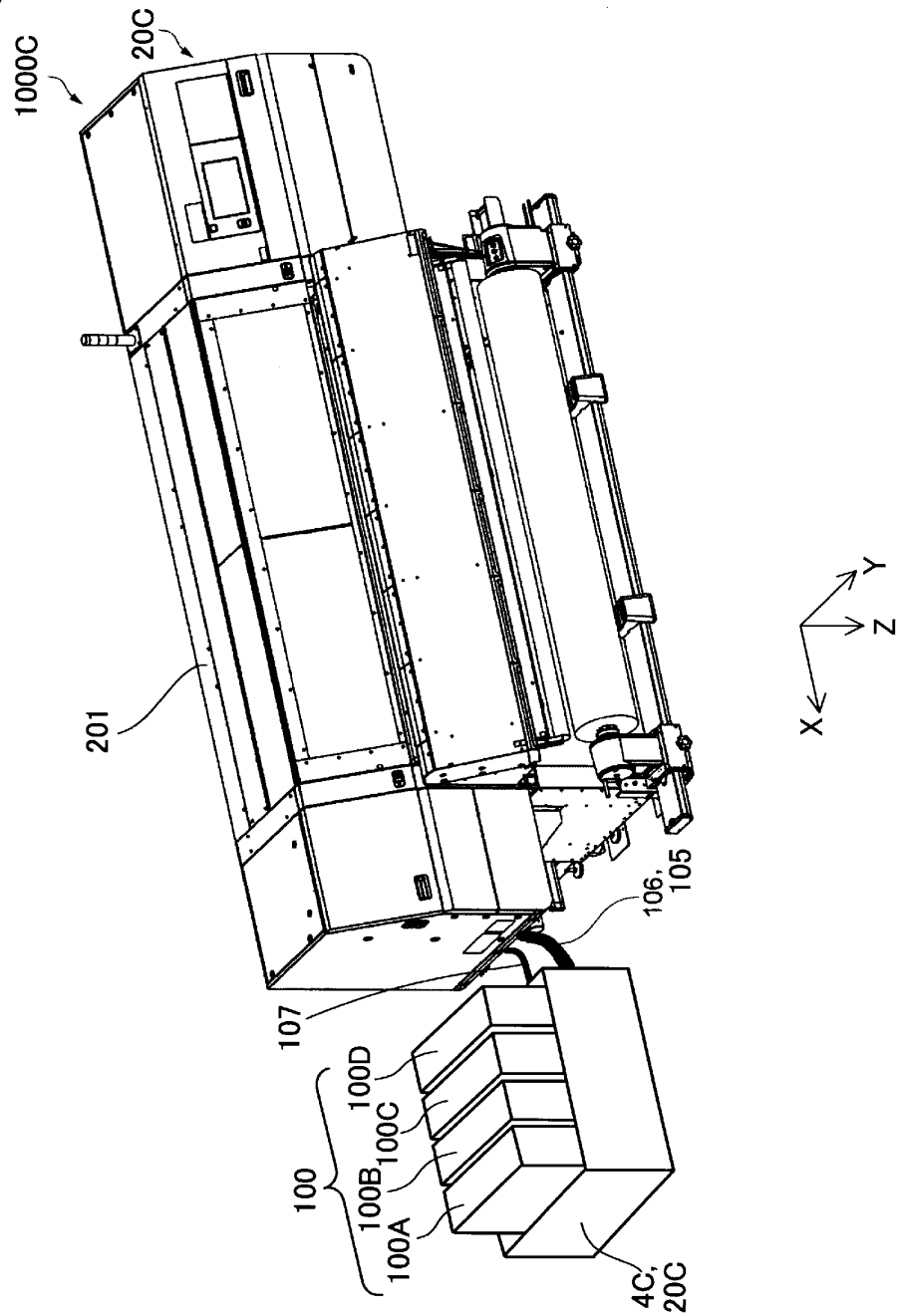
[図43]

Fig.43



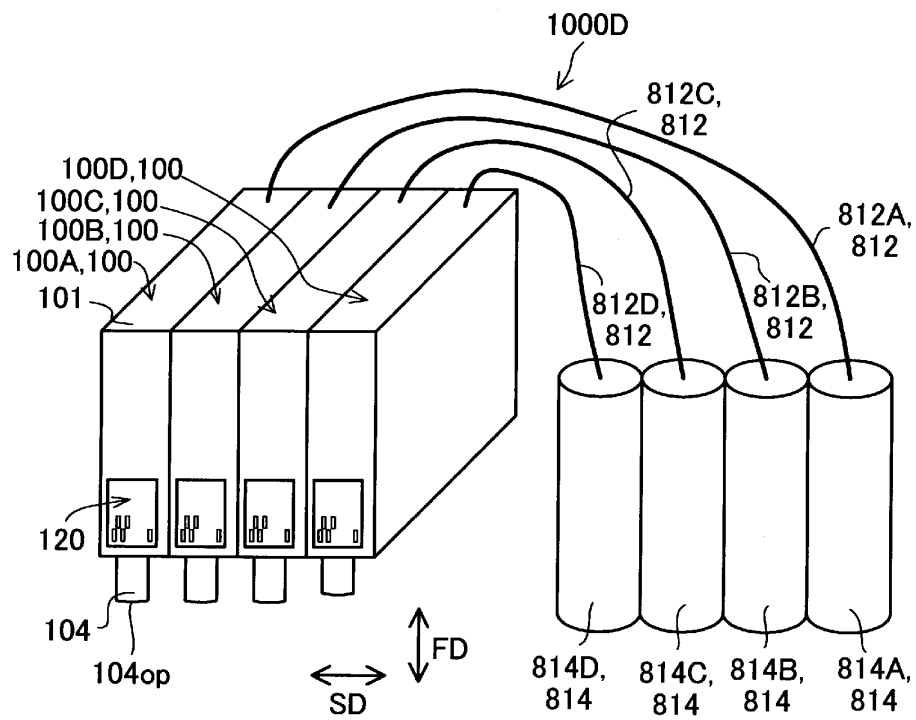
[図44]

Fig.44



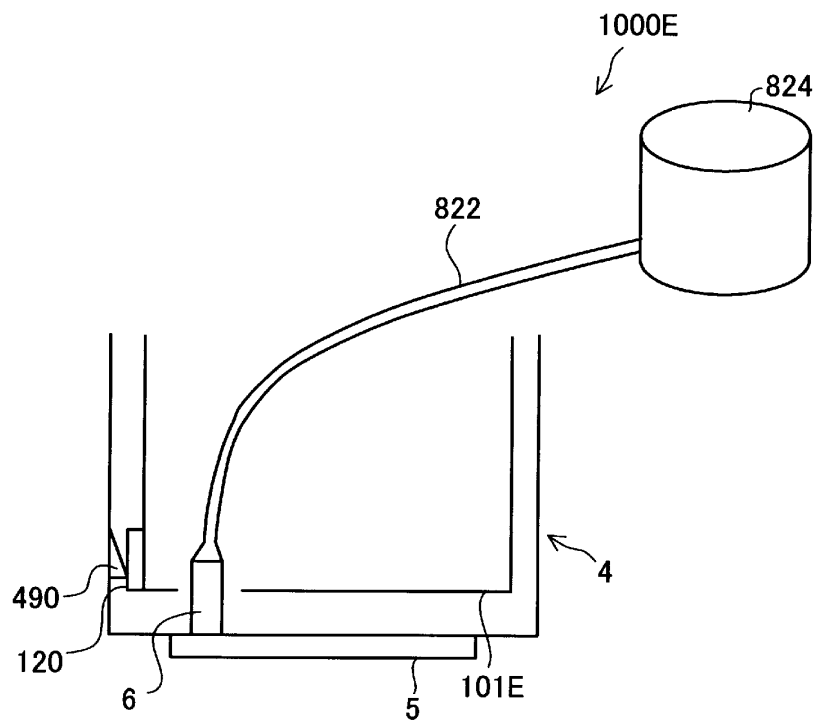
[図45]

Fig.45

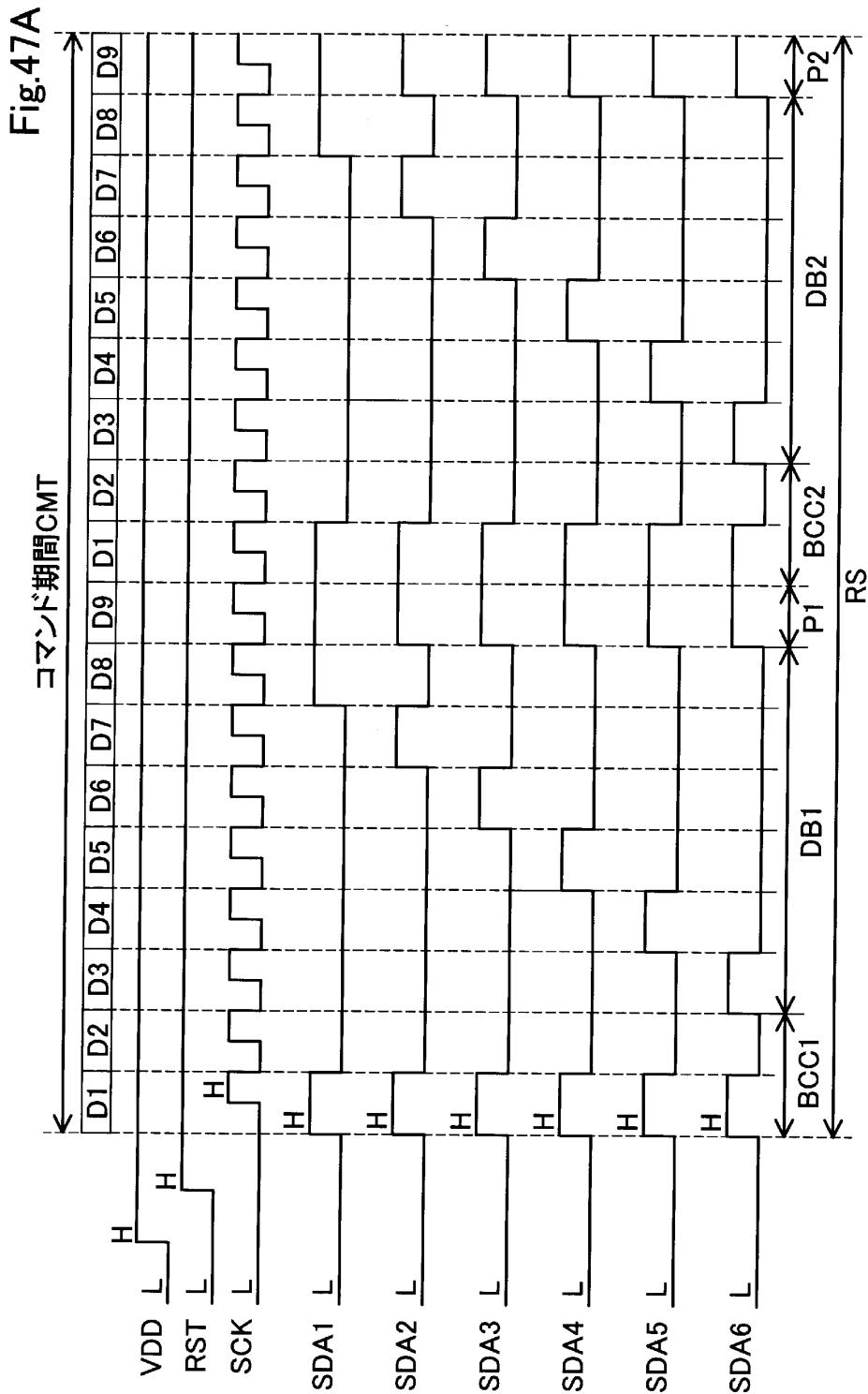


[図46]

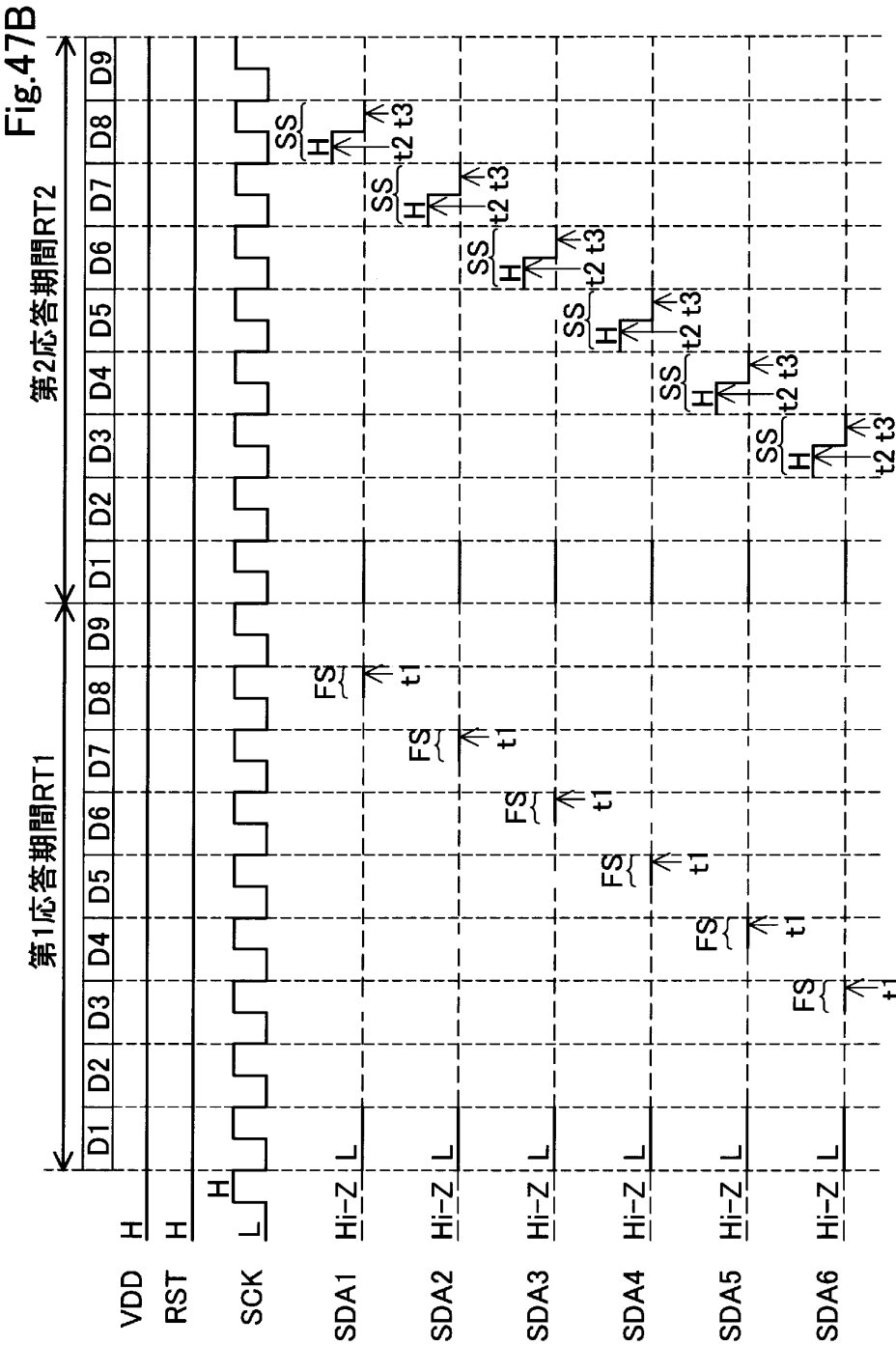
Fig.46



[図47A]

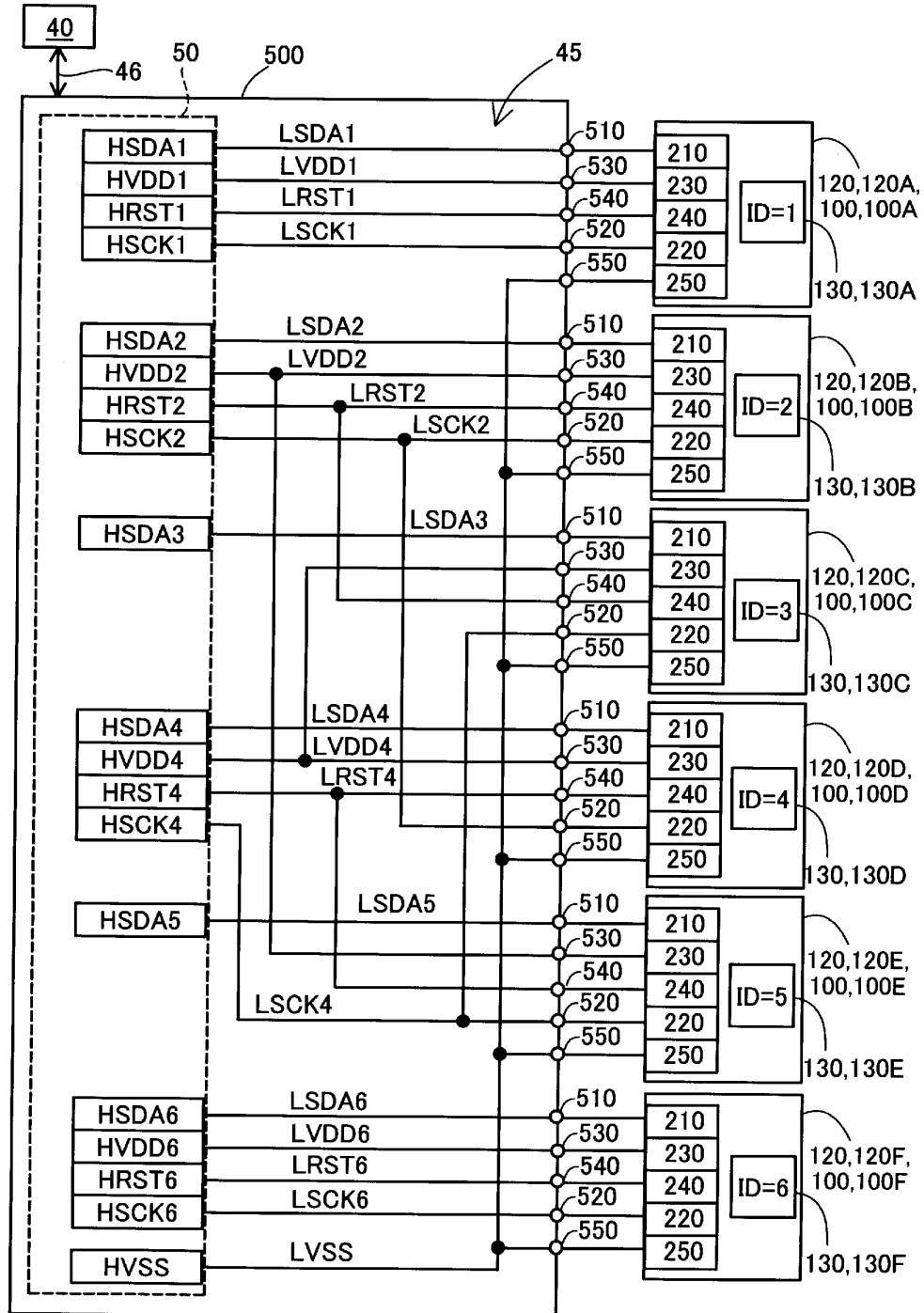


[図47B]



[図48]

Fig.48



[図49]

Fig.49

