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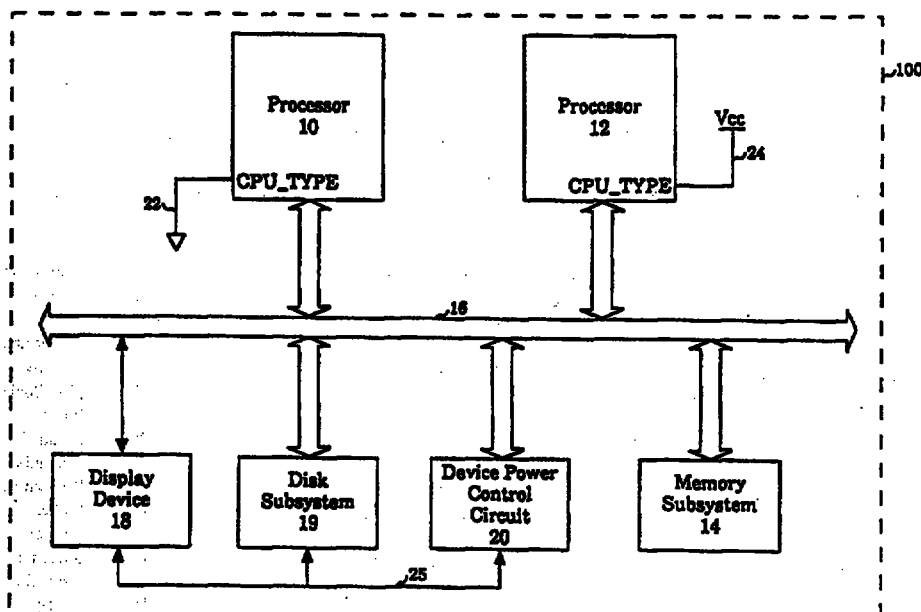
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(54) Title: PROCESSOR THAT INDICATES SYSTEM BUS OWNERSHIP IN AN UPGRADABLE MULTIPROCESSOR COMPUTER SYSTEM



(57) Abstract

A computer system (100) having a processor (10, 12) with a type input pin (22, 24) that indicates whether the processor (10, 12) is coupled as an OEM processor (10) in a single processor or a dual processor computer system. The processor includes an upgrade/OEM output pin coupled to transfer an upgrade/OEM signal over a system bus (16) if the type input pin (22, 24) indicates the OEM processor (10). The upgrade/OEM signal indicates whether the OEM processor (10) or the upgrade processor (12) is a bus owner of the system bus (16).

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