

DELTA SIGMA D/A CONVERTER

ABSTRACT

A thermometer code outputted from a thermometer code converter 2 is divided into P blocks for every Q bits. An arrangement of the blocks is shifted in rotation by a barrel shifter 5 at a frequency f_s . Along with this, an arrangement of bits in the block is shifted in rotation by a shift register 7 at a frequency $(Q \cdot f_s)$, the frequency f_s multiplied by Q. The bits of thus obtained code is given to local DACs (LDAC 1 to LDAC 12) each specified to correspond to each of the given bits. This allows to keep an operating frequency lower and to reduce distortion in the output which is due to change in shifting the data in rotation in the local DACs, which change occurs depending on bit levels of the data in a form of the thermometer code.

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