

ABSTRACT

A semiconductor chip (15) is bonded on a die pad (13) of a leadframe, and inner leads (12) are electrically connected to electrode pads of the semiconductor chip (15) with metal fine wires (16). The die pad (13), semiconductor chip (15) and inner leads are molded with a resin encapsulant (17). However, no resin encapsulant (17) exists on the respective back surfaces of the inner leads (12), which protrude downward from the back surface of the resin encapsulant (17) so as to be external electrodes (18). That is to say, since the external electrodes (18) protrude, a standoff height can be secured in advance for the external electrodes (18) in bonding the external electrodes (18) to electrodes of a motherboard. Thus, the external electrodes (18) may be used as external terminals as they are, and no ball electrodes of solder or the like need to be provided for the external electrodes (18). Accordingly, this process is advantageous in terms of the number of manufacturing process steps and the manufacturing costs.