

Abstract:

Arrangement for reproducing n digital signals from n adjacent tracks on a record carrier.

For the clock extraction from n digital signals read from each of n tracks ($T_{a,1}$ to $T_{a,n}$) there are provided n phase-locked loops (13.1 to 13. n). In order to compensate for speed variations in the speed of transport of the record carrier (20) there is provided an additional frequency control system (22, 19.1 to 19. n). This system
5 derives a control signal which is a measure of the speed variations, which control signal is applied to the voltage-controlled oscillators (21.1 to 21. n) of all the phase-locked loops as a common control signal.

(Fig. 1)

