

FIG. 1

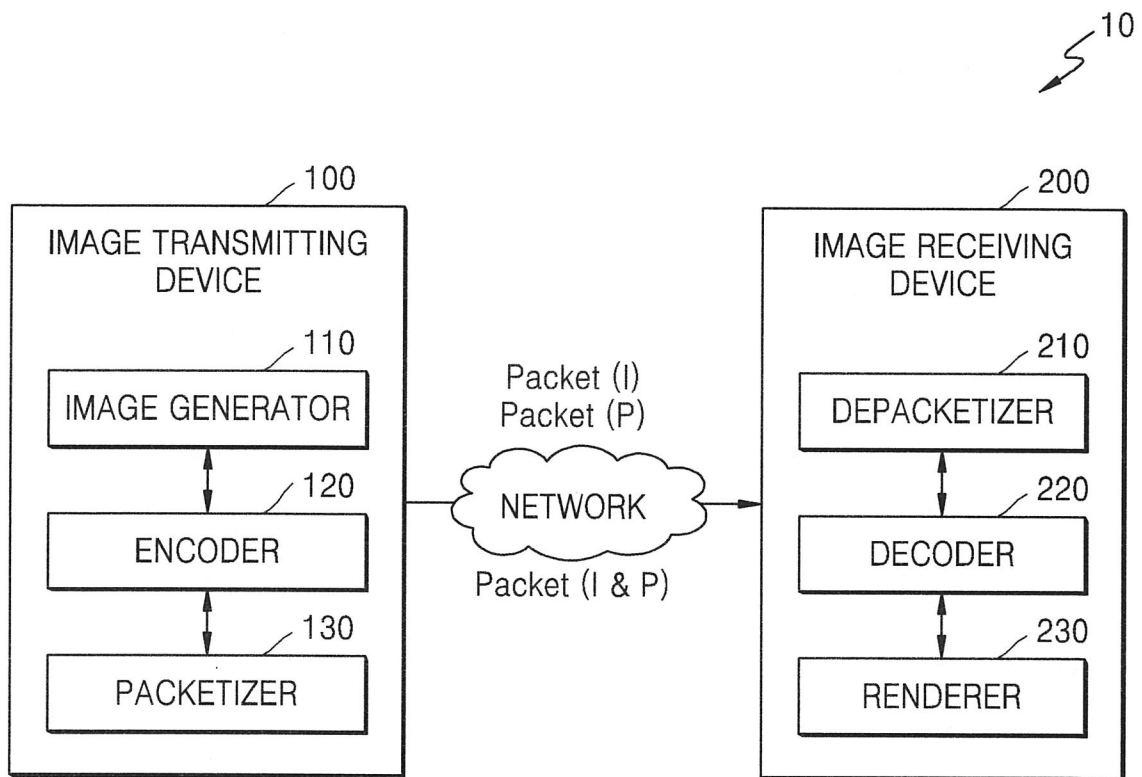


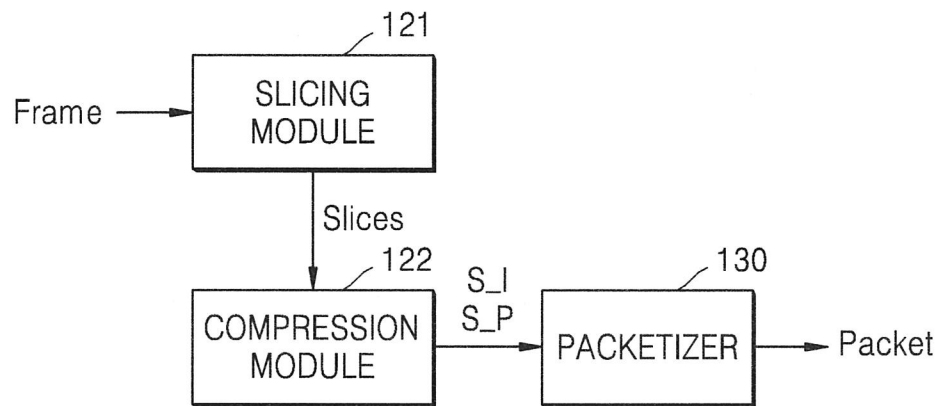
FIG. 2

FIG. 3

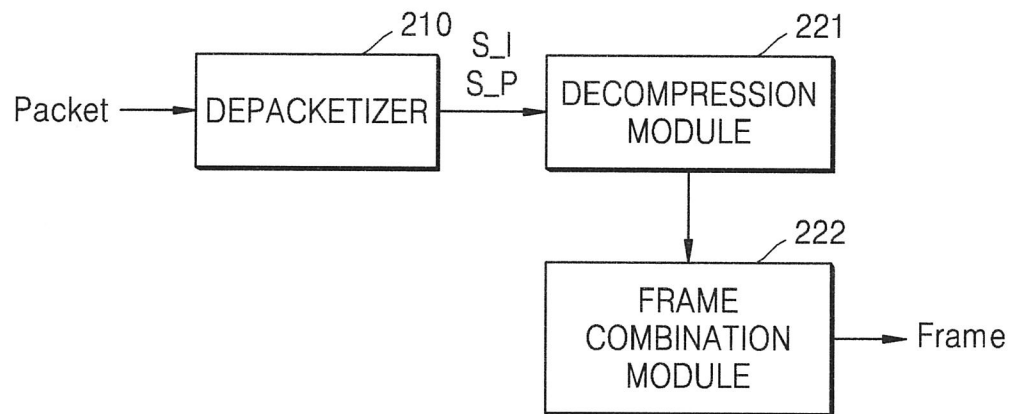


FIG. 4A

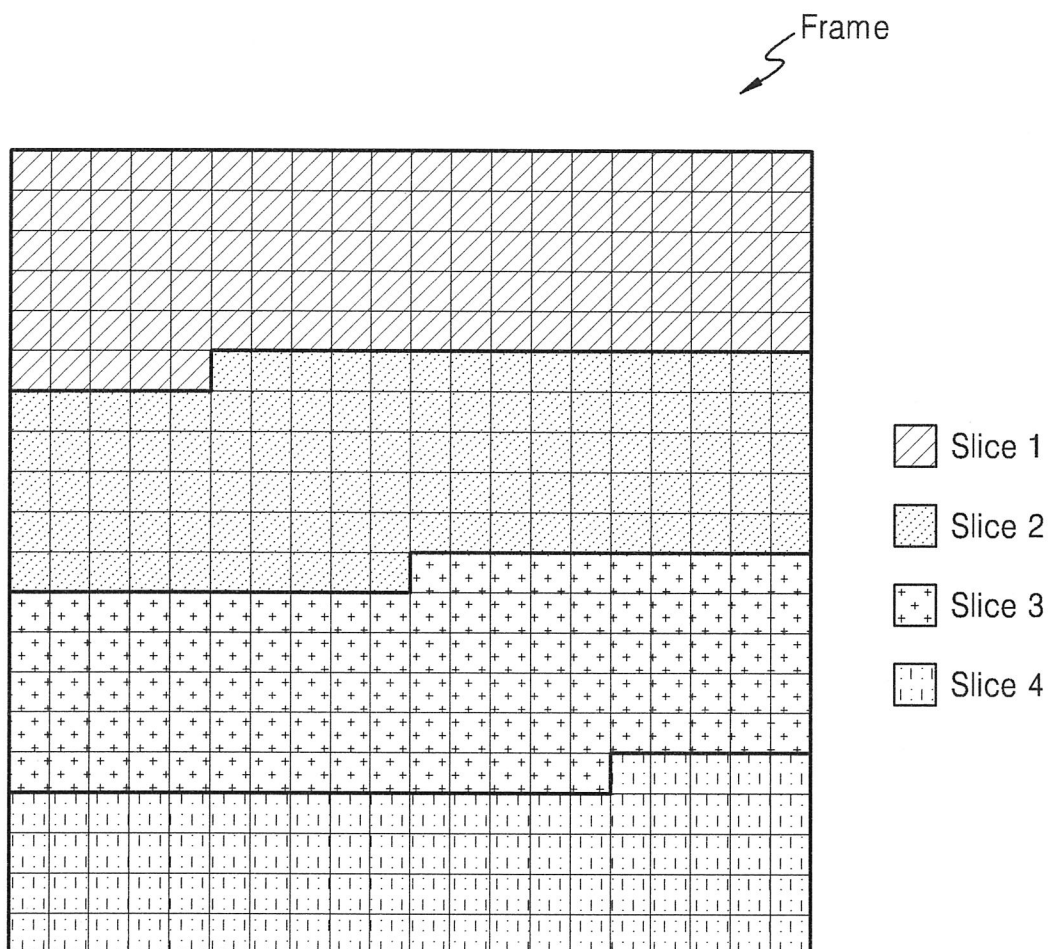


FIG. 4B

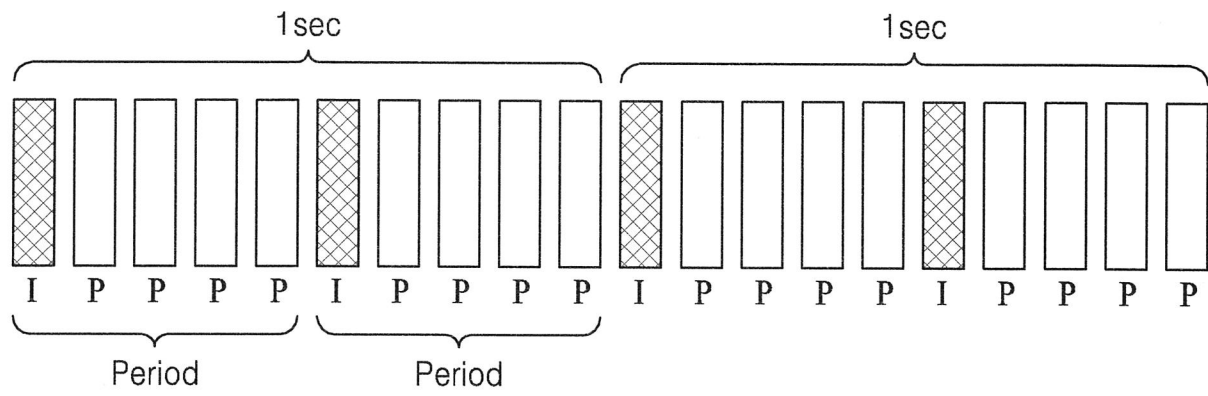


FIG. 5

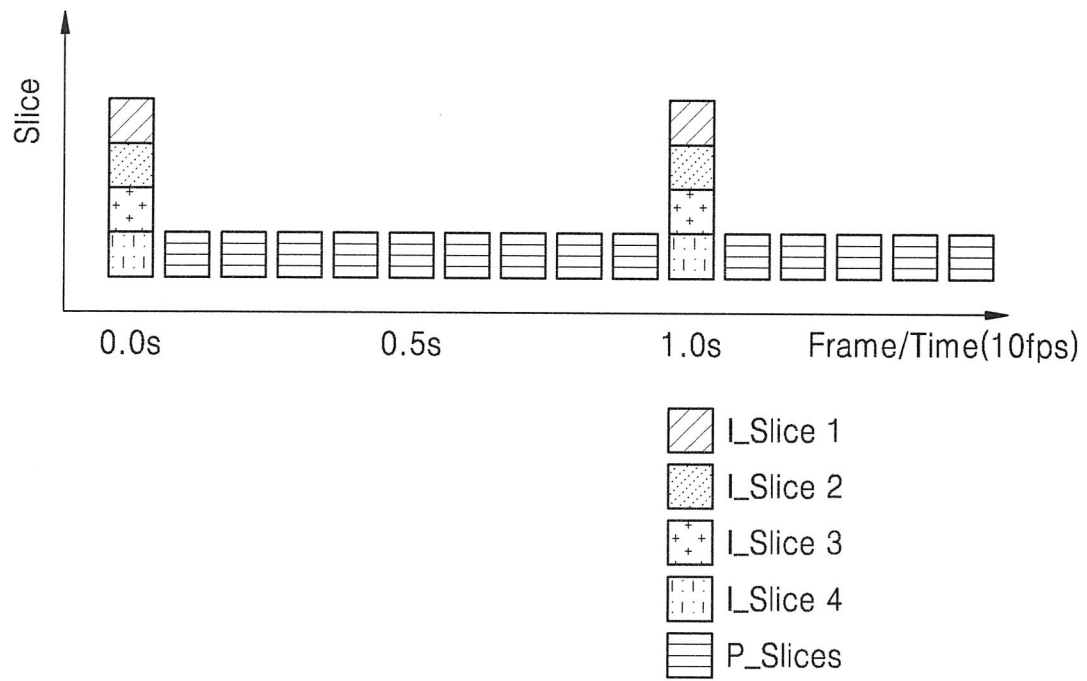


FIG. 6A

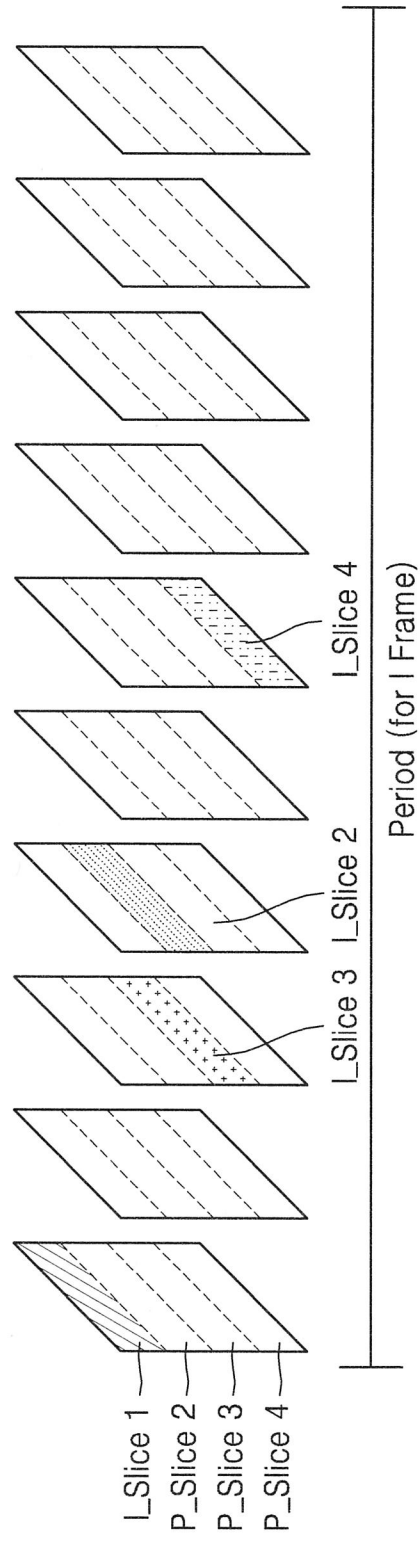


FIG. 6B

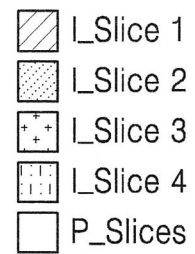
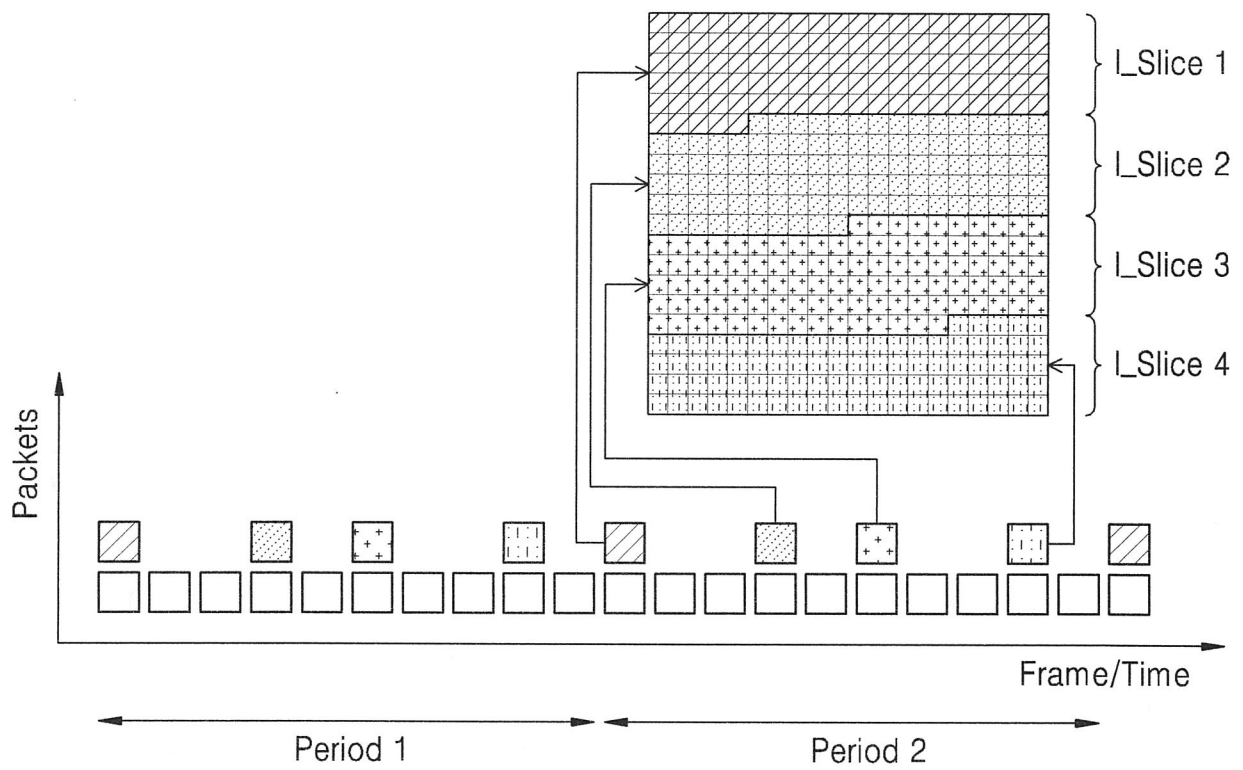


FIG. 7A

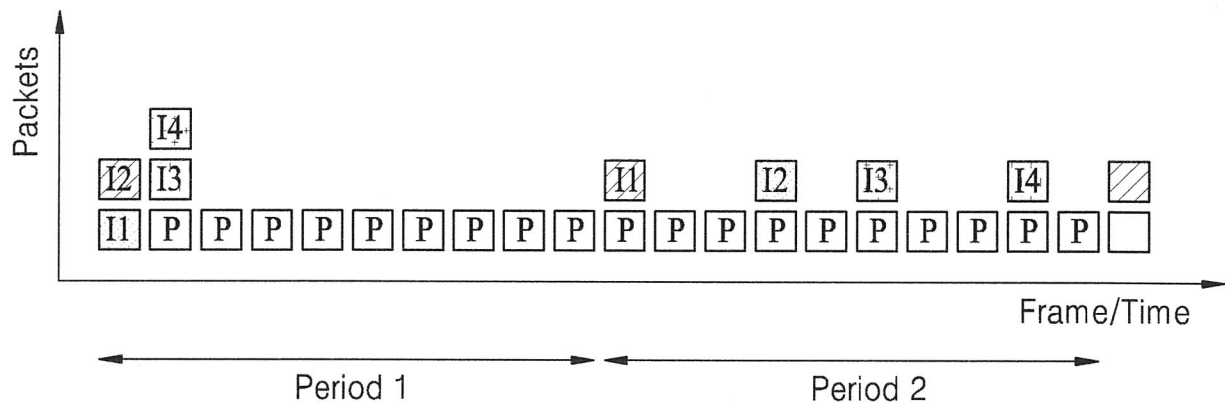


FIG. 7B

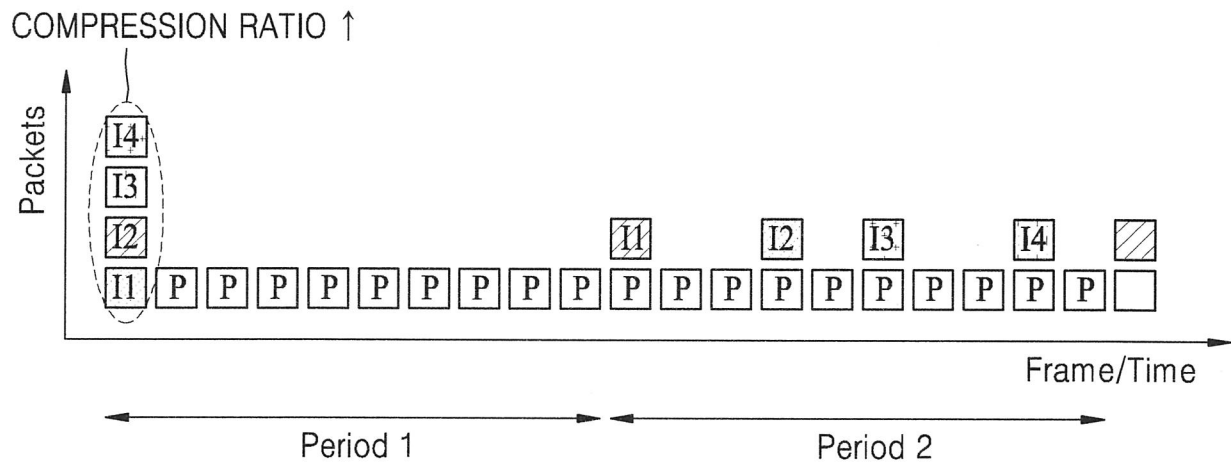


FIG. 8

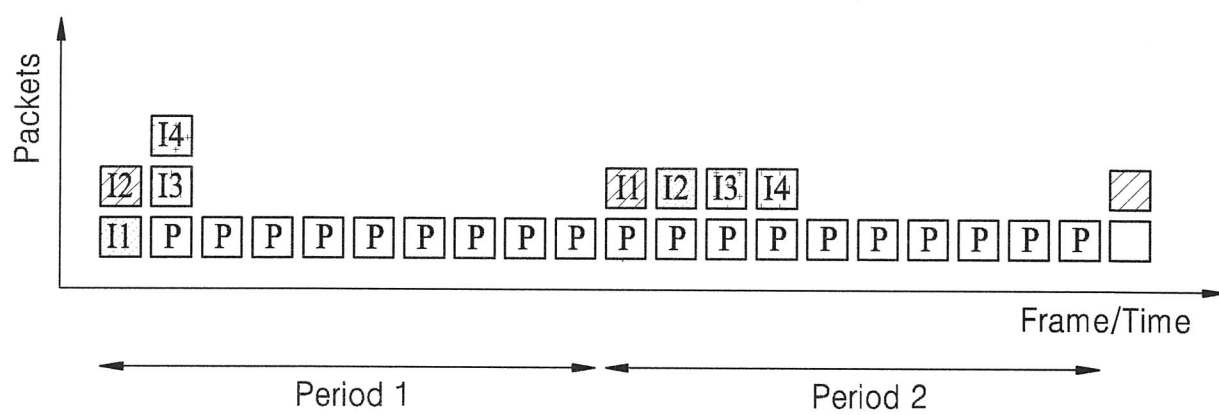


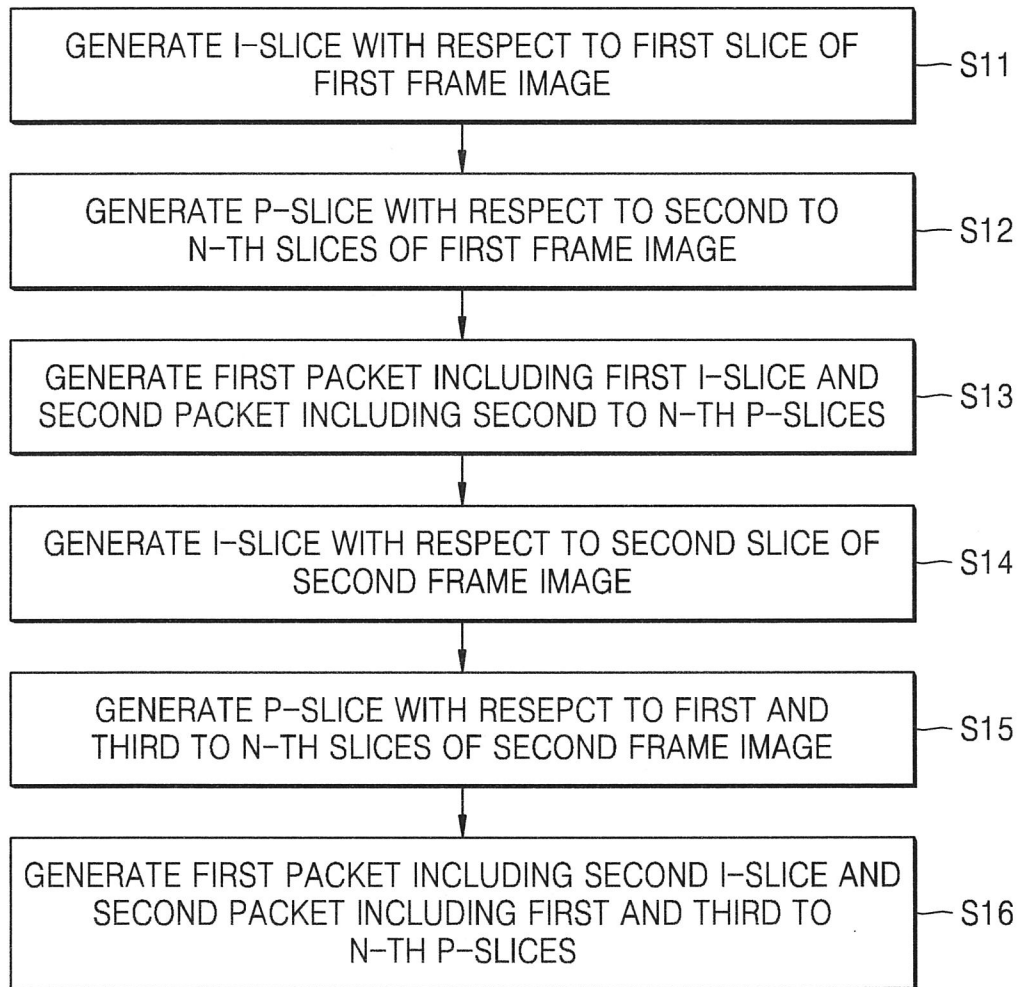
FIG. 9

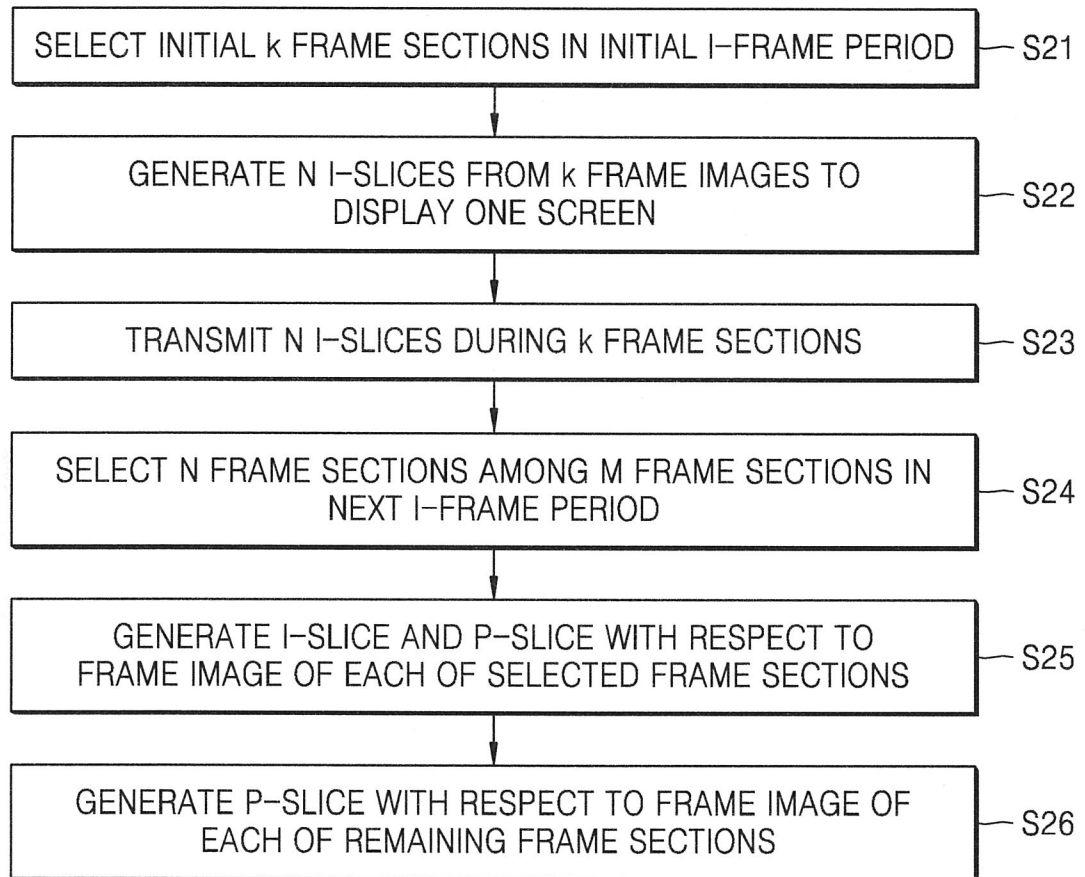
FIG. 10

FIG. 11

H	P2	P3	P4
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H	I1
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FIG. 12

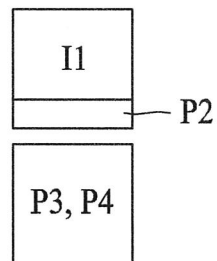
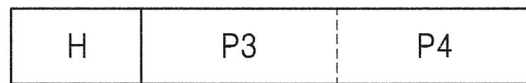
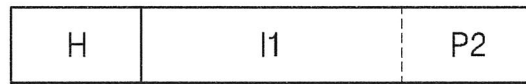


FIG. 13

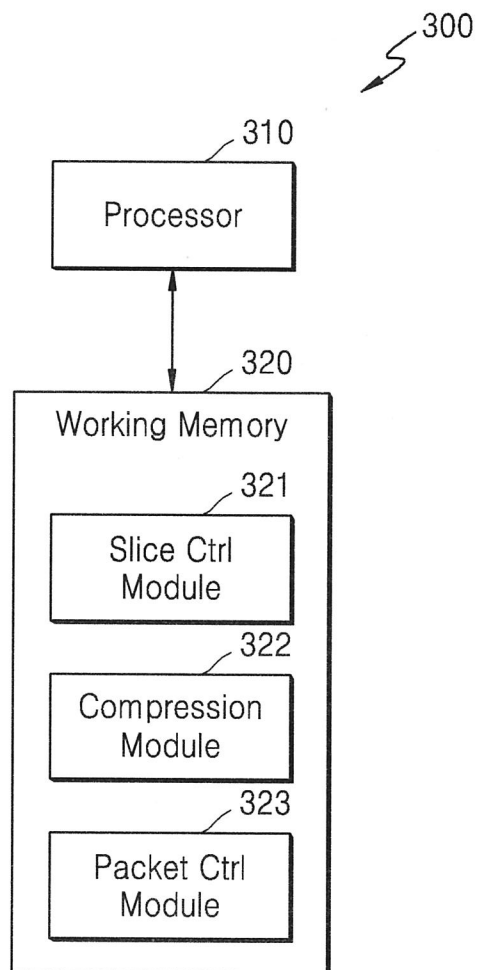


FIG. 14

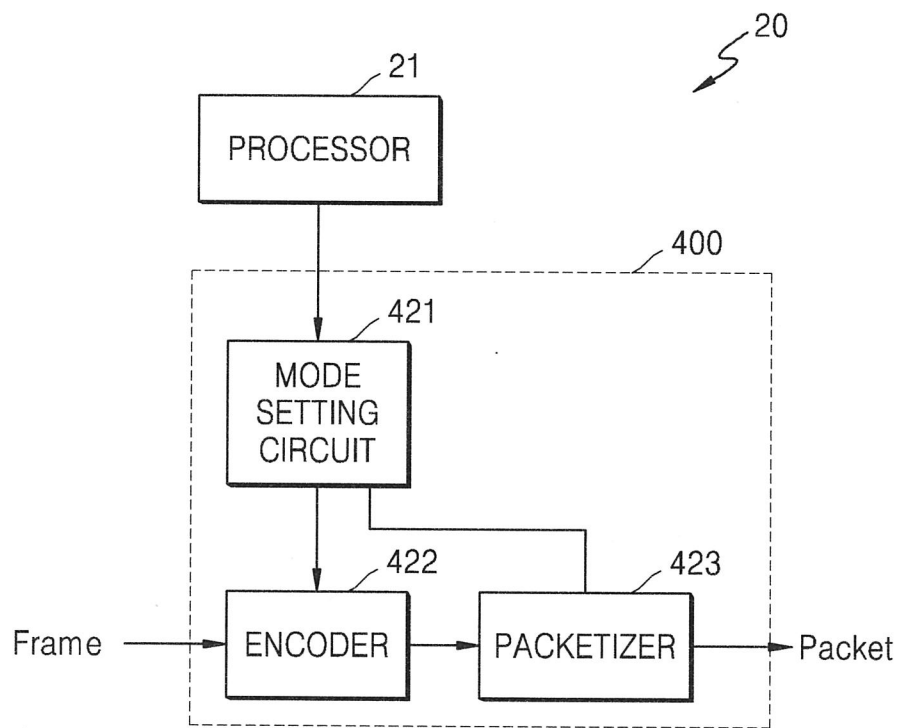


FIG. 15

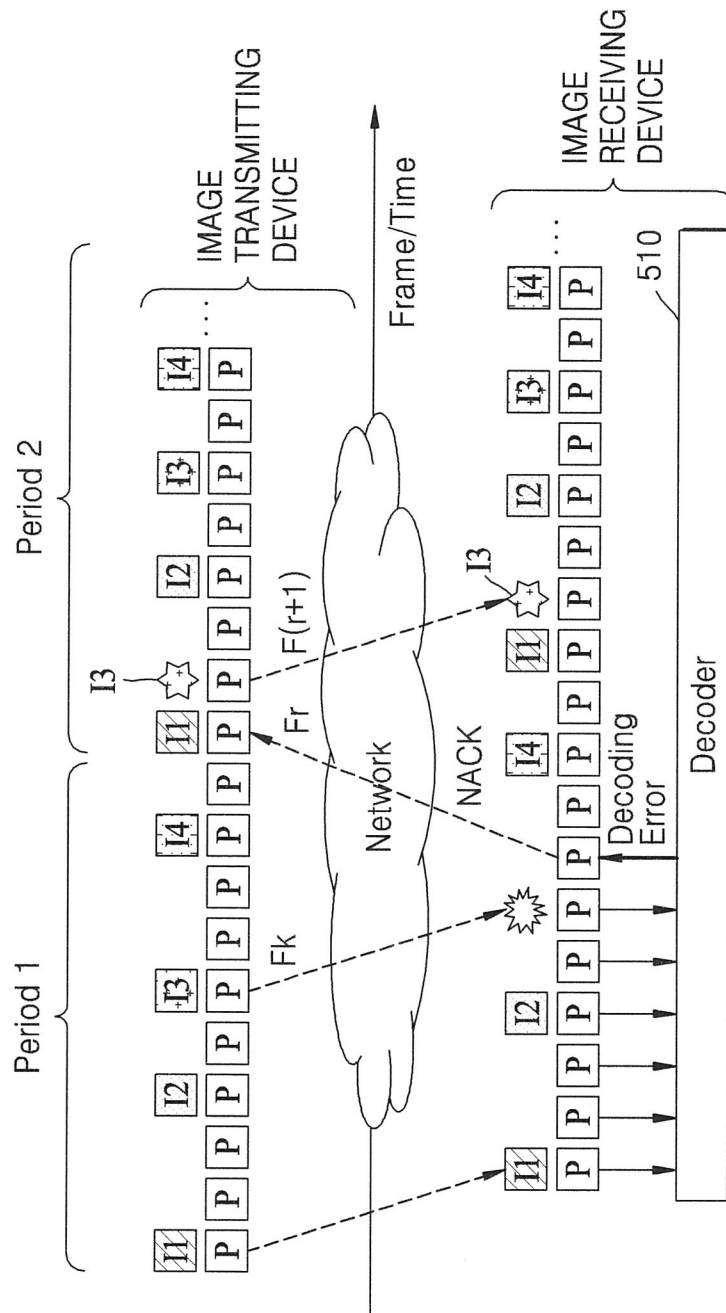


FIG. 16

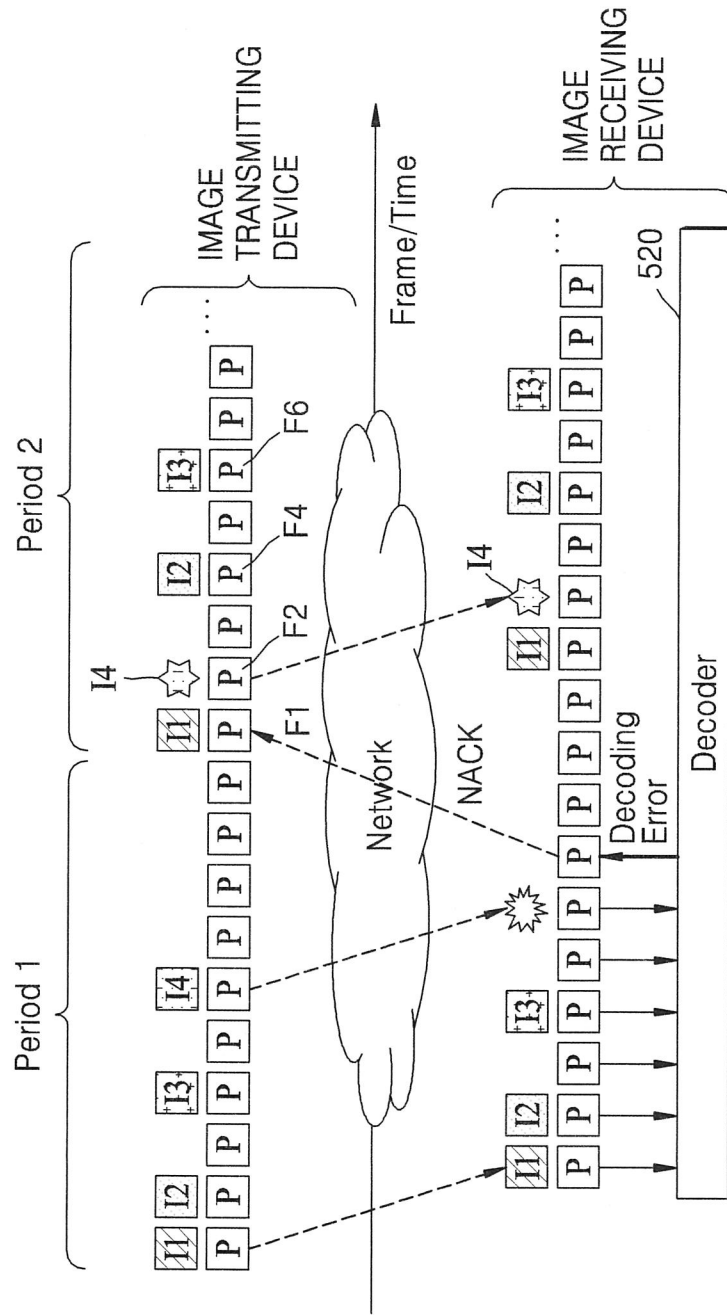


FIG. 17

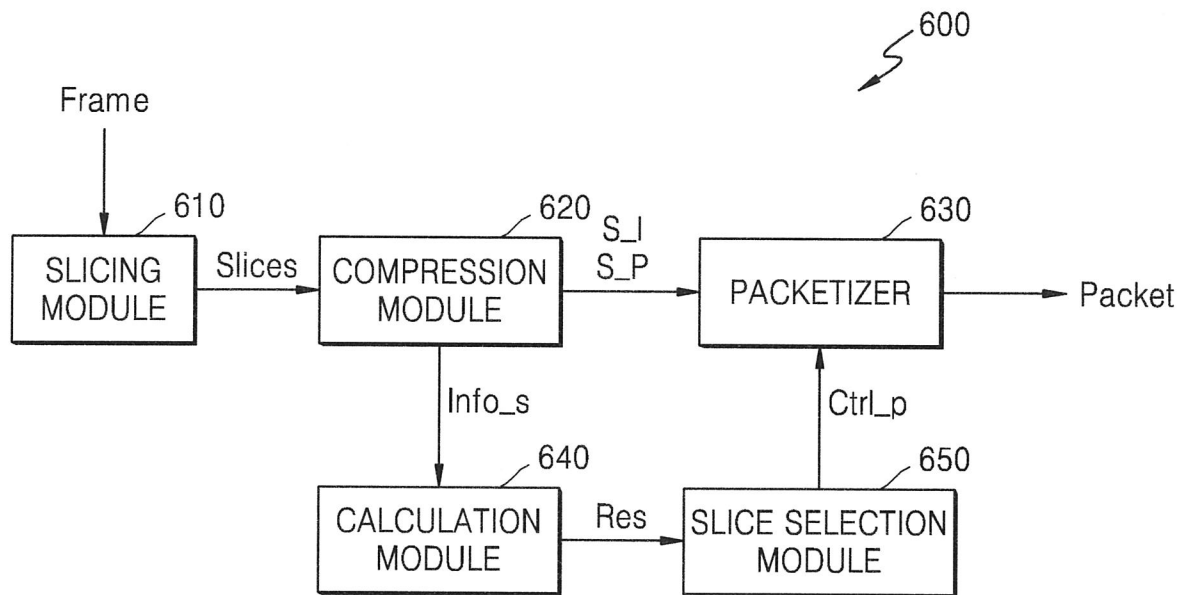


FIG. 18

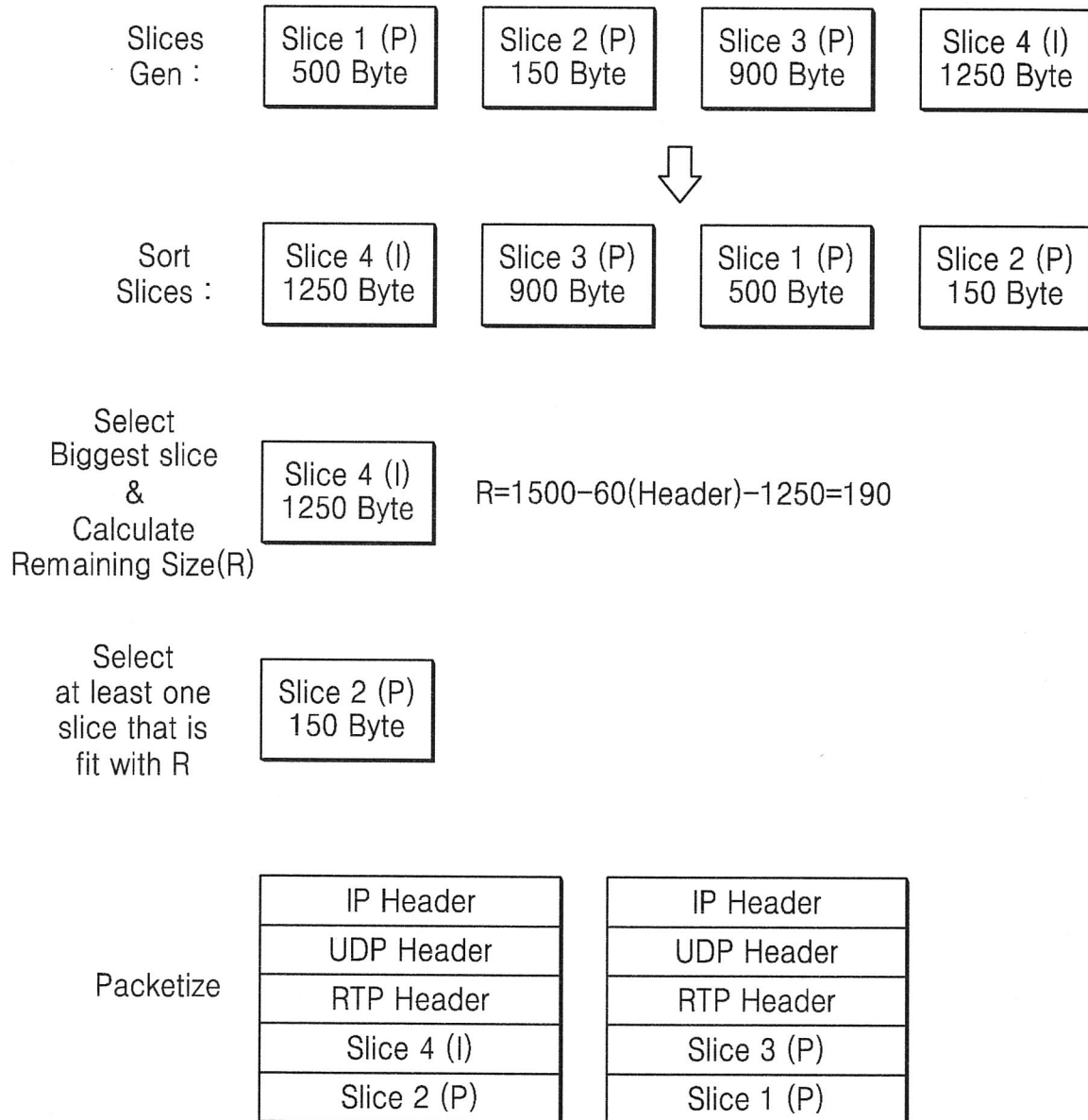


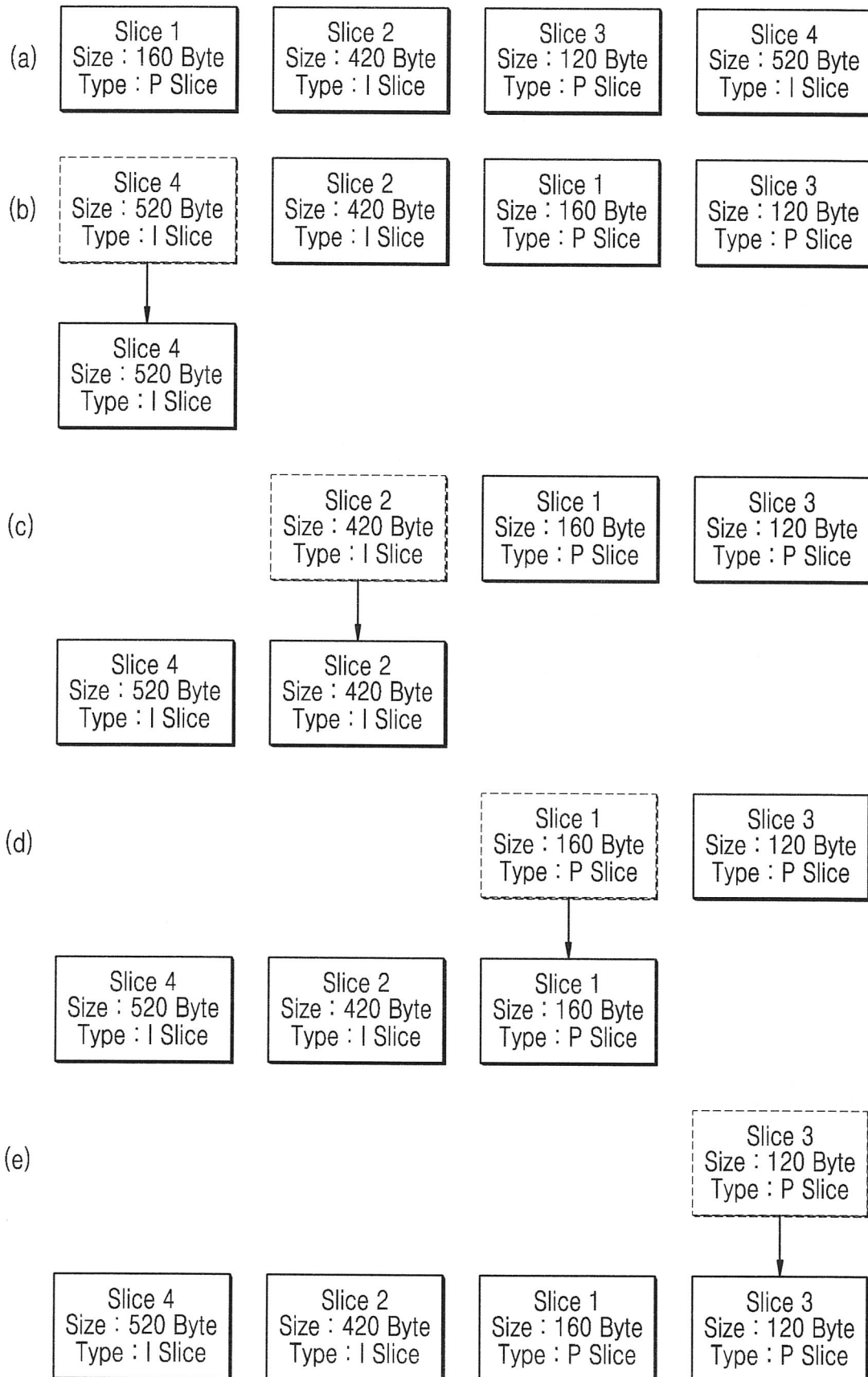
FIG. 19A

FIG. 19B

IP Header
UDP Header
RTP Header
Slice 4 (I)
Slice 2 (I)
Slice 1 (P)
Slice 3 (P)

FIG. 20A

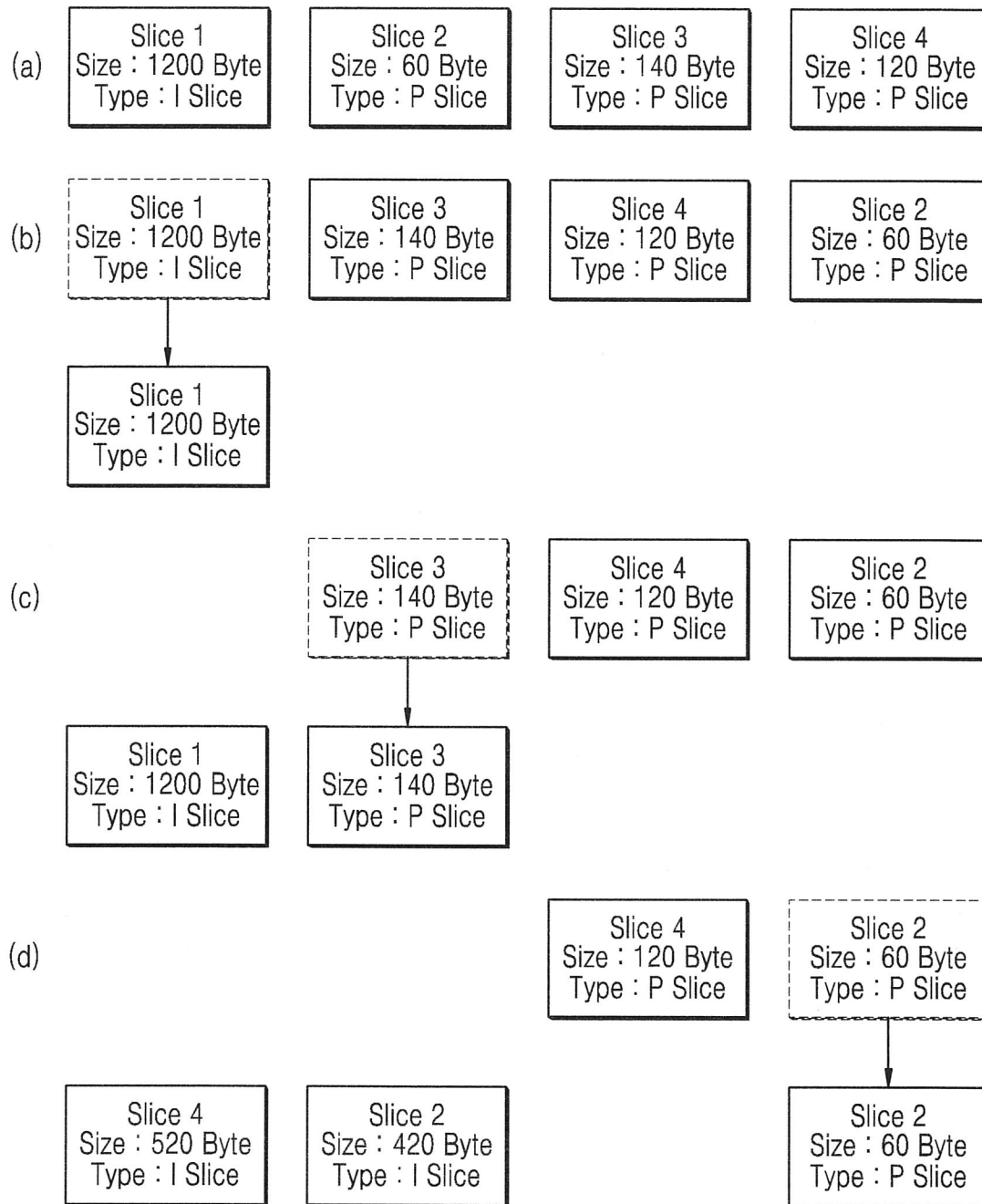


FIG. 20B

IP Header
UDP Header
RTP Header
Slice 1 (I)
Slice 3 (P)
Slice 2 (P)

IP Header
UDP Header
RTP Header
Slice 4 (I)

FIG. 21

