What is claimed is:

1. A storage device comprising:

a descramble module configured to descramble at least a portion of a read command, which includes logical block address (LBA) information and first meta information, into first signature information and first physical address (PA) information for accessing a memory,

wherein the first signature information is cached in a cache table;

a compare module configured to compare the first signature information against stored signature information to thereby determine an equivalency or discrepancy there between; and

an access module configured to use the first PA information to access a data region of the memory, upon determination of the equivalency by said compare module.

2. The storage device of claim 1, wherein said access module is further configured to load second PA information from the memory and access the data region using the second PA information, upon determination of the discrepancy by said compare module.

3. The storage device of claim 1 or claim 2, wherein the memory is a flash memory; wherein the stored signature information is second signature information; and wherein the flash memory includes a meta region which stores the second signature information.

4. The storage device of claim 3, wherein the meta region includes a first meta region and a second meta region; wherein the first meta region stores first meta region signature information on a first LBA set and the second meta region stores second meta region signature information on a second LBA set different from the first LBA set; and wherein the second signature information is equivalent to the first meta region signature information or the second meta region signature information in accordance with the value of the LBA information.

5. The storage device of claim 4, further comprising:

a garbage collection module (GC module) configured to change the value of the first meta region signature information or the second meta region signature information, when garbage collection is performed on at least a part of the data region.

6. The storage device of claim 5, wherein the flash memory further comprises a global signature information storage region which stores global signature information; wherein said garbage collection module is configured to reset the changed value of the first meta region signature information or the second meta region signature information, when: (i) the value of the first meta region signature information or the second meta region signature information is changed and an overflow occurs, and (ii) the garbage collection module changes the value of the global signature information to another value.

7. The storage device of claim 6, wherein said descramble module descrambles the first meta information, using the global signature information.

8. The storage device of claim 6 or claim 7, further comprising:

a scrambling module configured to scramble the second signature information and the second PA information associated with the second signature information using the global signature to thereby generate second meta information.

9. The storage device of any one of claims 1 to 8, further comprising:

a WMC management module configured to manage the PA information and the signature information associated with the LBA information included in a write command, using a write map cache (WMC) table, when the write command is received from a host.

10. The storage device of claim 9, wherein said compare module is configured to compare the value of the first signature information with the value of the second signature information stored in the write map cache table to thereby determine whether the value of the first signature information and the value of the second signature information are equal to each other.

11. A storage device comprising:

a descramble module configured to receive a read command including logical block address (LBA) information and first meta information from a host, and further configured to descramble the first meta information to acquire first signature information and first physical address (PA) information for accessing a flash memory,

wherein the first signature information is cached in a cache table; and

a compare module configured to compare a value of the first signature information with a value of second signature information to determine the validity of the first PA information.

12. The storage device of claim 11, wherein the compare module determines that the value of the first PA information is valid when the value of the first signature information is equal to the value of the second signature information; and wherein the compare module determines that the value of the first PA information is invalid when the value of the first signature information is different from the value of the second signature information.

13. The storage device of claim 12, further comprising an access module configured to access a data region of the flash memory using the first PA information, when the first PA information is determined to be valid, and further configured to load second PA information from the flash memory and accesses the data region using the second PA information, when the first PA information is determined to be invalid.

14. The storage device of any of claims 11 to 13, wherein the flash memory includes a meta region which stores the second signature information, and wherein said compare module compares the value of the first signature information with the value of the second signature information stored in the meta region.

15. The storage device of claim 14, wherein the meta region includes a first meta region and a second meta region; wherein the first meta region stores first meta region signature information on a first LBA set; wherein the second meta region stores second meta region signature information on a second LBA set distinguished from the first LBA set; and wherein the second signature information is determined as the

first meta region signature information or the second meta region signature information in accordance with the value of the LBA information.

16. The storage device of any of claims 11 to 15, further comprising:

a WMC management module which manages the PA information and the signature information associated with the LBA information included in a write command, using a write map cache (WMC) table, when the write command is received from the host.

17. The storage device of claim 16, wherein the compare module is configured to compare the value of the first signature information with the value of the second signature information stored in the write map cache table.

18. A storage device comprising:

a descramble module configured to receive a read command including logical block address (LBA) information and first meta information from a host, and further configured to descramble the first meta information to acquire first signature information and first physical address (PA) information for accessing a flash memory,

wherein the first signature information is cached in a cache table; and

a compare module configured to compare a value of the first signature information with a value of second signature information stored in advance in a meta region of the flash memory to thereby determine validity of the first PA information.

19. The storage device of claim 18, wherein the meta region includes a first meta region and a second meta region; wherein the first meta region stores first meta region signature information on a first LBA set; wherein the second meta region stores second meta region signature information on a second LBA set distinguished from the first LBA set; and wherein the second signature information is determined as the first meta region signature information or the second meta region signature information in accordance with the value of the LBA information.

20. The storage device of claim 18 or claim 19, wherein said compare module determines that the value of the first PA information is valid when the value of the first

signature information is equal to the value of the second signature information, and said compare module determines that the value of the first PA information is invalid when the value of the first signature information is different from the value of the second signature information.