ABSTRACT

INTEGRATED CIRCUIT SYSTEM EMPLOYING A CONDENSATION PROCESS

An integrated circuit system that includes: providing a PFET device including a PFET gate and a PFET gate dielectric; forming a source/drain extension from a first epitaxial layer aligned to a first PFET gate sidewall spacer; and forming a source/drain from a second epitaxial layer aligned to a second PFET gate sidewall spacer.

Fig. 8