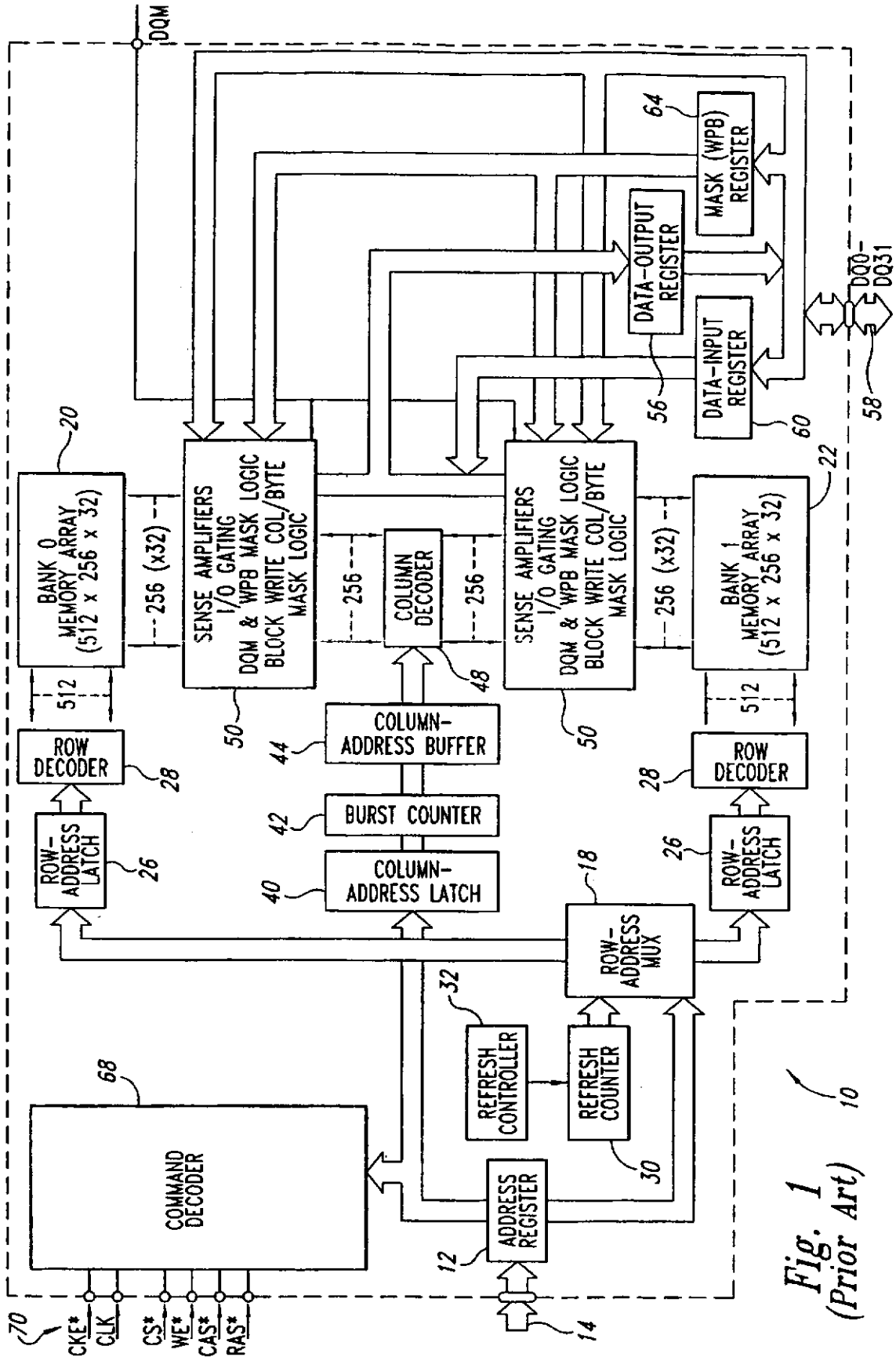


Fig. 1 (Prior Art)

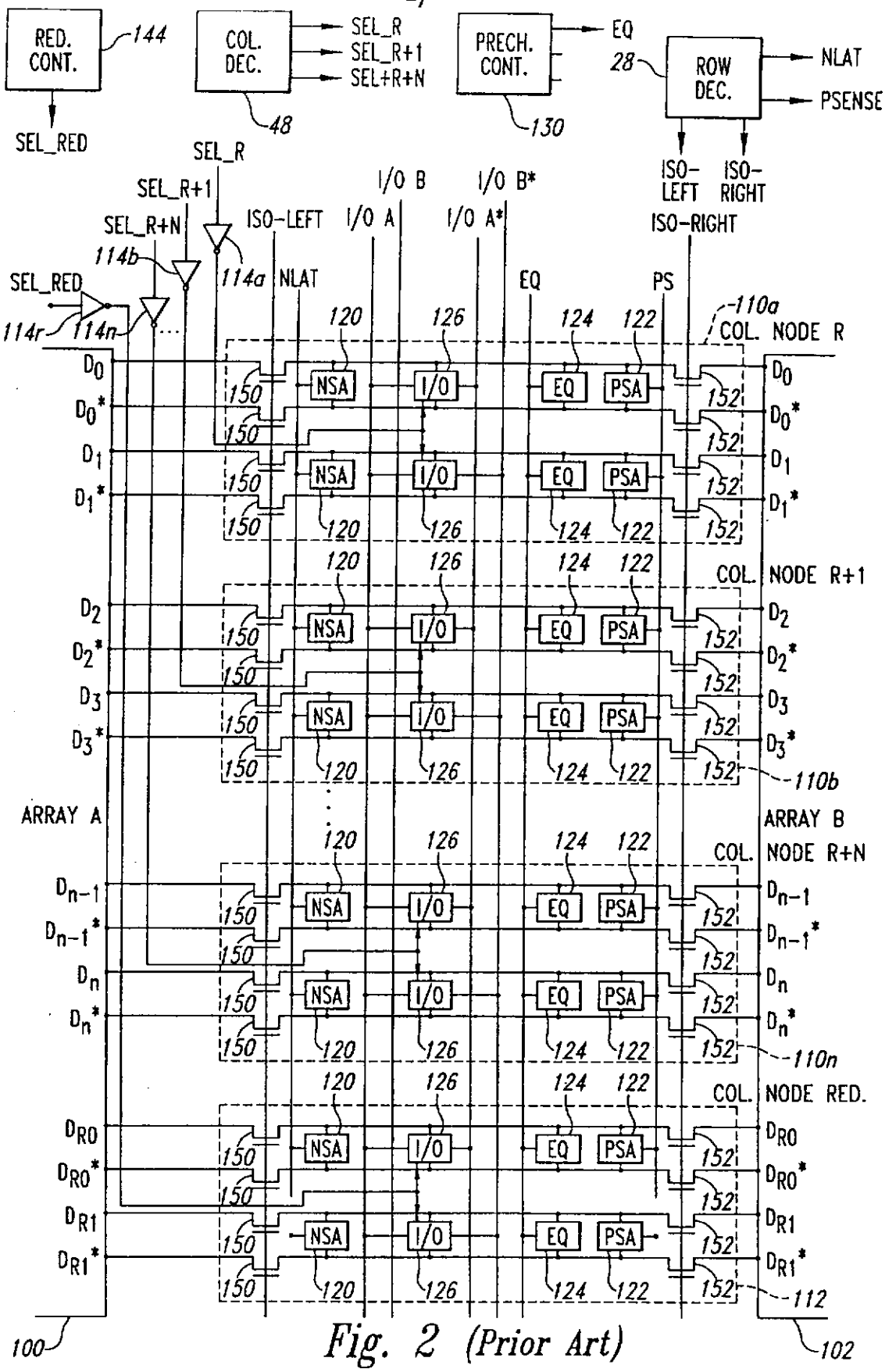


Fig. 2 (Prior Art)

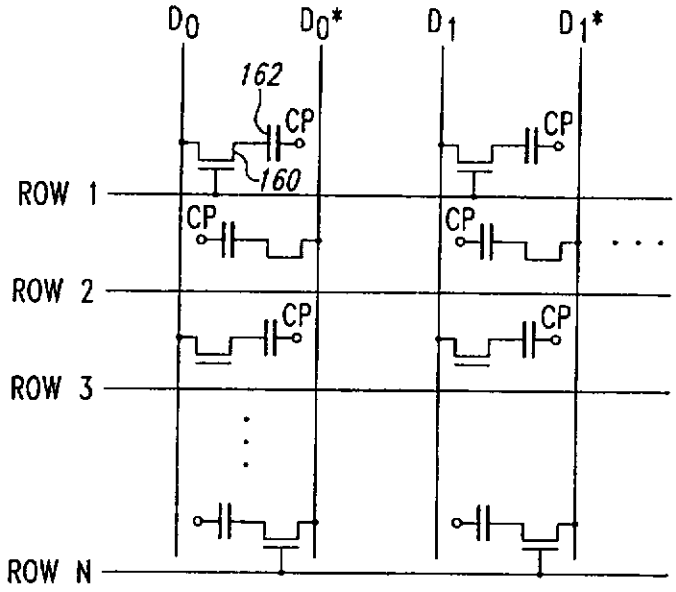
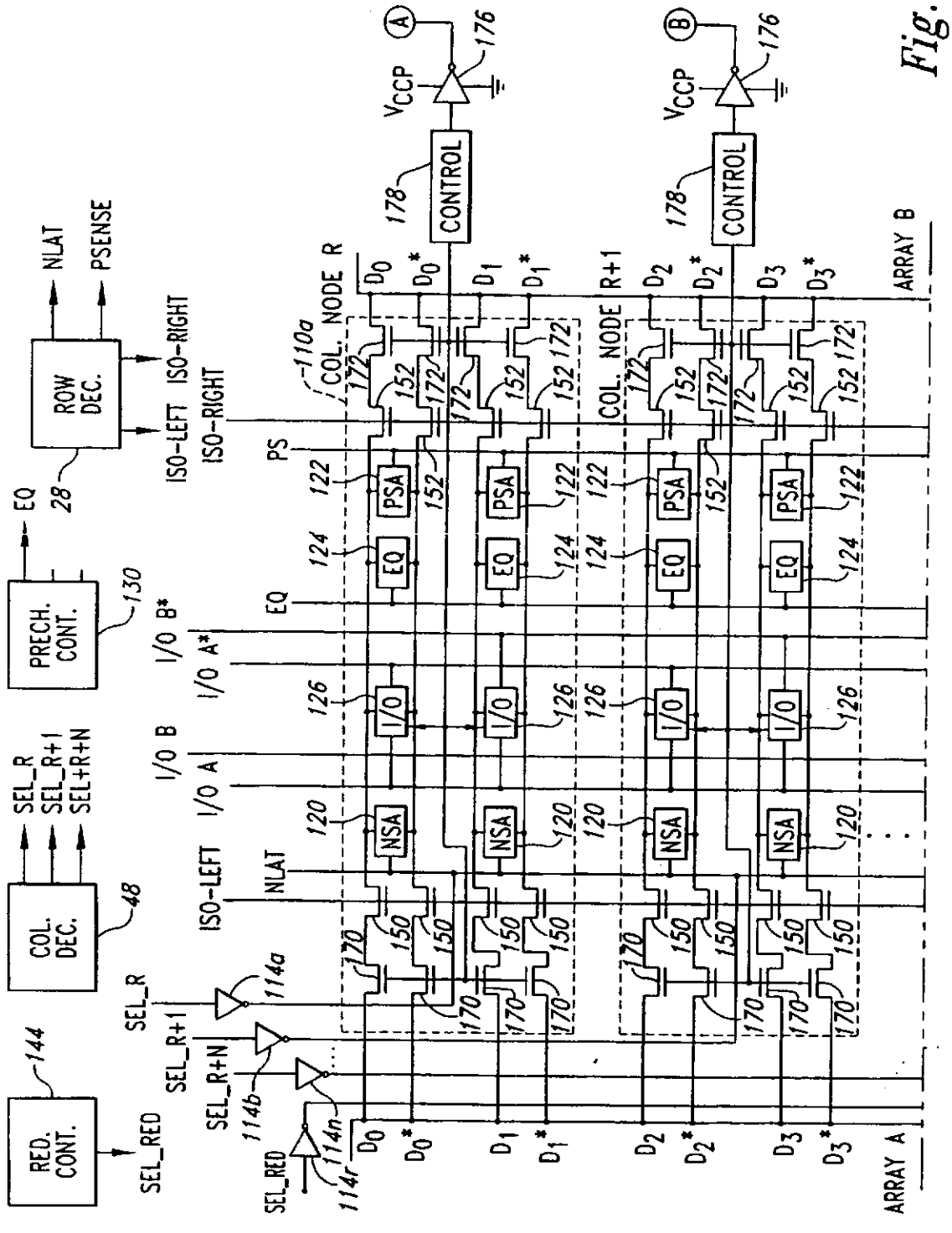


Fig. 3
(Prior Art)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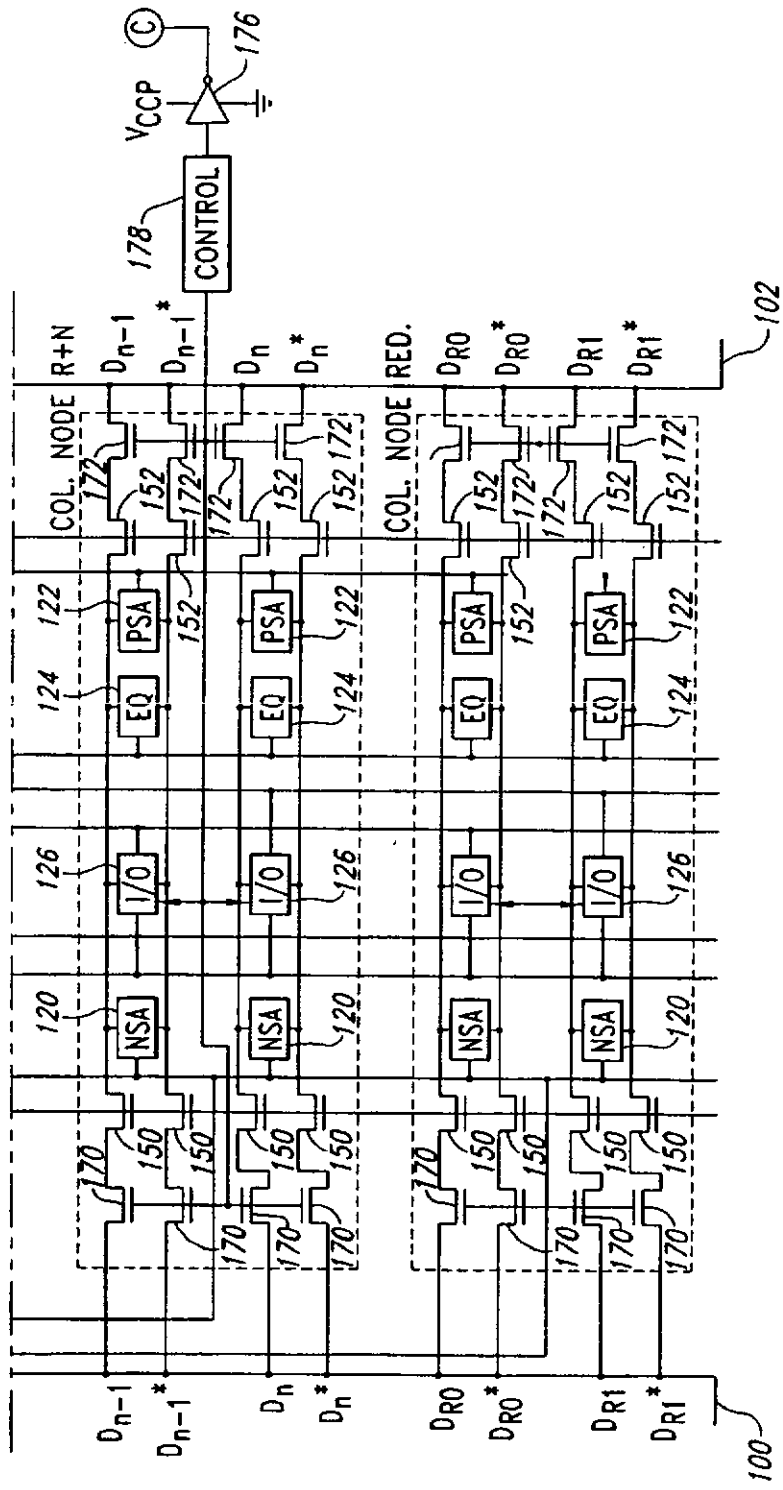
144
SEL_RED
COL. DEC.
48
SEL_R
SEL_R+1
SEL+R+N
PRECH. CONT.
130
EQ
ROW DEC.
28
NLAT
PSENSE
ISO-LEFT
ISO-RIGHT
SEL_R
SEL_R+1
SEL_R+N
SEL_RED
114a
114b
114n
114r
I/O B*
I/O A*
I/O B
I/O A
EQ
PS
NLAT
ISO-LEFT
ISO-RIGHT
COL. NODE R
COL. NODE R+1
110a
D_0
D_0^*
D_1
D_1^*
D_2
D_2^*
D_3
D_3^*
ARRAY A
ARRAY B
NSA
I/O
EQ
PSA
CONTROL
178
176
V_{CCP}
A
B
120
122
124
126
150
152
170
172

Fig. 4A


C
176
V_{CCP}
178
CONTROL
R+N
D_{n-1}
D_{n-1}^*
D_n
D_n^*
RED.
D_{R0}
D_{R0}^*
D_{R1}
D_{R1}^*
COL. NODE
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122
124
126
120
PSA
EQ
I/O
NSA
152
172
150
170
100

Fig. 4B

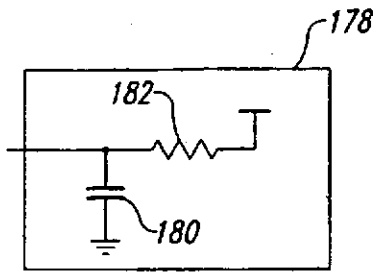

Fig. 5A

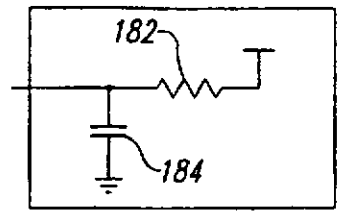

Fig. 5B

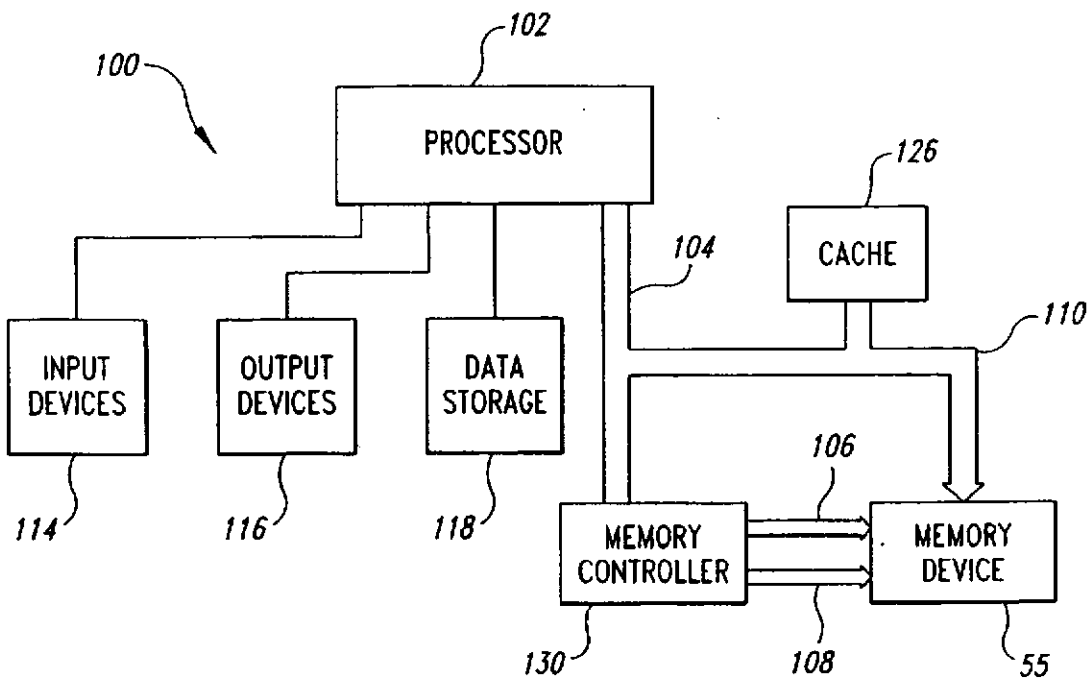

Fig. 6