

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
23 June 2005 (23.06.2005)

PCT

(10) International Publication Number
WO 2005/057338 A3

(51) International Patent Classification⁷: **H04L 12/66**

(21) International Application Number:
PCT/US2004/039943

(22) International Filing Date:
29 November 2004 (29.11.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
10/725,691 2 December 2003 (02.12.2003) US

(71) Applicant (for all designated States except US): **CISCO TECHNOLOGY, INC.** [US/US]; 170 W. Tasman Drive, San Jose, California 95134-1706 (US).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **MACKNIGHT, James** [US/US]; 14930 Sword Dancer Court, Morgan Hill, California 95037-6022 (US).

(74) Agents: **PALERMO, Christopher, J.** et al.; Hickman Palermo Truong & Becker LLP, Suite 550, 2055 Gateway Place, San Jose, CA 95110-1089 (US).

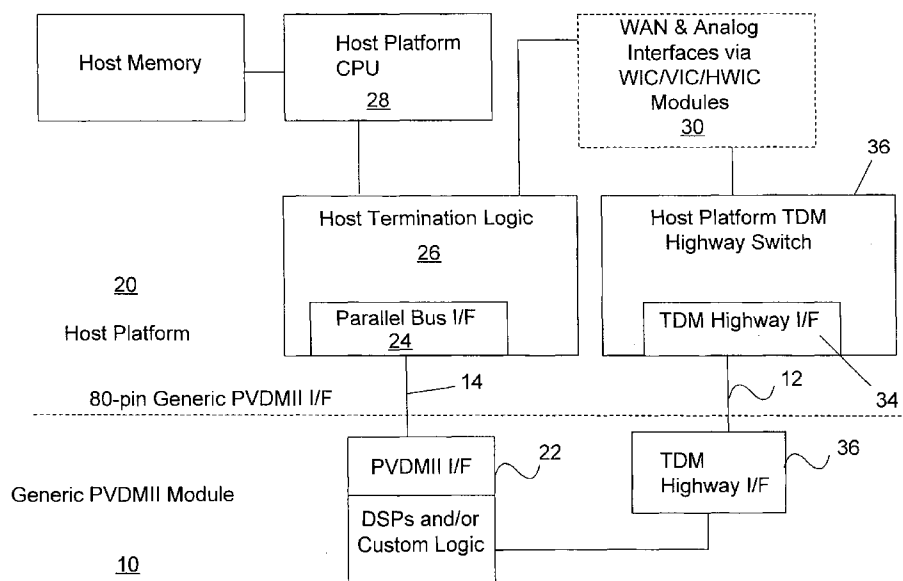
(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:
— with international search report

[Continued on next page]

(54) Title: METHOD AND APPARATUS TO COMBINE HETEROGENEOUS HARDWARE INTERFACES FOR NEXT GENERATION PACKET VOICE MODULE DEVICES



(57) Abstract: A Packet Voice Data Module (PVDM)/Host Platform Interface that supports multiple vendor's DSPs without sacrificing performance. The interface also provides a hardware chip select for selecting non-DSP functionality to expand the use of the interface. Testing and module identification functions are also enabled by the interface.

WO 2005/057338 A3



— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(88) Date of publication of the international search report:

24 November 2005