

SYSTEM AND METHOD FOR COMBINING ELECTRICAL SIGNALS

ABSTRACT

5 The present invention provides a system and method for combining a plurality of electrical signals. The system may include a first central processing unit (CPU) located within a housing, the first CPU capable of transmitting and receiving a first signal on an A bus; a second CPU located within the housing, the second CPU capable of transmitting and receiving a second signal on an A bus; wherein each of
10 said first and second signals are combined on a single connector. The system may further include a first resistor network electrically connected between each of said A busses and said connector, such that said first resistor network assists in preventing an overload of one of said A busses when a signal is transmitted from the other of said A busses and output through said connector

15

Figure 2